

FIG. 1

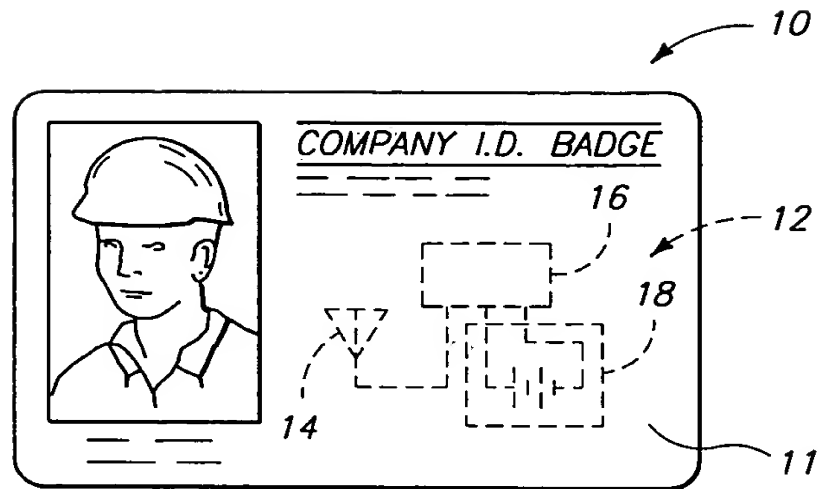
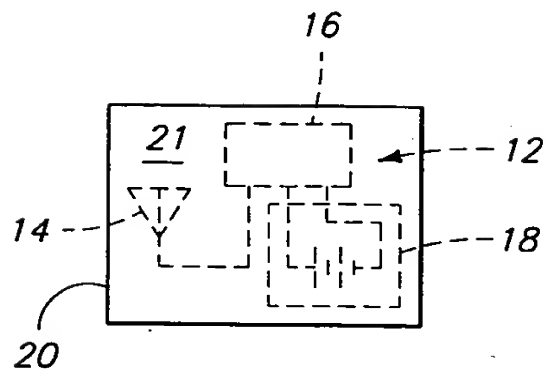
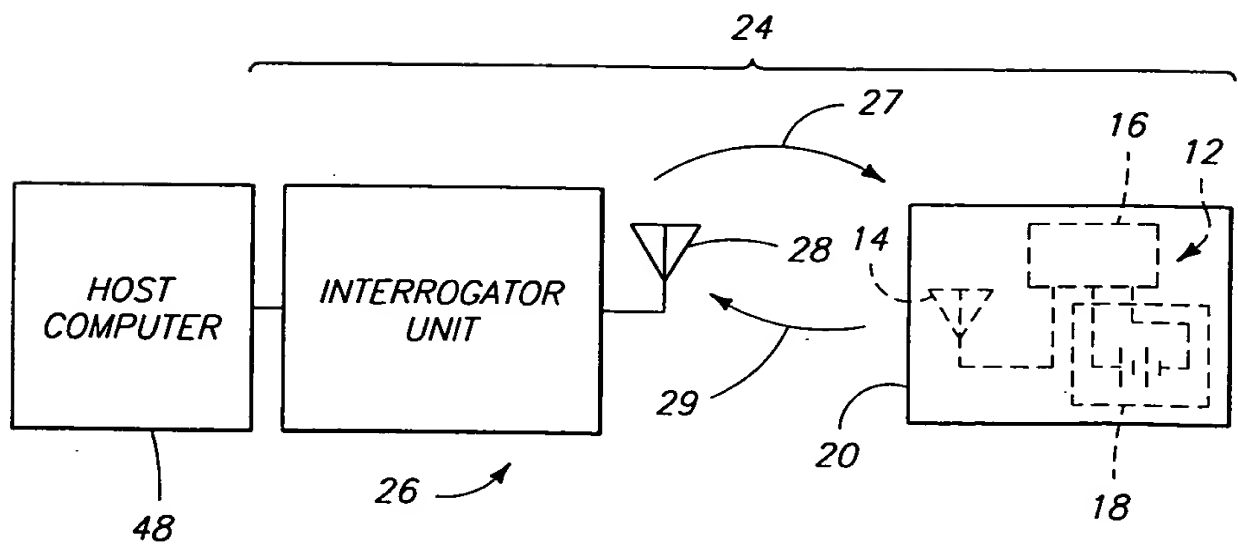


FIG. 2

2025 RELEASE UNDER E.O. 14176

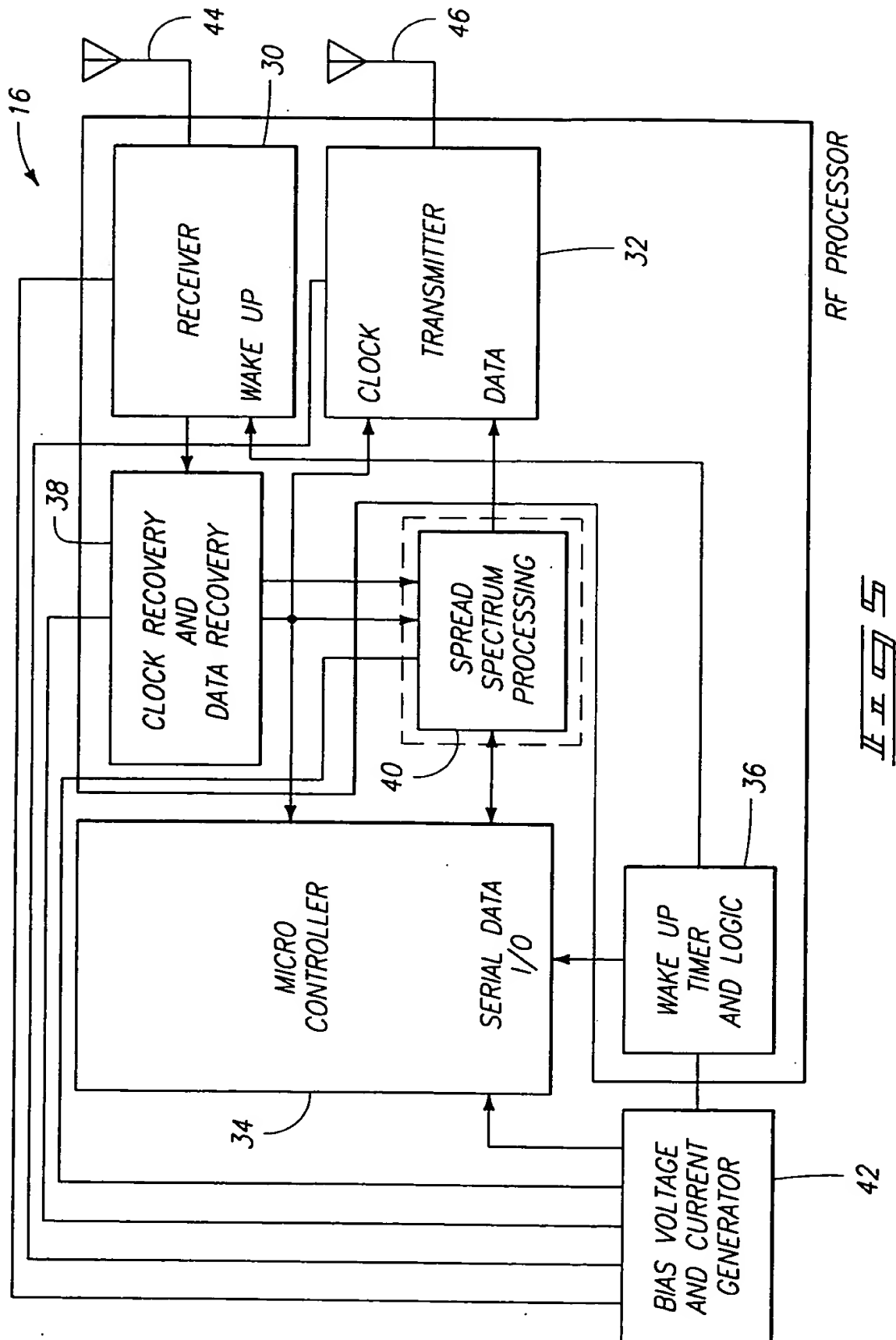


II 27 E



Итого

[illegible]



SECRET

007723 26920500

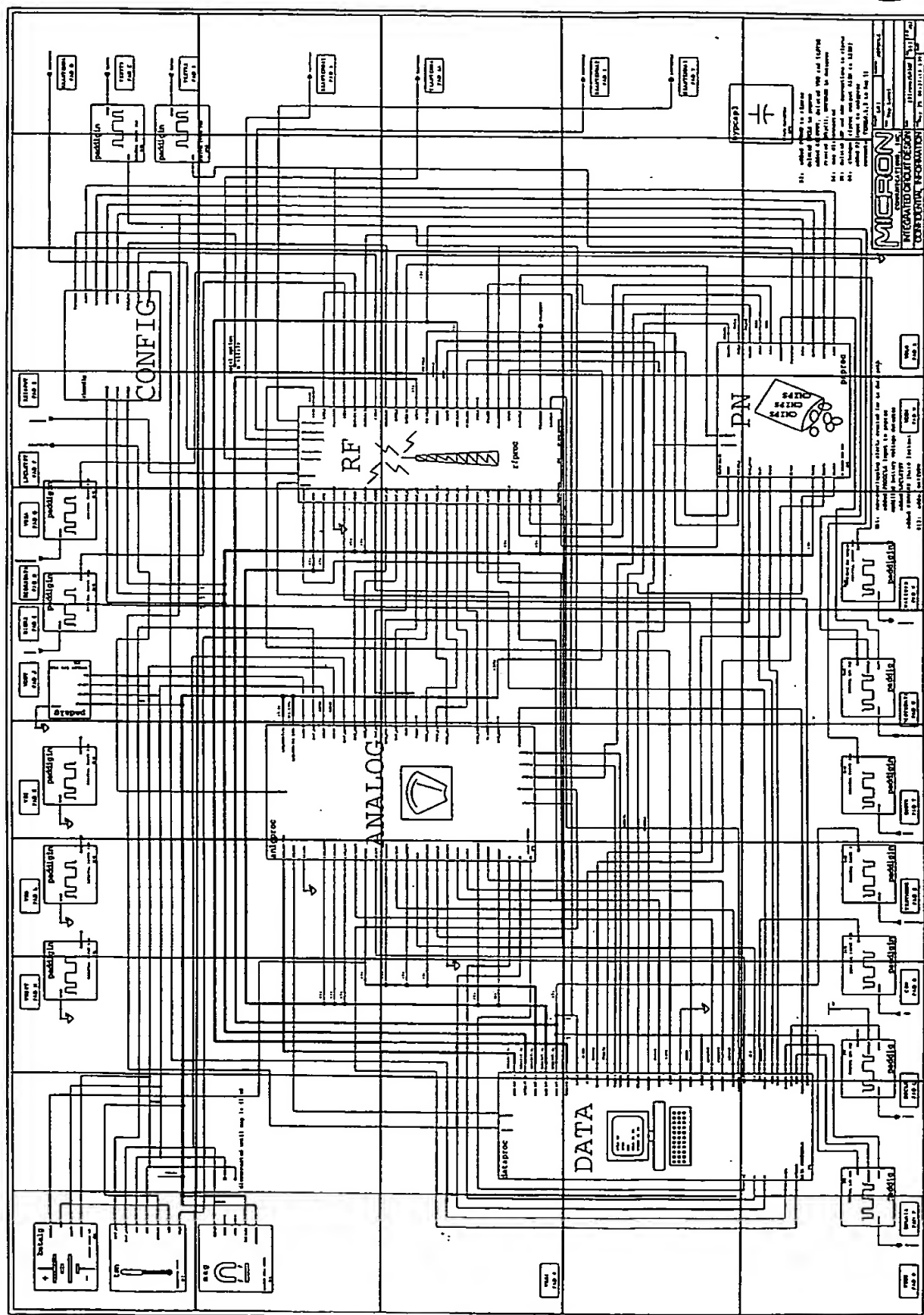
MI40-030

6AA	6AB	6AC	6AD	6AE	6AF	6AG	6AH	6AI	6AJ	6AK
6BA	6BB	6BC	6BD	6BE	6BF	6BG	6BH	6BI	6BJ	6BK
6CA	6CB	6CC	6CD	6CE	6CF	6CG	6CH	6CI	6CJ	6CK
6DA	6DB	6DC	6DD	6DE	6DF	6DG	6DH	6DI	6DJ	6DK
6EA	6EB	6EC	6ED	6EE	6EF	6EG	6EH	6EI	6EJ	6EK

IF II a7 ED



FIG. 6A-EK



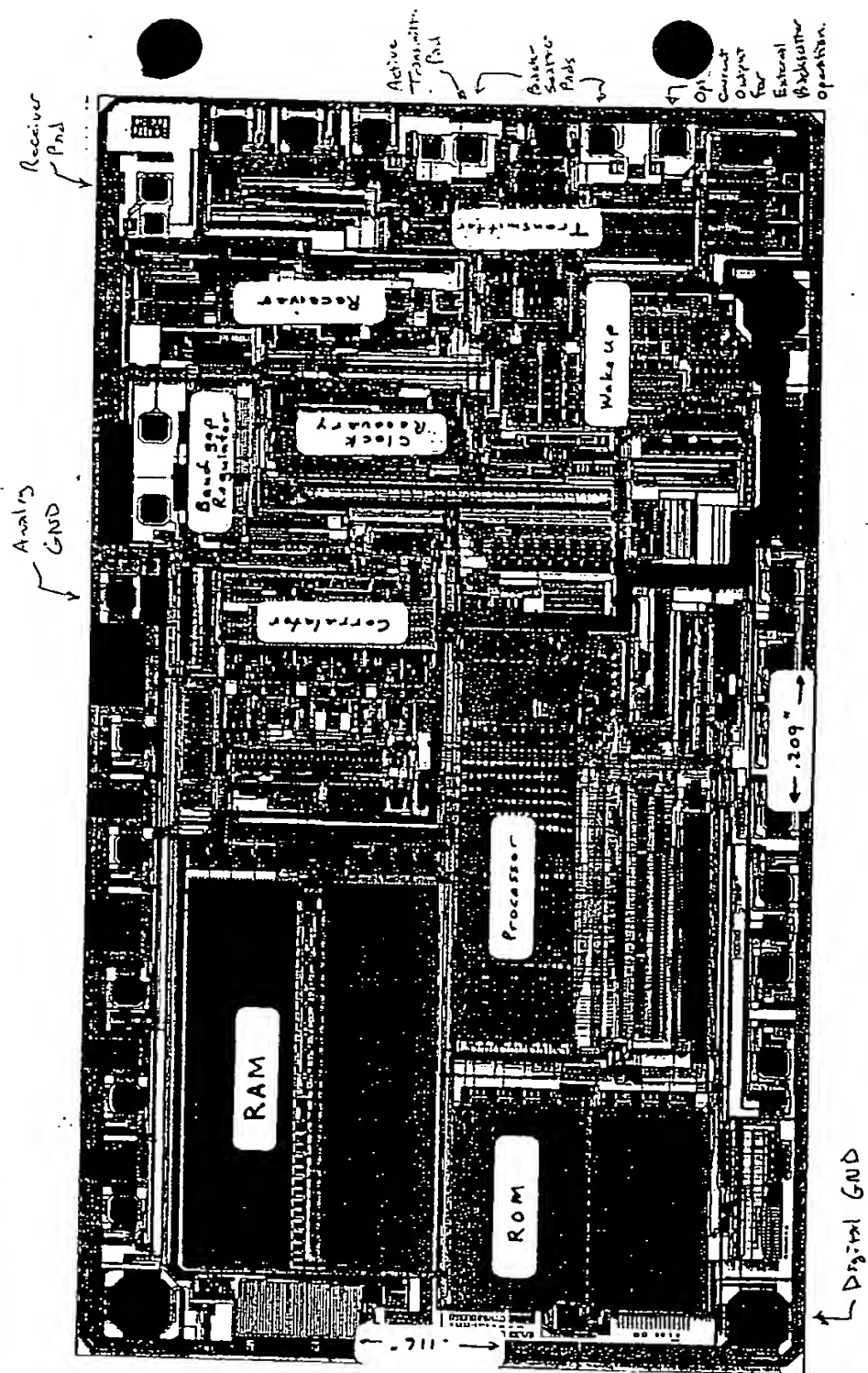


Fig. 6.01

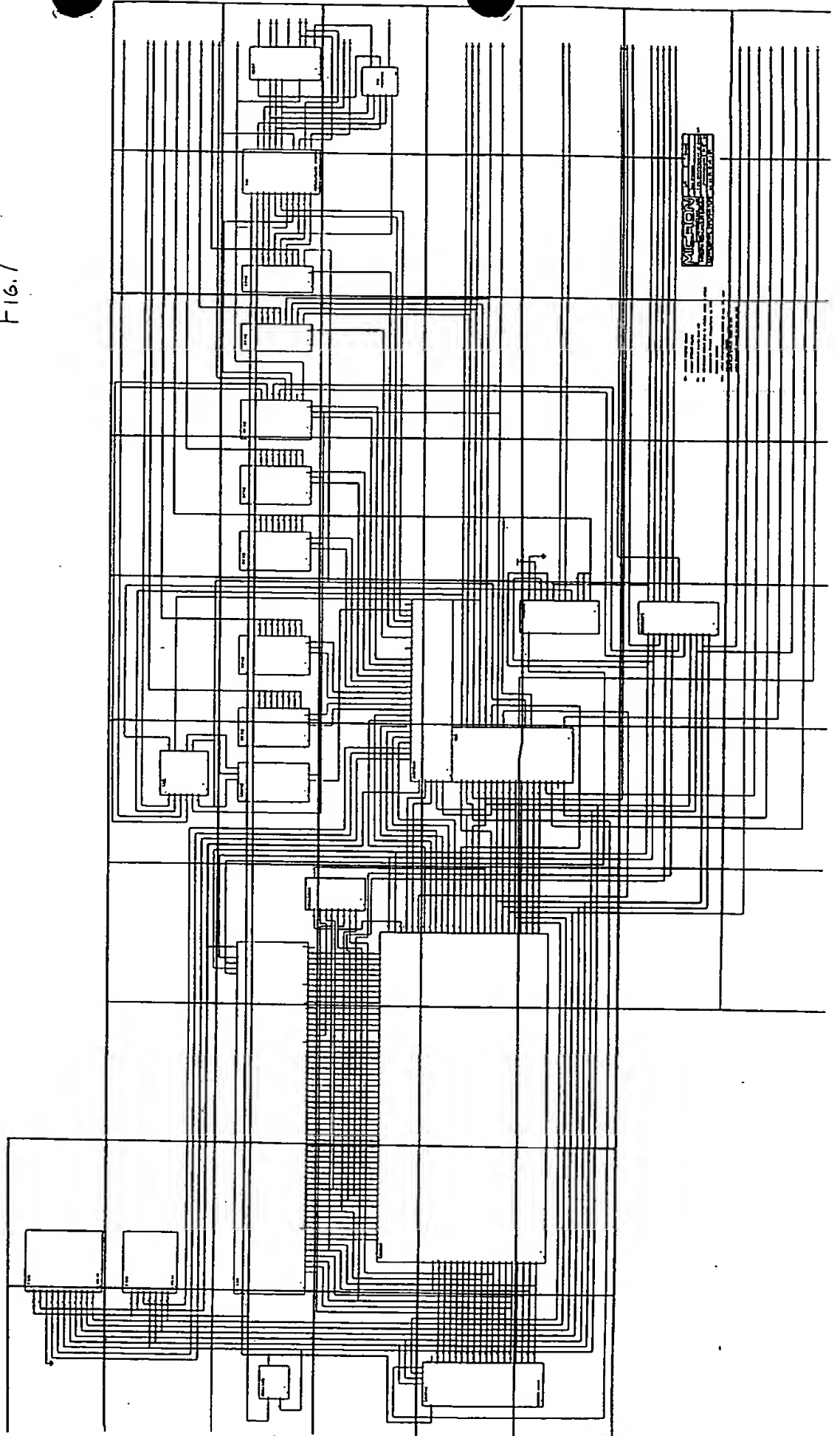
BEST AVAILABLE COPY

MI40-030

$$\frac{\pi}{\pi + \pi}$$

00120 20920500

FIG. 7



004420" 20920900

MI40-030

7.01AA	7.01AB
7.01BA	7.01BB

II II II 11.11.11

Fig. 7.01

82: created ENUPH1 signal  
 added ENUPH2 logic  
 B11: added hard lockout to clkst

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

PROJECT: L03  
 DATE: 10/11/96  
 REVISION: 10/11/96  
 PROCESSOR Clock Generator  
 2-Phase/4-State/8-Cycle  
 103revs/clk  
 B11  
 10/11/96  
 Apr 13 09:58:52 1996

Fig. 7.01

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	REV: 00000001
TITLE: Processor Clock Generator	
2-Phase/4-State/8-Cycle	
DATE:	103revs/c/hk
DATE:	Apr 13 09:59:32 1996
DATE:	011
DATE:	0000

7.0101AA	7.0101AB
7.0101BA	7.0101BB

И. П. И. И. П. И.

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

BB2: created ENIPLIN signal



Ex 11.11

# ZOOBIS

88: Added 6 inverters to non-overlap time

Make the number of inverters adjustable on metal

MICRON		PART NO. L03		REVISED		POZZOLI	
PROCESSOR Phase Generator							
2-Phase Non-overlapping							
PART NO.		101revs/clark		REV. BB		DATE	
SERIAL NO.		57-157-556		56		1985	
CONFIDENTIAL INFORMATION							

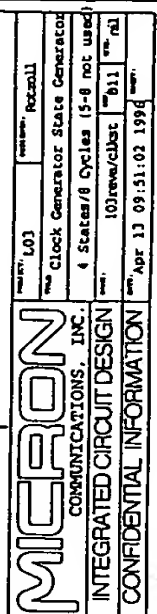
007420 20920500

MI40-030

7.0103AA	7.0103AB	7.0103AC	7.0103AD	7.0103BD
7.0103BA	7.0103BB	7.0103BC		

II 7.0103

FIG. 7.0103



00420" 00000000

MI40-030

7.010301AA	7.010301AB
7.010301BA	7.010301BB

II II II II II II II

MICROCON		PART NO. L03		REVISED		P030211	
COMMUNICATIONS, INC.		max		Clock Generator Counter Bit			
INTEGRATED CIRCUIT DESIGN		max		Sync/Asynch Resets			
CONFIDENTIAL INFORMATION		max		101revs/c/habit		rev. = max = n/d	
CONFIDENTIAL INFORMATION		max		1978 Sep. 28 16:14:06.131		max.	

7.02AA	7.02AB	7.02AC	7.02AD	7.02AE	7.02AF
7.02BA	7.02BB	7.02BC	7.02BD	7.02BE	7.02BF

EX-112

CONFIDENTIAL

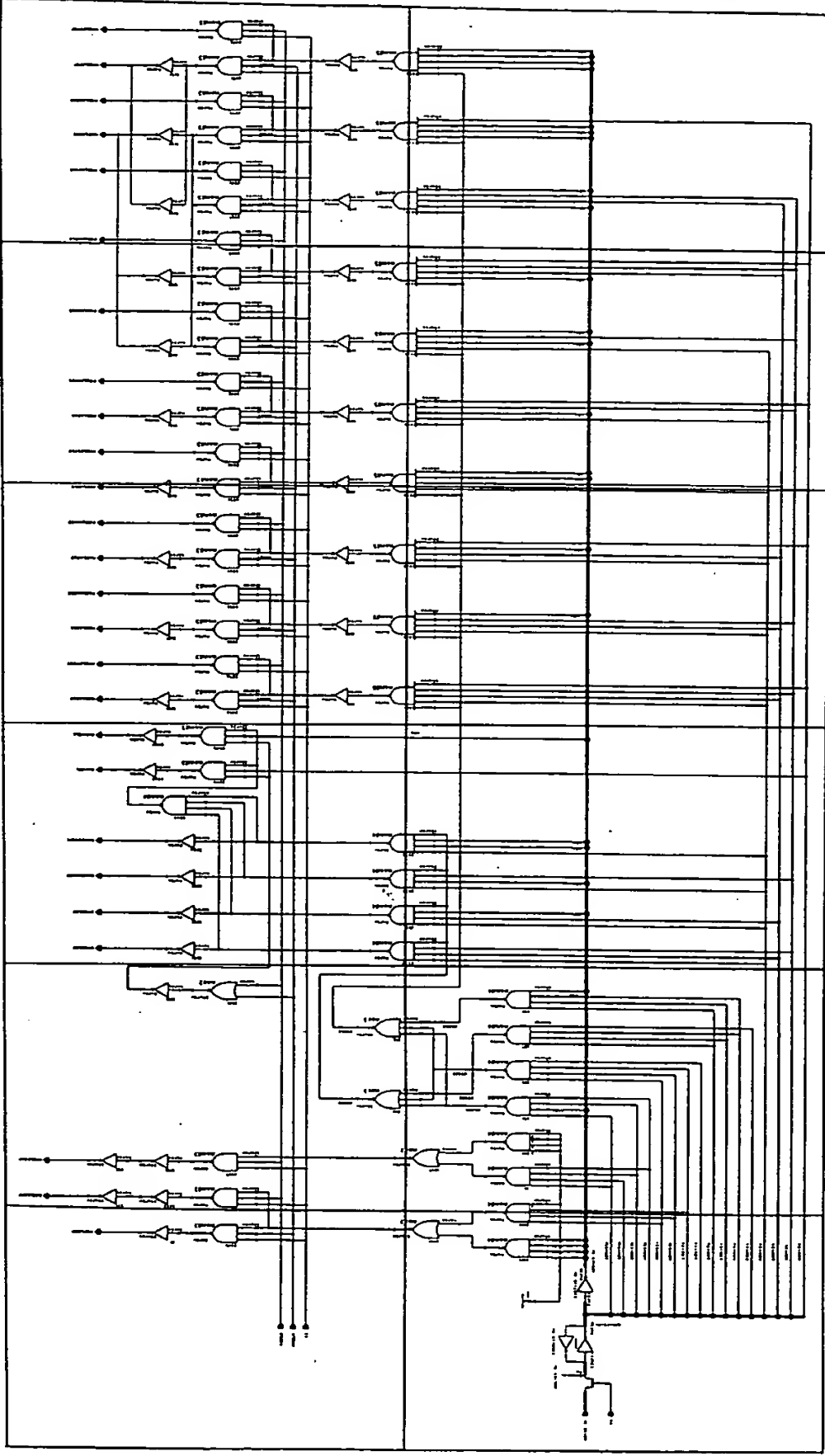


Fig. 7.02

<b>MICRON</b>	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DATE	REVISED
10/1/71	1
DESIGNED BY	10/1/71
10/1/71	10/1/71

NOT FOR RELEASE OR REPRODUCTION



Е.М.И. Е.И.И.

MEIJIN

7.0301AA	7.0301AB
7.0301BA	7.0301BB

THEO. L. L. L.

0000000000

RAMCTL

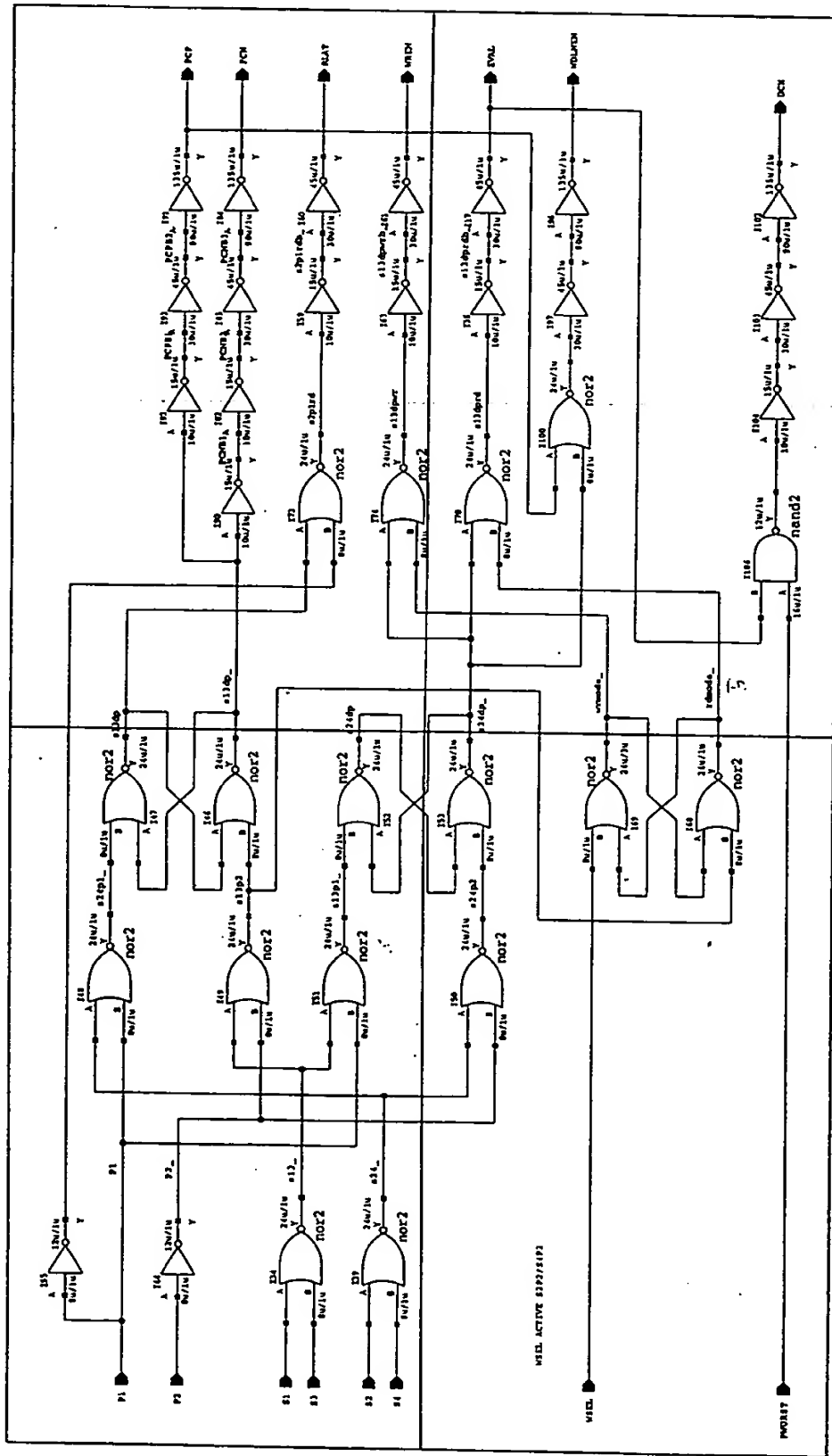


Fig. 7.0301

MICRON		REV. L03 A63	REV. L03 A63	REV. L03 A63
COMMUNICATIONS, INC.		RAM Control		
INTEGRATED CIRCUIT DESIGN		103revs/ramctl	REV. -	REV. -
CONFIDENTIAL INFORMATION		Feb 11 16:47:36 1994	DATE	DATE

004420" 00920500

MI40-030

7.0302AA	7.0302AB	7.0302AC
----------	----------	----------

IL 11 09 11.03.0002

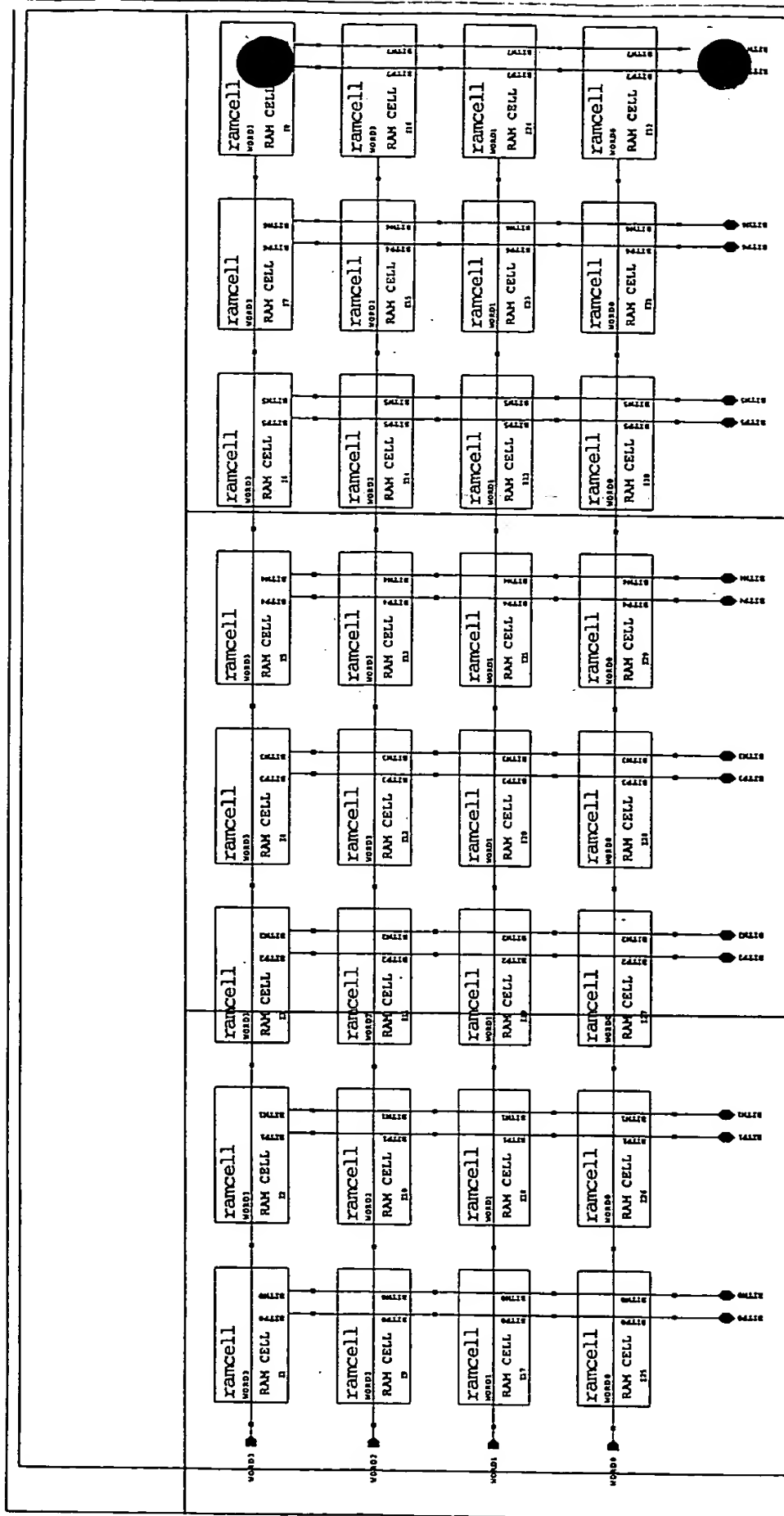
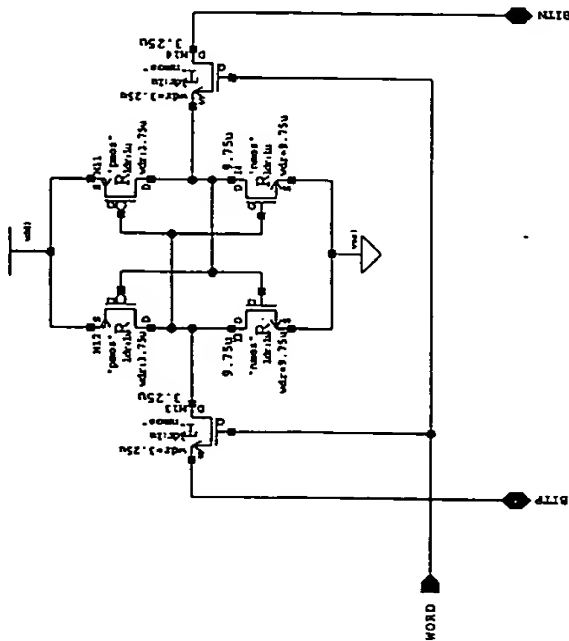


Fig. 7.0302

# 3i

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Fig. 7.030201



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: 6T RAM Cell			
<div> <div>micron</div> <div>COMMUNICATIONS, INC.</div> </div>			
NAME: 103reva/rancell		REV: -	SIZE: A
DATE: Nov 6 11:34:48 1993		SHEET: 1	

001120 20920500

MI40-030

7.0303AA	7.0303AB	7.0303AC	7.0303AD
----------	----------	----------	----------

ITEM 11



001120 00000000

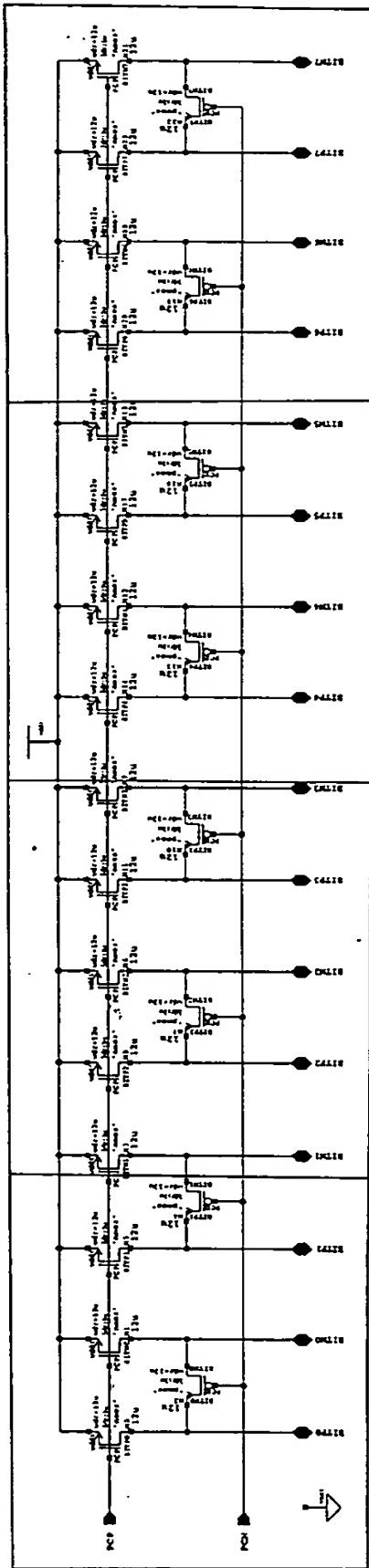


Fig. 7.0303

MICRON		PRODUCT: L03	DESIGNER: Ratzoll
COMMUNICATIONS, INC.		TYPE: RAM Precharge	
INTEGRATED CIRCUIT DESIGN		DATE: 103rova/rampch	REV: 1
CONFIDENTIAL INFORMATION		DATE: Nov 12 02:58:36 1993	FILE: rdl

00000000000000000000

MI40-030

7.0304AA	7.0304AB	7.0304AC	7.0304AD
----------	----------	----------	----------

7.0304

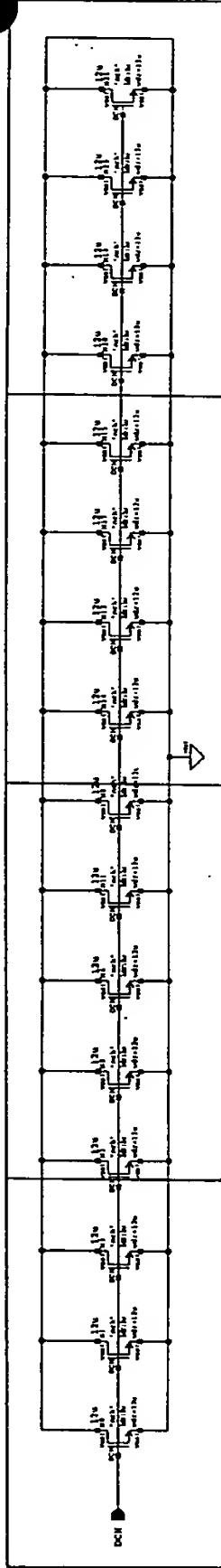
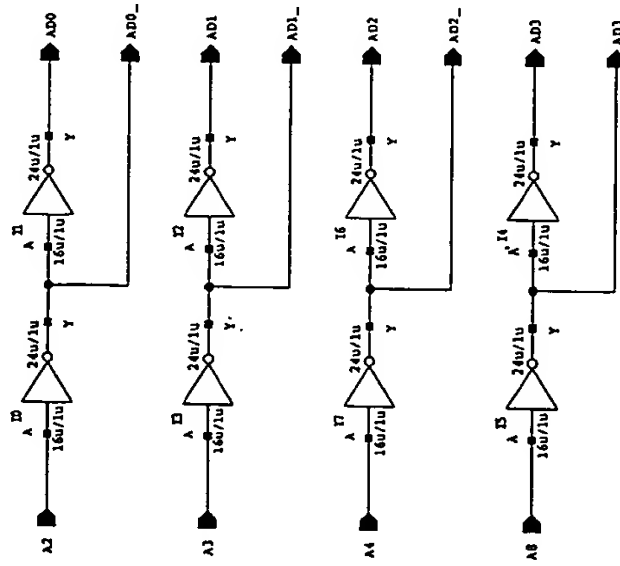


FIG. 7.0304

MICRON COMMUNICATIONS, INC.	INSTR. 103	REVISION: J0000LE
	NAME BAH Precharge	
	DATE: 10/3/95	REV: B8
INTEGRATED CIRCUIT DESIGN	10/3/95	REV: B8
CONFIDENTIAL INFORMATION	DATE: Jan 28 09:51:27 1996	REV: B8

B8: disconnected dch devices from bit lines and tied to vss

FIG. 7.0305



# NICRON COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNER: Rotzoll
TITLE: RAM Address Buffer	
NAME: 103reva/ramadb	REV: -
ITEM: A	
DATE: Sep 29 16:04:01 1993	SHEET:

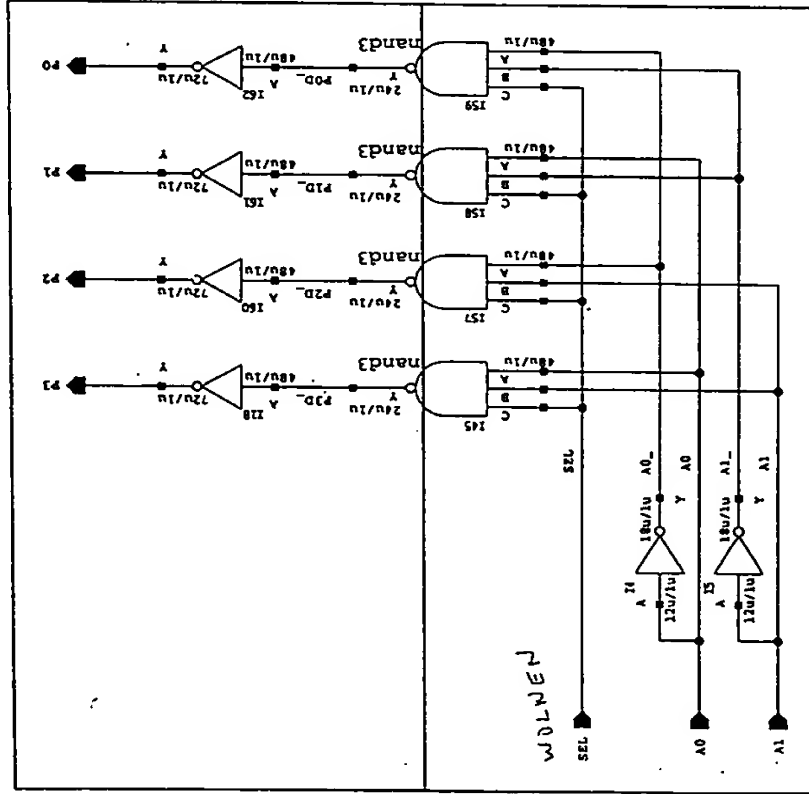
7.0306AA

7.0306BA

[illegible]

001400 00000000

Fig. 7.0306

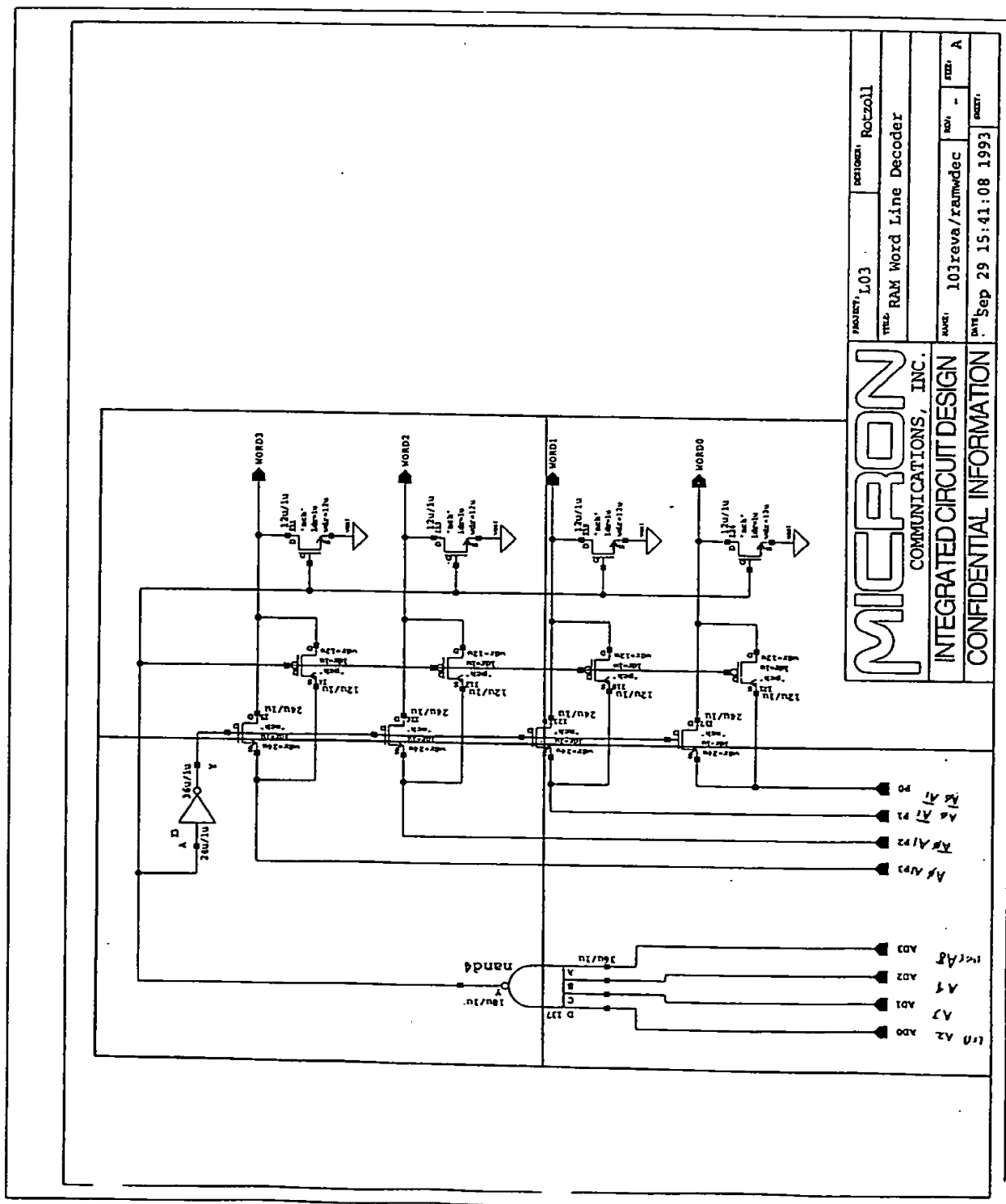


MICRON				PROJECT: L03		DESIGNER: Rotzoll			
COMMUNICATIONS, INC.				TITLE: RAM Word Line Driver					
INTEGRATED CIRCUIT DESIGN				NUMBER:	103revA/ramwdr		REV: -	SHEET: A	
CONFIDENTIAL INFORMATION				DATE: Sep 29 16:04:16 1993					SHEET:

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Итого 7.03007

FIG. 7.0307





7.0308BB

И И Г 7.00308

[illegible]

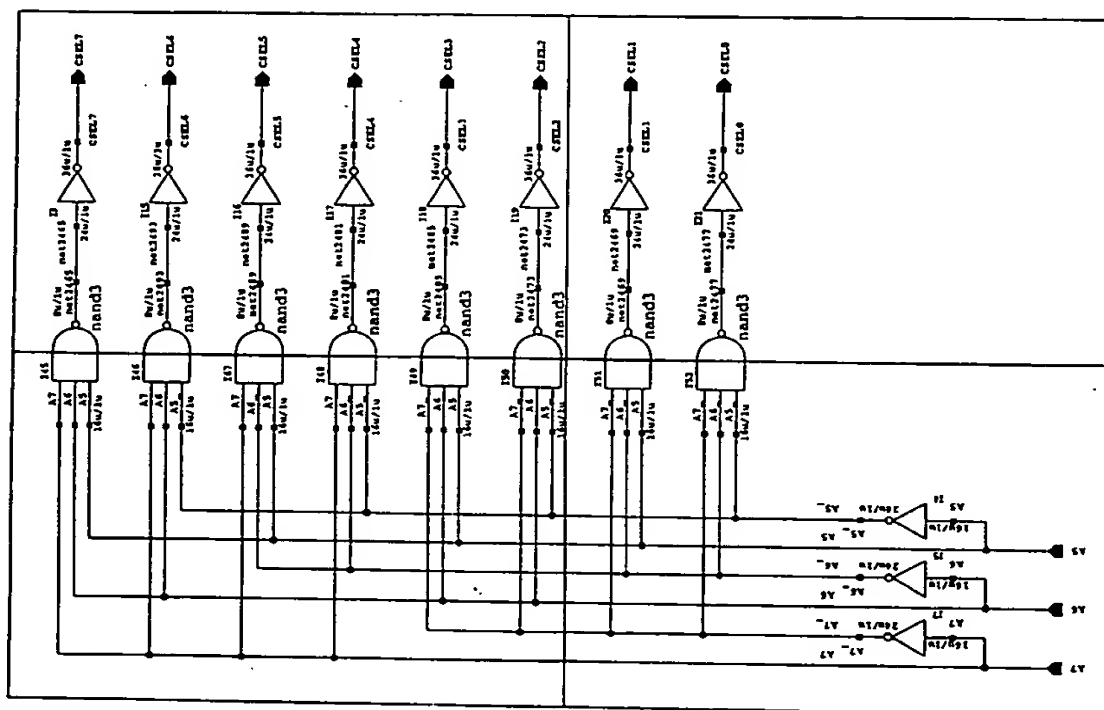


Fig. 7.0308

MICRON		RAM	RAM Column Select Decode	REV.	DATE
COMMUNICATIONS, INC.		3 to 8	103revA/ramcdec	REV.	DATE
INTEGRATED CIRCUIT DESIGN		Nov 5 17:21:07 1993			
CONFIDENTIAL INFORMATION					

007700 26920500

MI40-030

7.0309AA	7.0309AB	7.0309AC	7.0309AD	7.0309AE	7.0309AF	7.0309AG
7.0309BA	7.0309BB	7.0309BC	7.0309BD	7.0309BE	7.0309BF	7.0309BG

II II 7.0309

004400 20920500

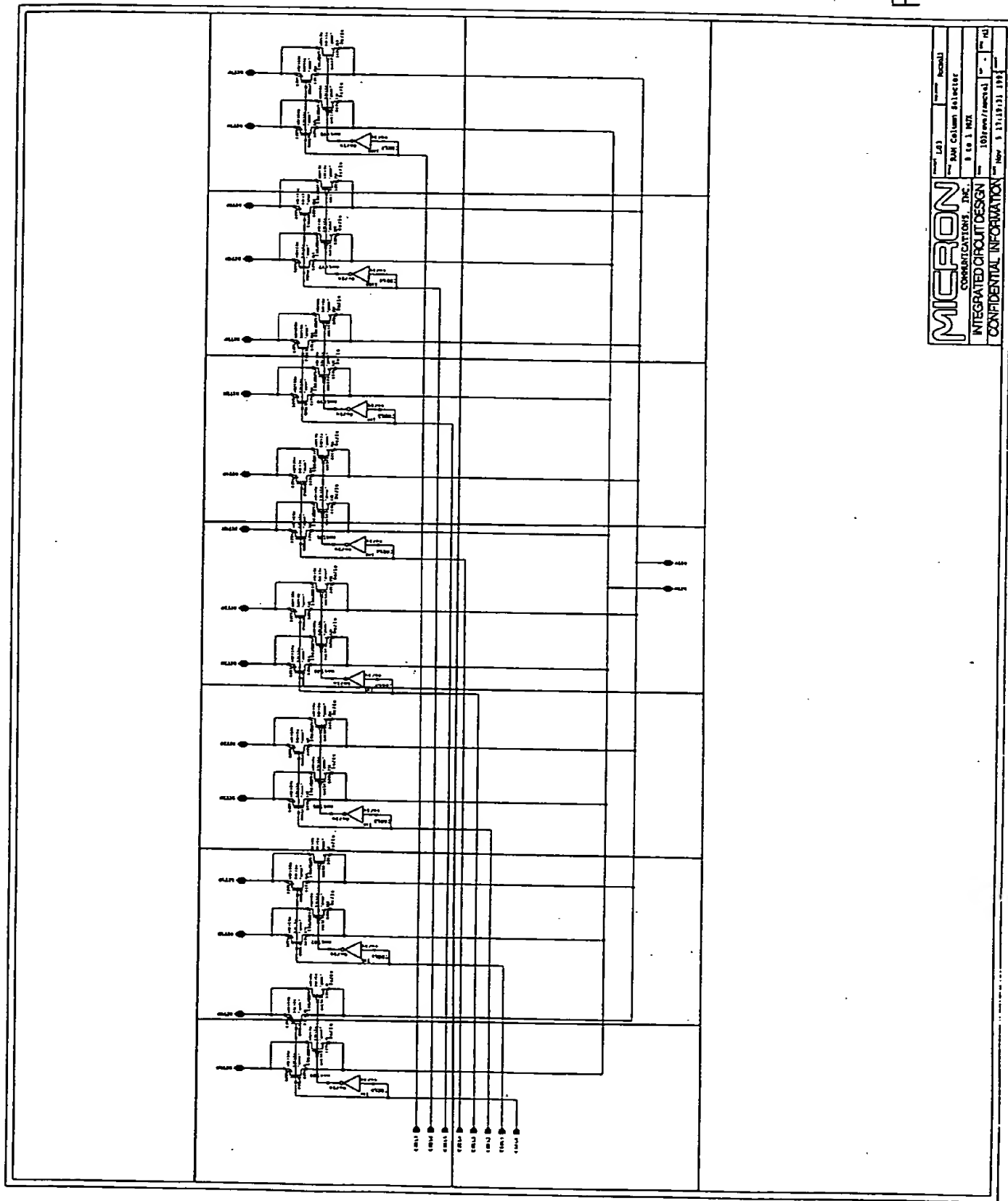


Fig. 7.0309

<b>MICRON</b>		Part No.	Rev.
CONVOLUTION, INC.		RAM Column Selector	1
INTEGRATED CIRCUIT DESIGN		8 to 1 MUX	1
CONFIDENTIAL INFORMATION		101mm/100mil	1
		Nov. 5, 1973	1

007420 20920500

MI40-030

7.0310AA	7.0310AB
7.0310BA	7.0310BB

II II III III III III III III

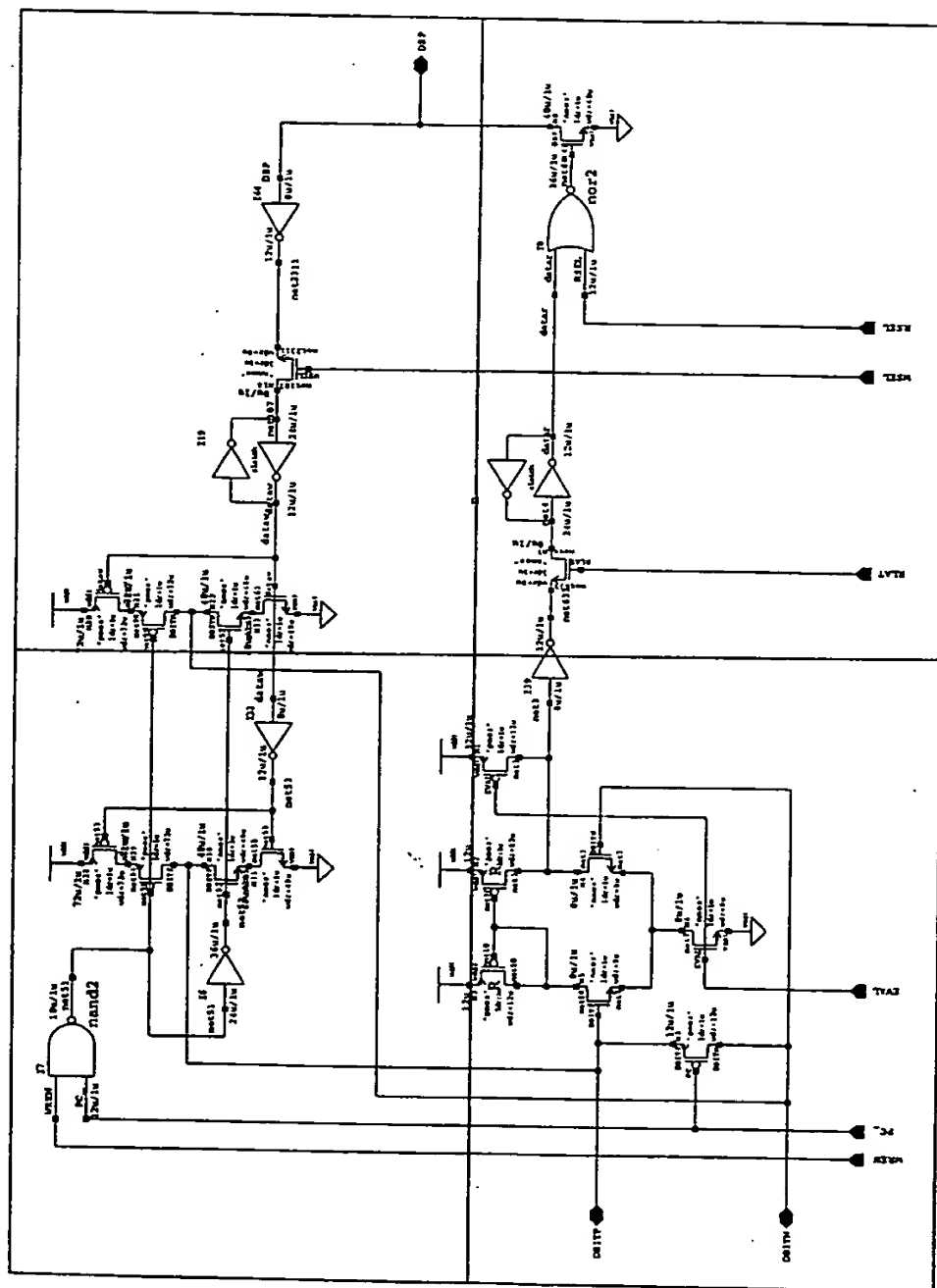


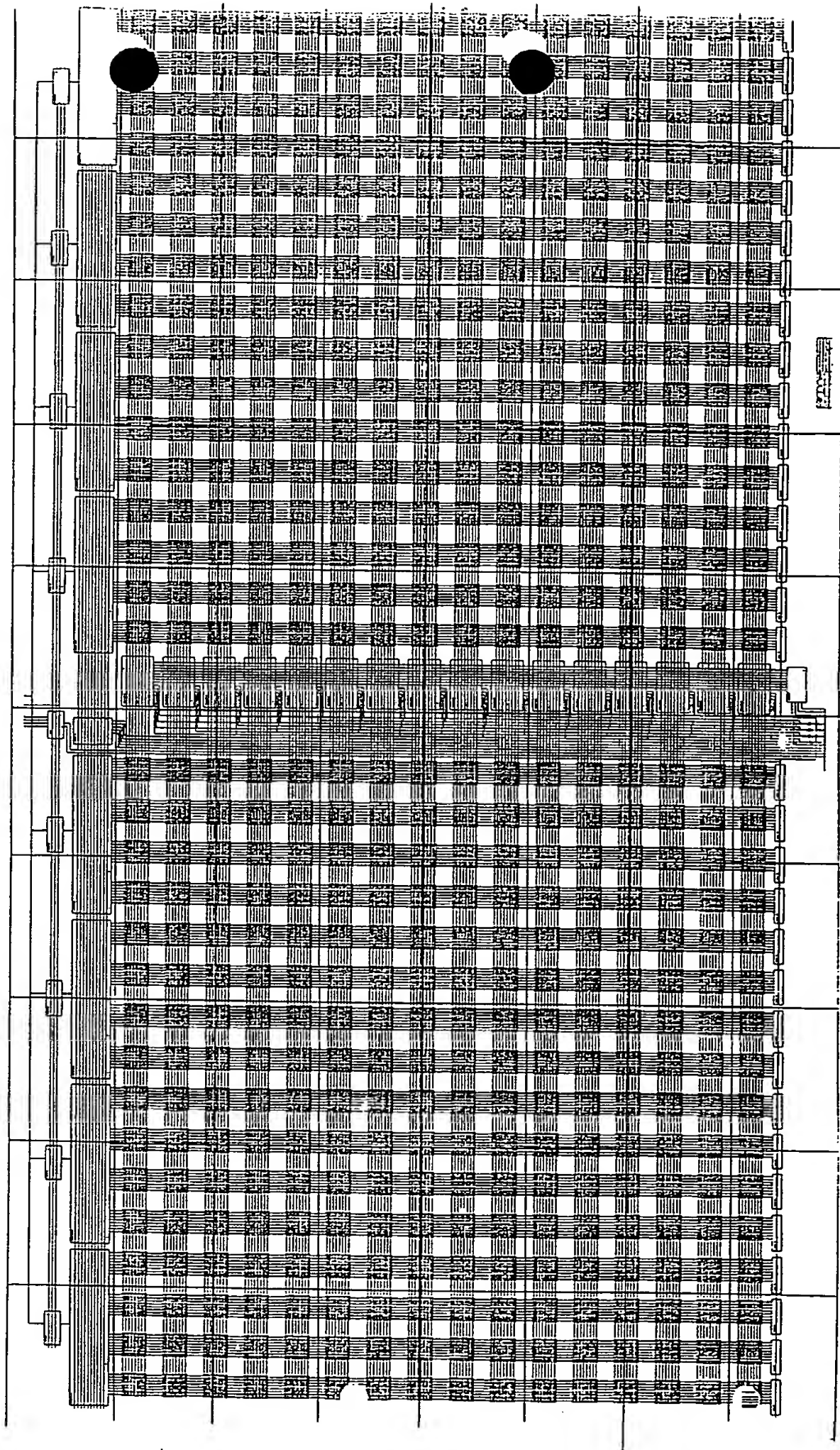
FIG. 7.0310

MICRON		REV. 1.03	REV. 1.03	REV. 1.03
COMMUNICATIONS, INC.		RAM Databus Interface		
INTEGRATED CIRCUIT DESIGN		103revs/ramdb		
CONFIDENTIAL INFORMATION		REV. Oct 6 12:08:33 1993		

7.04AA	7.04AB	7.04AC	7.04AD	7.04AE	7.04AF	7.04AG	7.04AH	7.04AI	7.04AJ
7.04BA	7.04BB	7.04BC	7.04BD	7.04BE	7.04BF	7.04BG	7.04BH	7.04BI	7.04BJ
7.04CA	7.04CB	7.04CC	7.04CD	7.04CE	7.04CF	7.04CG	7.04CH	7.04CI	7.04CJ
7.04DA	7.04DB	7.04DC	7.04DD	7.04DE	7.04DF	7.04DG	7.04DH	7.04DI	7.04DJ
7.04EA	7.04EB	7.04EC	7.04ED	7.04EE	7.04EF	7.04EG	7.04EH	7.04EI	7.04EJ
7.04FA	7.04FB	7.04FC	7.04FD	7.04FE	7.04FF	7.04FG	7.04FH	7.04FI	7.04FJ
7.04GA	7.04GB	7.04GC	7.04GD	7.04GE	7.04GF	7.04GG	7.04GH	7.04GI	7.04GJ
7.04HA	7.04HB	7.04HC	7.04HD	7.04HE	7.04HF	7.04HG	7.04HH	7.04HI	7.04HJ

00740 0030300

FIG. 7.04





DATA: 00000000

MI40-030

7.0401AA	7.0401AB
----------	----------

7.0401

CONFIDENTIAL

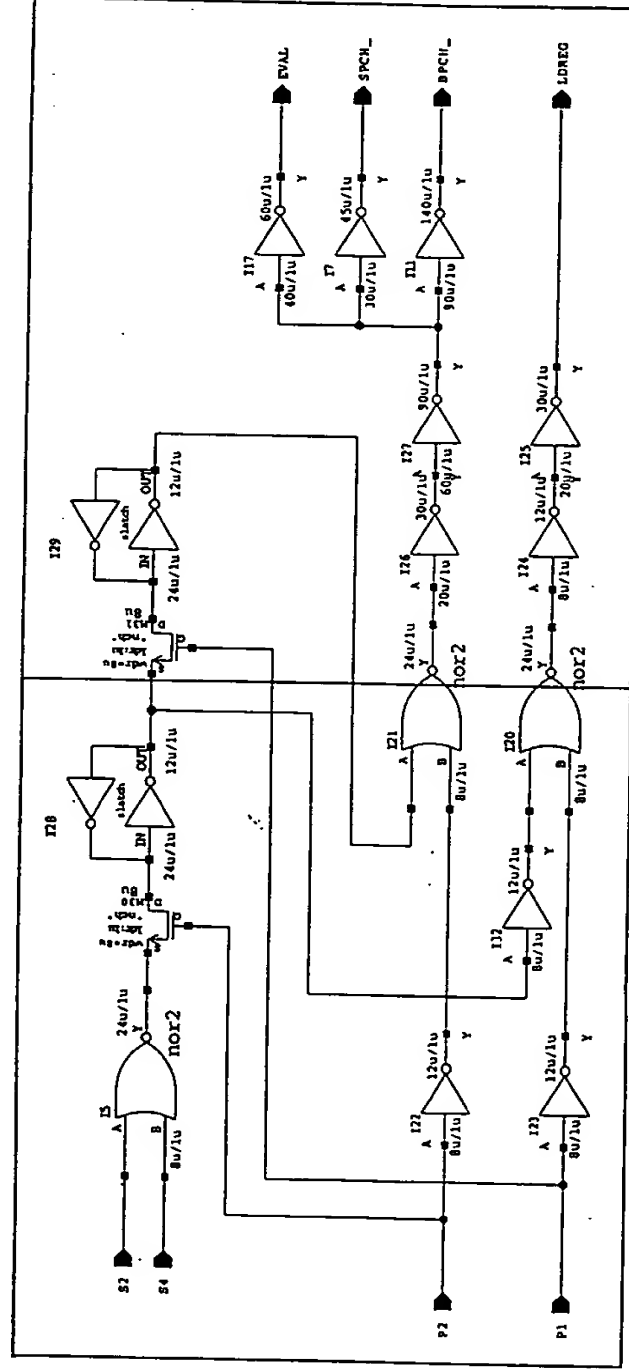


Fig. 7.0401

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ROM Control Logic			
NAME: 103reva/ronct1		REV: -	REV: A
DATE: Oct 3 13:16:28 1993		PART:	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

00000000000000000000

MI40-030

7.0402AA	7.0402AB
----------	----------

EX 7.0402

SECRET

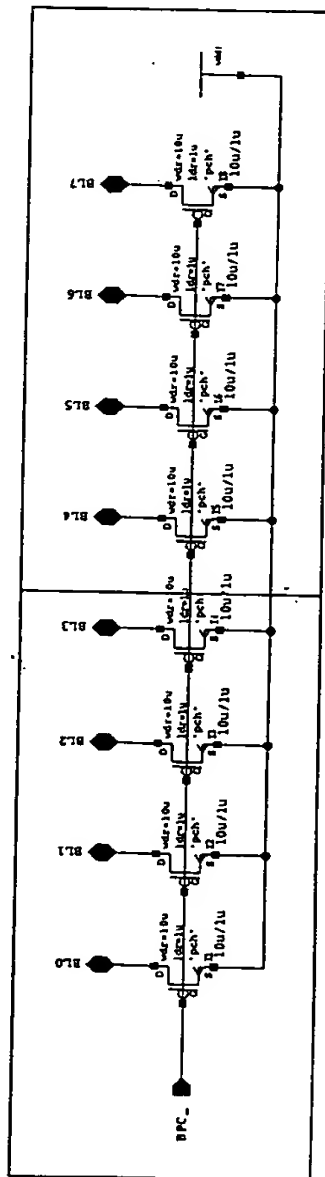


FIG. 7.0402

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ROM Bit Line Precharge			
MADE: 103reva/rompoch		REV: -	SIZE: A
DATE: Oct 7 18:09:48 1993		SHEET:	
MICRON			
COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			

00141000000000000000

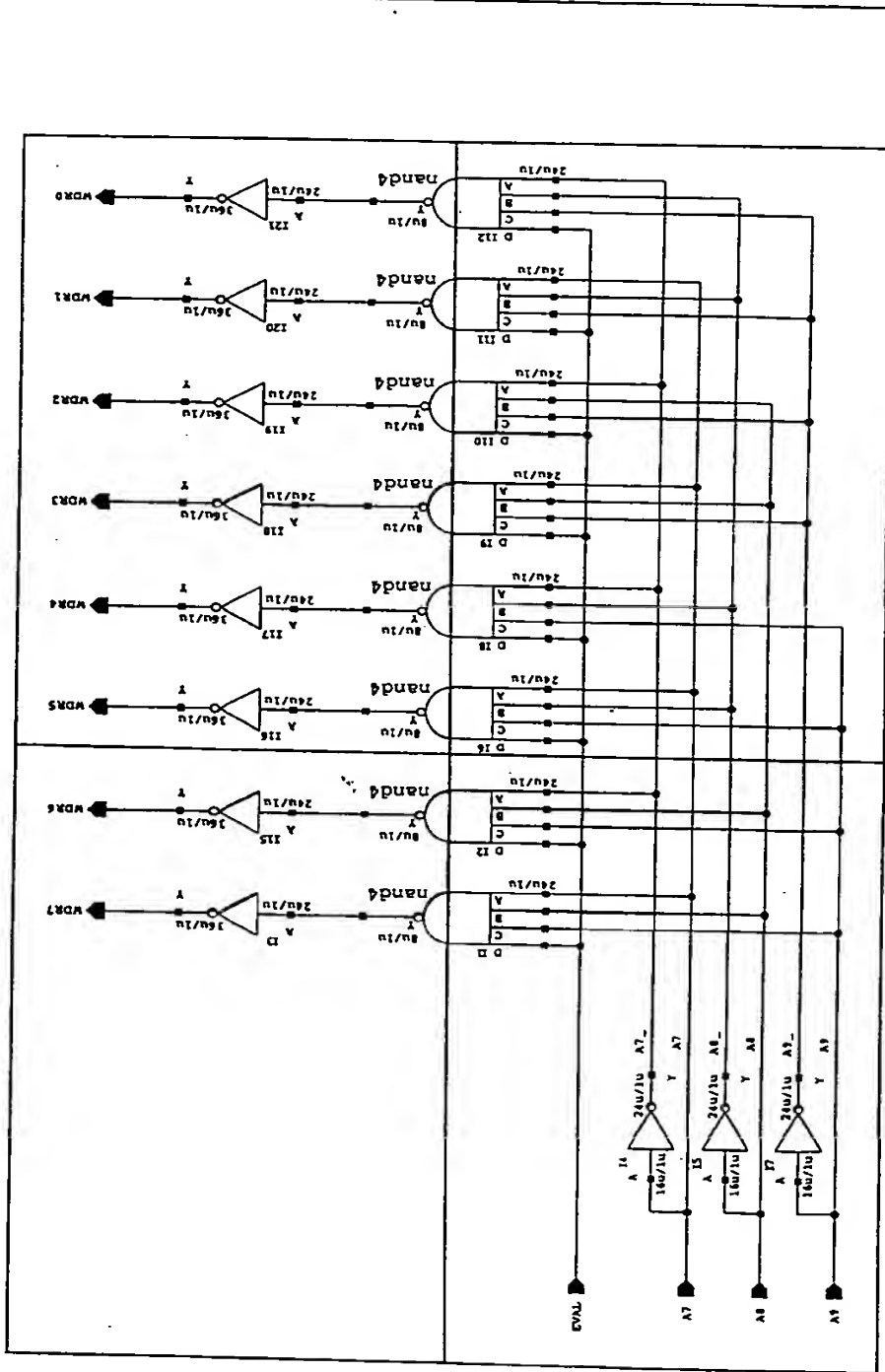
MI40-030

7.0403AA	7.0403AB
7.0403BA	7.0403BB

00141000000000000000

004420-000000

FIG. 7.0903



MICRON				PROJECT: L03		DESIGNER: Rotzoll	
				TITLE: ROM Word Line Driver			
				NAME: 103revA/romwdr		REV: -	SIZE: A
				DATE: Oct 7 18:11:34 1993		SHEET: 1	
MICRON COMMUNICATIONS, INC.							
INTEGRATED CIRCUIT DESIGN							
CONFIDENTIAL INFORMATION							

Итого 7.0404

004720" 000000

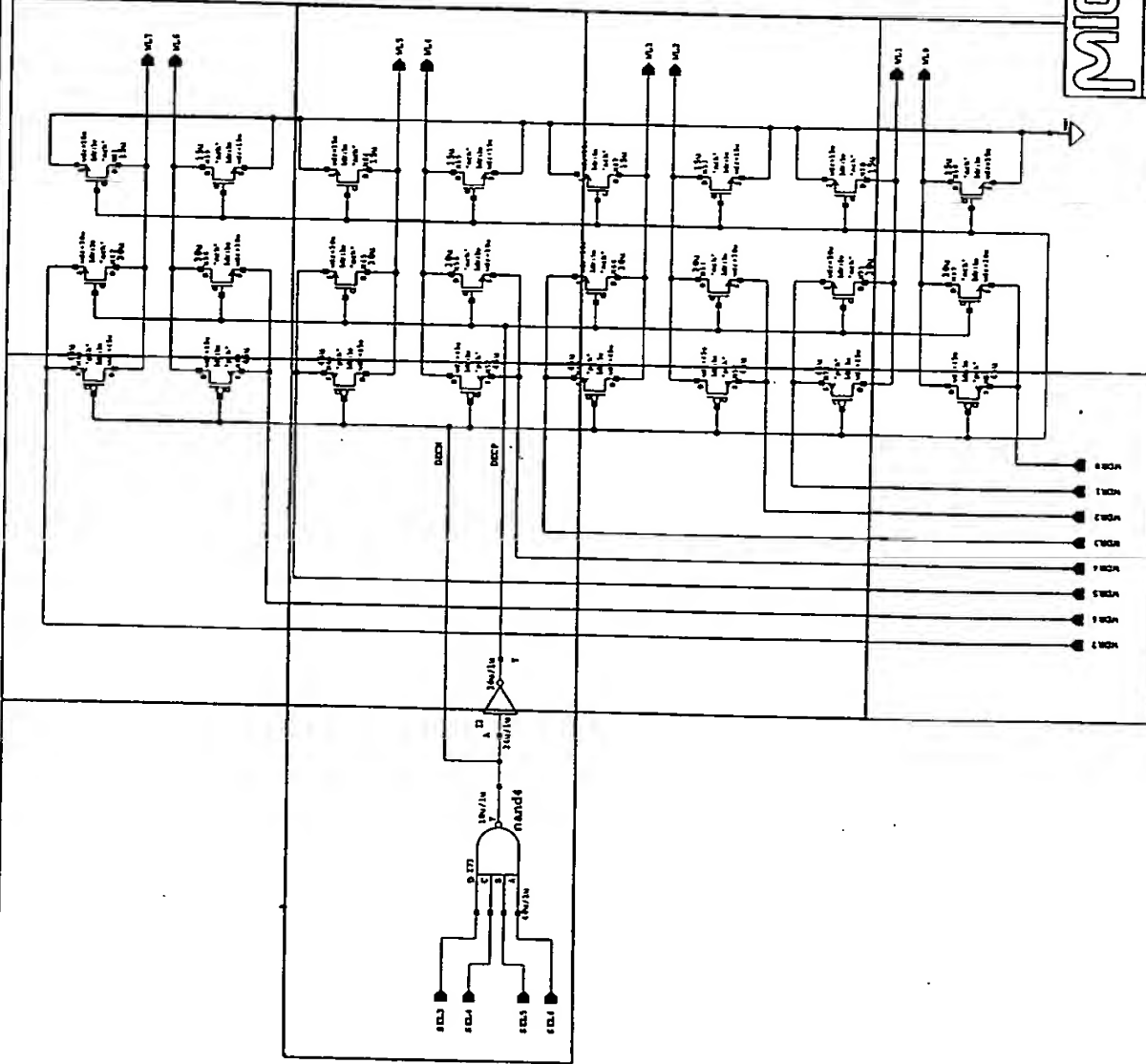


FIG. 7.0404

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DESIGN: L03	REVISED: 002011
Title: Word Block Decoder	
101revn/100ndec_rev	REV: -
Nov 5 17:38:09 1993	DATE:







004420" 00900500

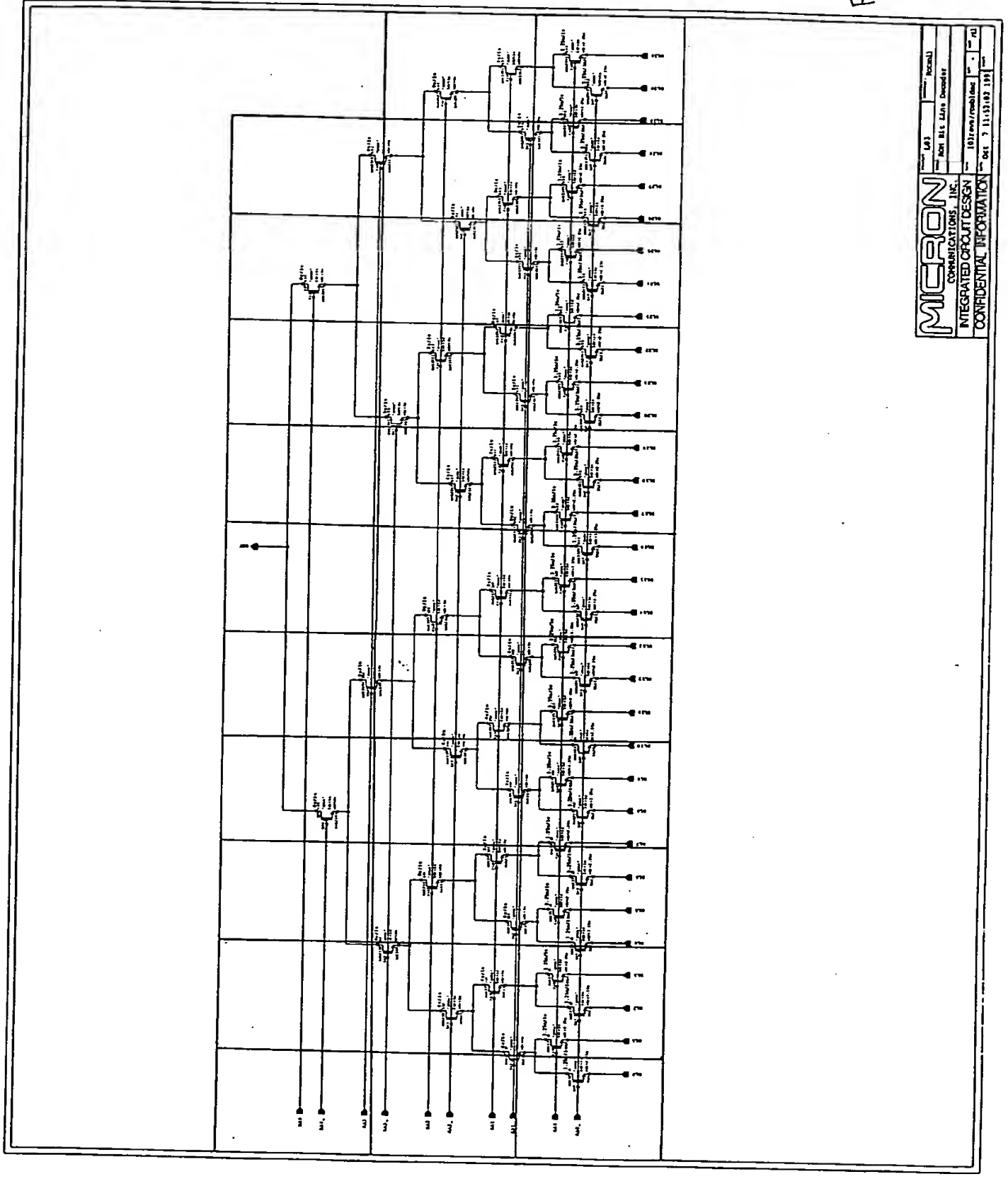
MI40-030

7.0406AA	7.0406AB	7.0406AC	7.0406AD	7.0406AE	7.0406AF	7.0406AG	7.0406AH	7.0406AI	7.0406AJ	
7.0406BA	7.0406BB	7.0406BC	7.0406BD	7.0406BE	7.0406BF	7.0406BG	7.0406BH	7.0406BI	7.0406BJ	7.0406BK
7.0406CA	7.0406CB	7.0406CC	7.0406CD	7.0406CE	7.0406CF	7.0406CG	7.0406CH	7.0406CI	7.0406CJ	7.0406CK

II 7.0406

004720 20900000

Fig. 7.0406



**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Lot	Rev	Part	Decade
101	101	101	101
101	101	101	101
101	101	101	101

Oct 7 11:51:02 1981

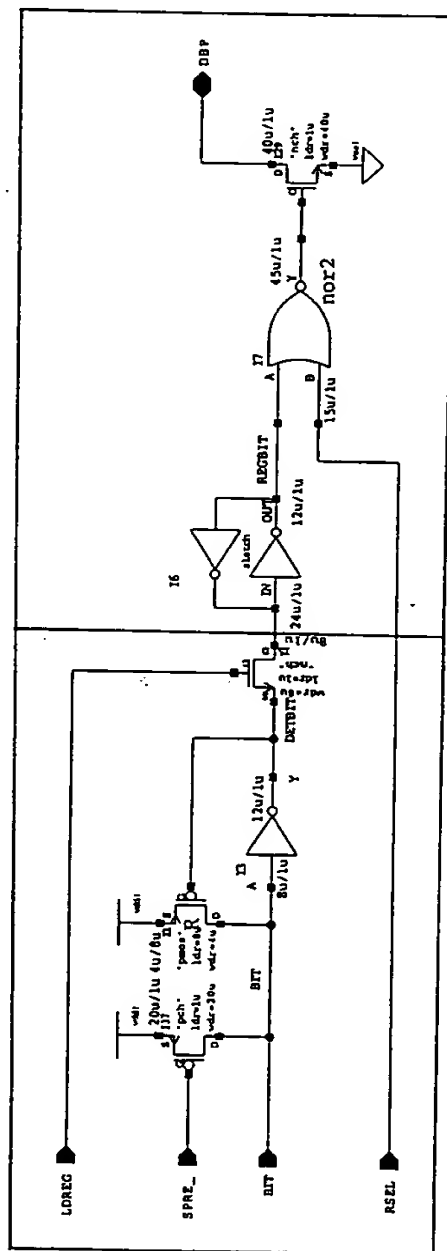
SECRET

MI40-030

7.0407AA	7.0407AB
----------	----------

SECRET

FIG. 7.0407



# 2001

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

PROJECT: L03	DESIGNER: Rotzoll
--------------	-------------------

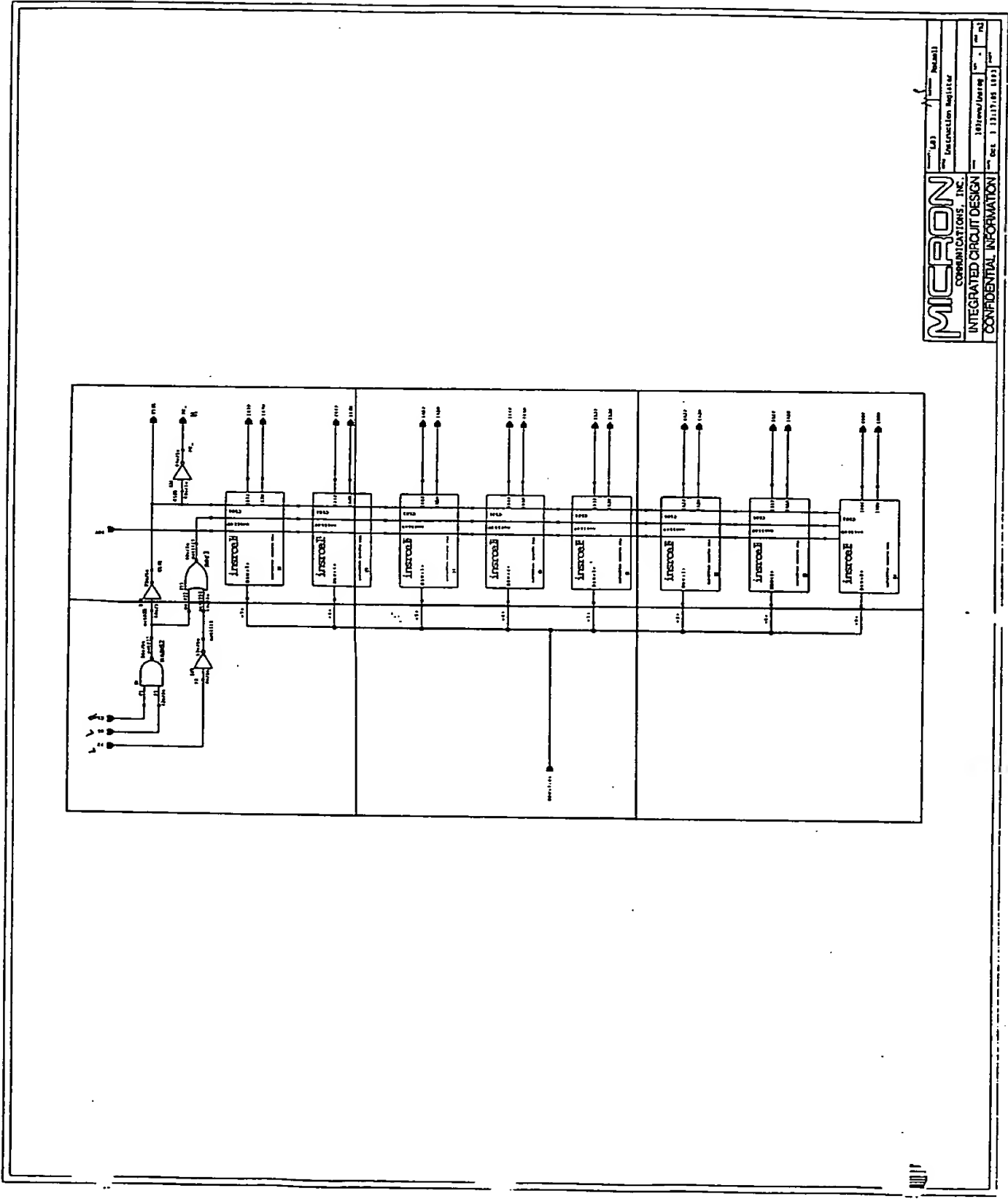
ROM Sense Amplifier	
---------------------	--

NAME:	103reva/romsns	REV:	-	SIZE:	A
-------	----------------	------	---	-------	---

DATE, Oct 7 18:12:58 1993	SHEET:
---------------------------	--------



SECRET



MICRON		Instruction Register	Rev. 1.0
COMPUTATIONS, INC.		1000000000	1000000000
INTEGRATED CIRCUIT DESIGN		1000000000	1000000000
CONFIDENTIAL INFORMATION		1000000000	1000000000

Fig. 7.05



001100 00000000

MI40-030

7.0501AA	7.0501AB
----------	----------

7.0501

DATE: 02/02/93

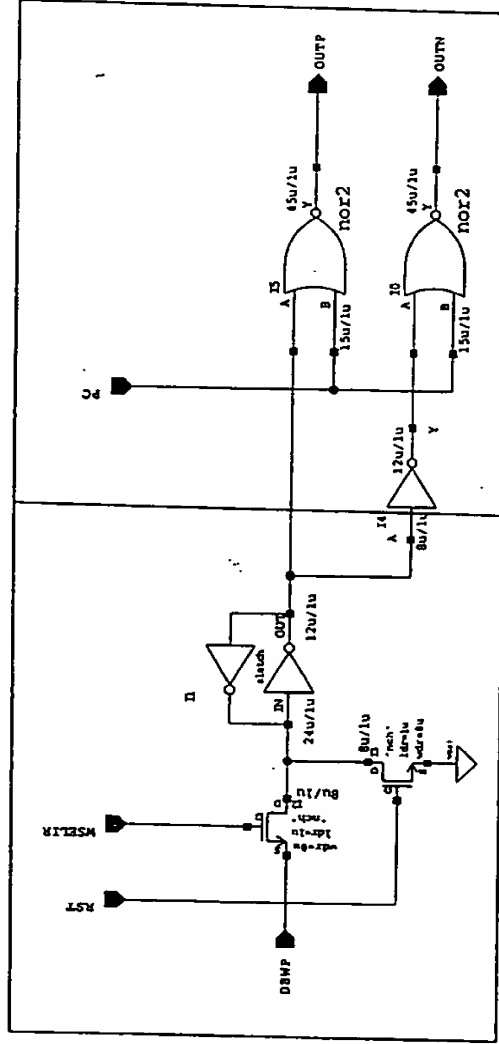


Fig. 7.0501

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: Instruction Register Cell			
NAME: 103reva/insrcel		REV: -	SIZE: A
DATE: Oct 5 20:12:49 1993		PAGE: 1	

007420 20920500

MI40-030

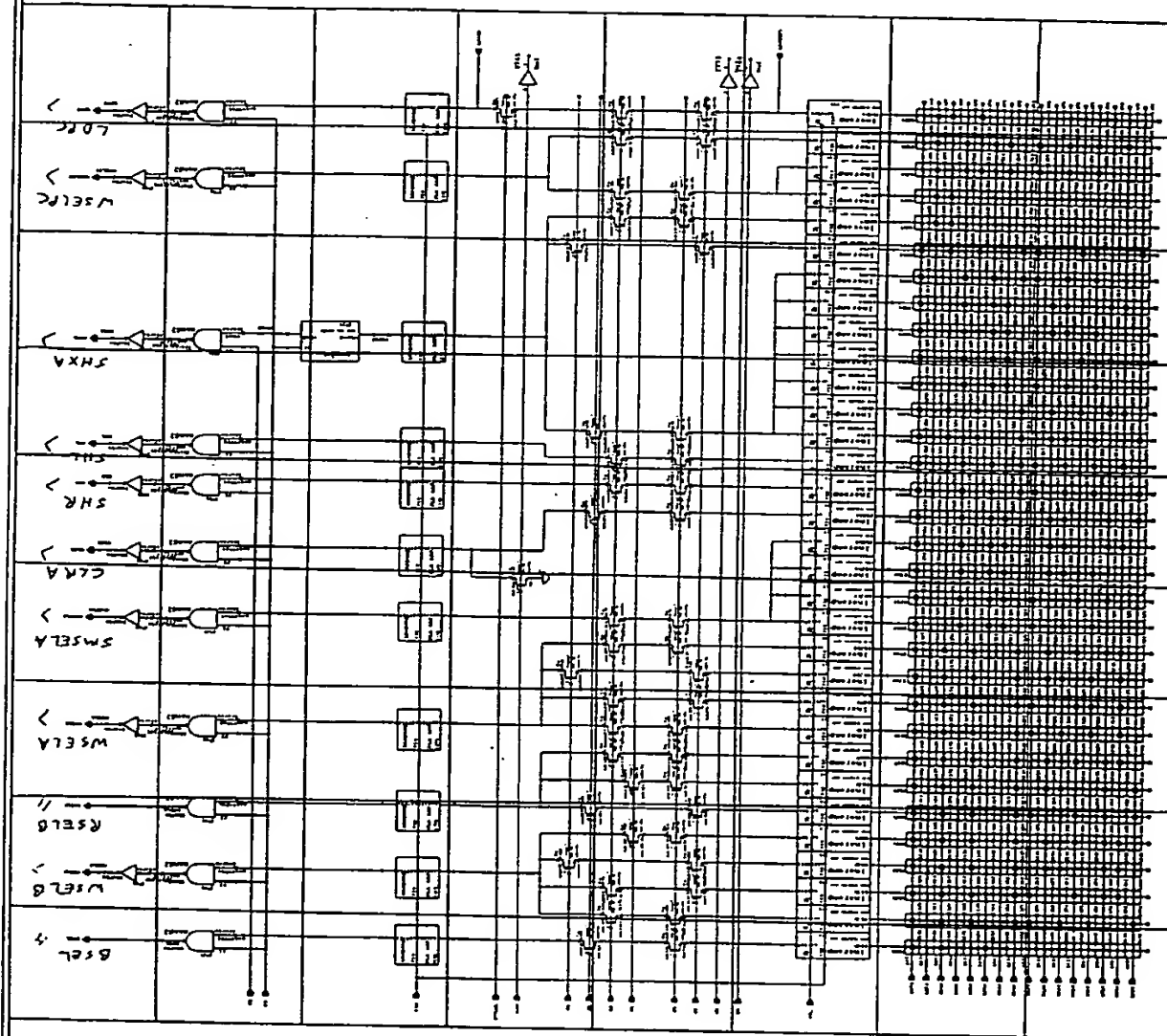
7.06AA	7.06AB	7.06AC	7.06AD	7.06AE	7.06AF	7.06AG	7.06AH	7.06AI	7.06AJ	7.06AK	7.06AL	7.06AM	7.06AN
	7.06BB	7.06BC	7.06BD	7.06BE	7.06BF	7.06BG	7.06BH	7.06BI	7.06BJ	7.06BK	7.06BL	7.06BM	7.06BN
7.06CA	7.06CB	7.06CC	7.06CD	7.06CE	7.06CF	7.06CG	7.06CH	7.06CI	7.06CJ	7.06CK	7.06CL	7.06CM	7.06CN

II II 7.0615

**MICRON**  
CONSULTANTS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

И. П. Е. 1.0601

FIG. 7.0601



Итого 1.0602

CONFIDENTIAL INFORMATION	
INTEGRATED CIRCUIT DESIGN	
Speedy 1647000, 125	
<b>micron</b>	
Speedy 1647000, 125	

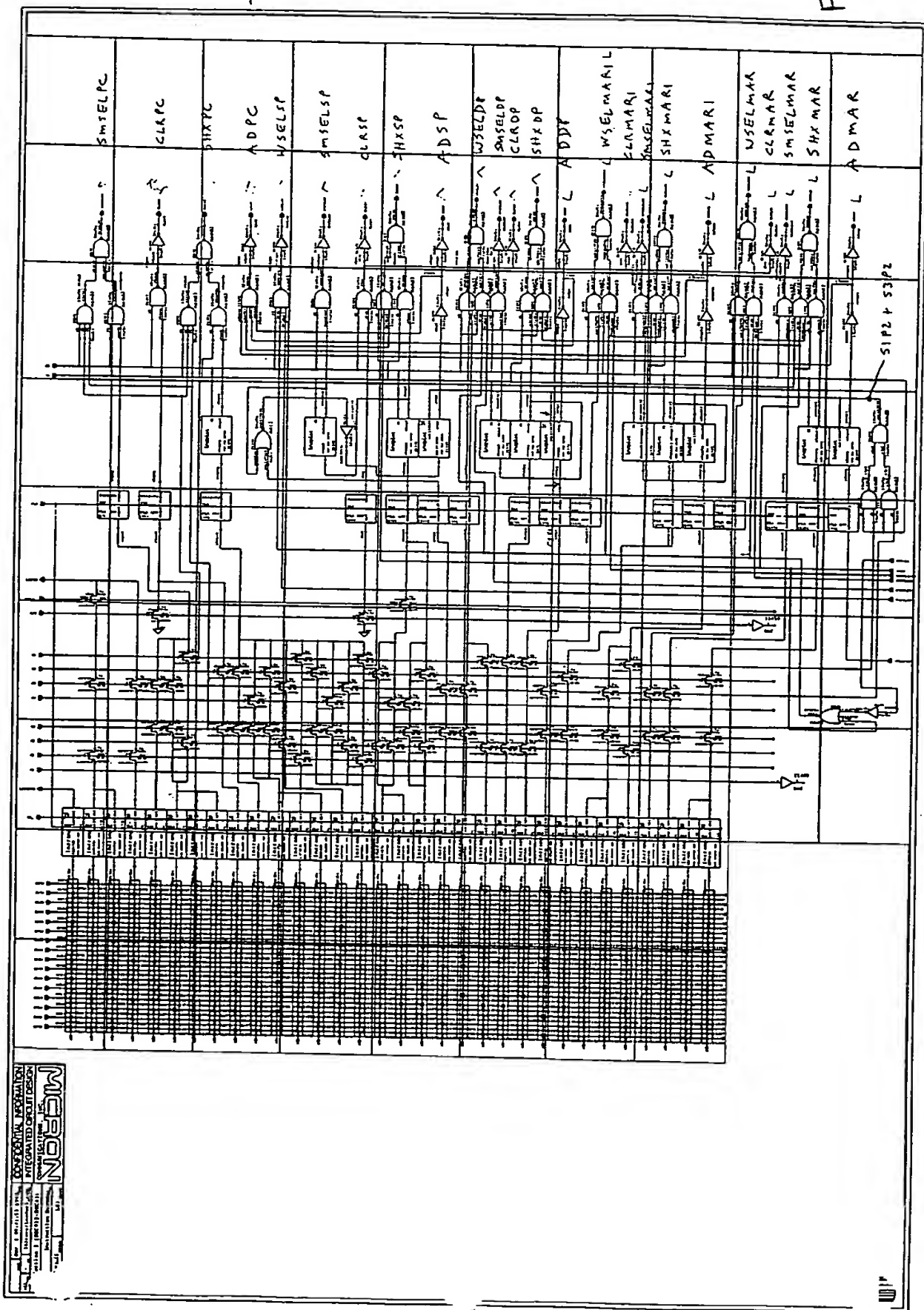


FIG. 7.0602

- ADPC is <sup>not</sup> one of the assent.  
ADPC, ADPC, ADPC,  
ADPC



ЕОГН. II

007420 2050500

PROJECT	DATE	BY
007420	2050500	

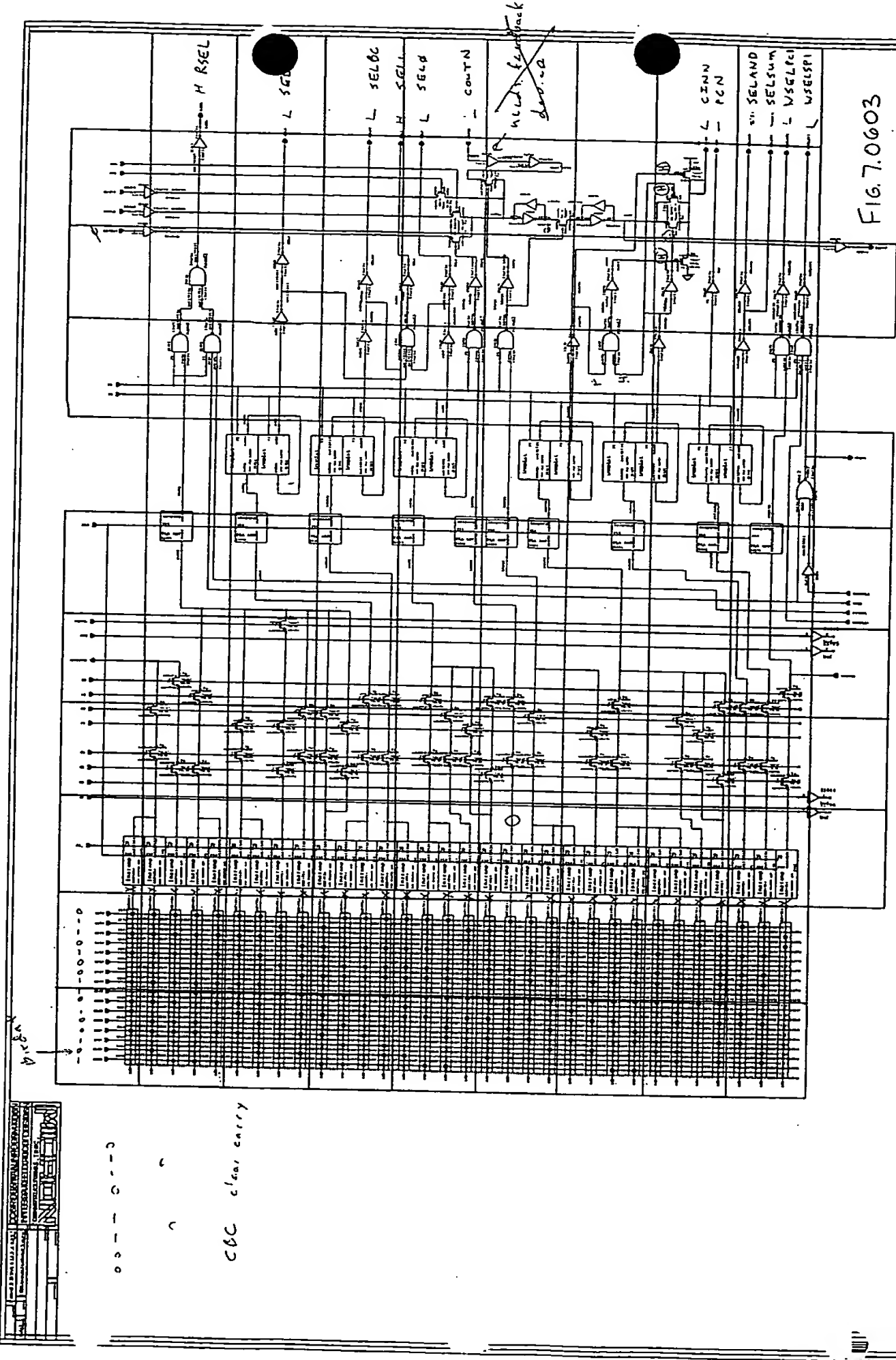


FIG. 7.0603

Итого: 26000 руб.

00101050

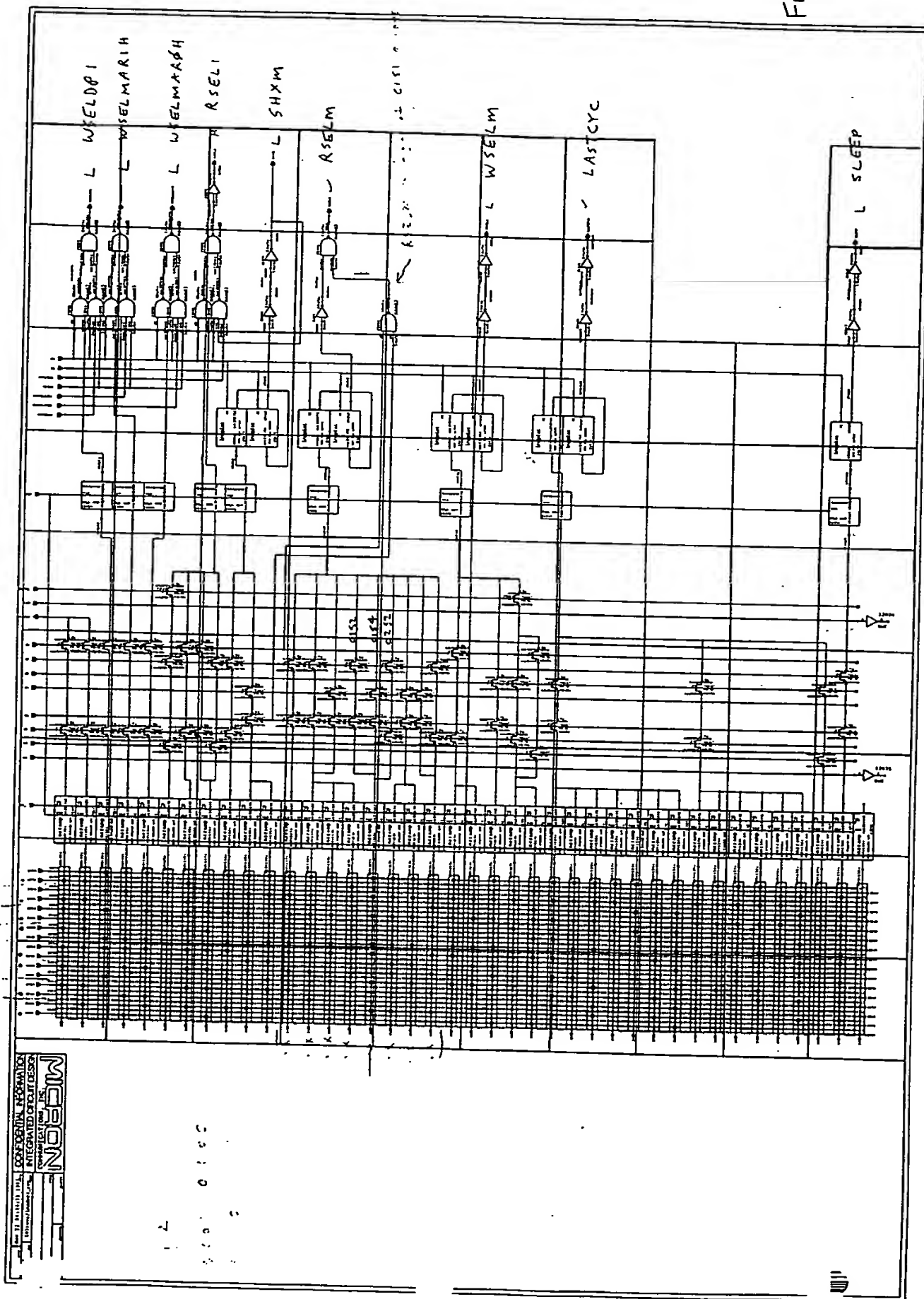
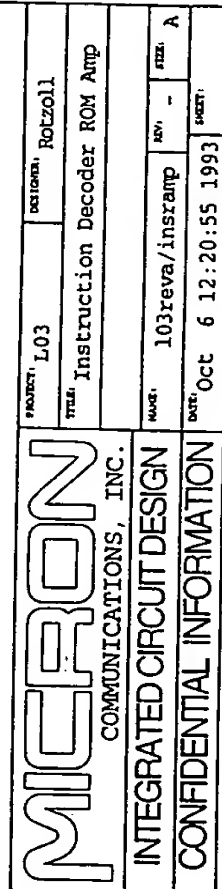
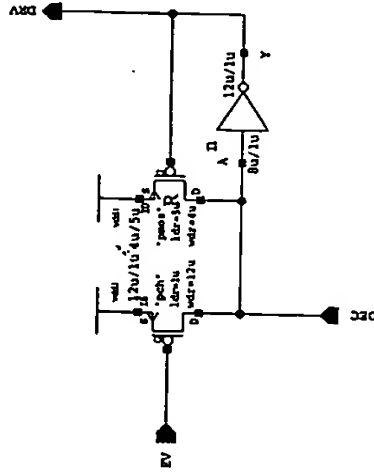


Fig. 7.0604

FIG. 7.060401

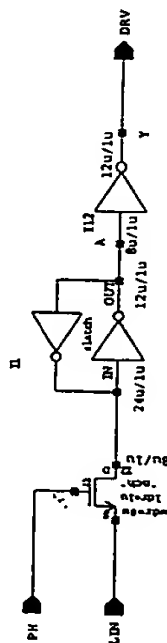




<b>MICRON</b>		PROJECT: L03	DESIGNER: Retzoll
COMMUNICATIONS, INC.		TITLE: Instruction Decoder PLA Amp	
INTEGRATED CIRCUIT DESIGN		NUMBER: 103reva/inspamp	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 6 12:21:34 1993	SIZE: A

Fig. 7.060402

FIG. 7.060903



<b>MICRON</b>		PROJECT: L03	DESIGNER: Rotzoll
		TITLE: Instruction Decoder PLA Latch	
		NAME: 103reva/insplat	REV: A
		DATE: Sep 29 16:10:56 1993	

7.0788

7.07BA

Π Π Π Π.Π.Π.



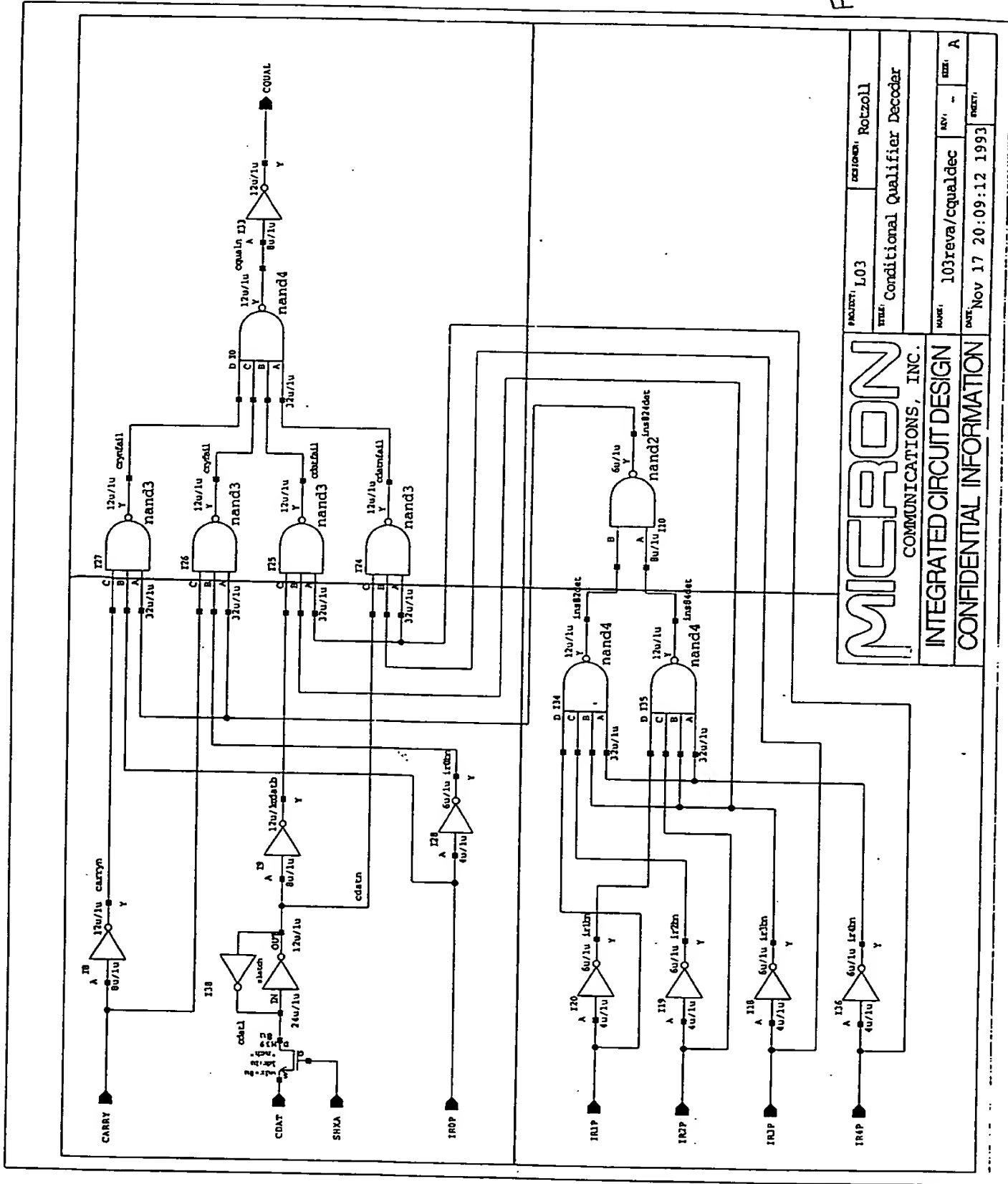


Fig. 7.07

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03

DESIGNER: Rotzoll

TITLE: Conditional Qualifier Decoder

NAME: 103reva/cqualdec

DATE: Nov 17 20:09:12 1993

REV: -

REV: A

7.08AA

7.08BA

7.08CA

И.И.Ев 1.088

[illegible]

2.

MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
PROJECT	L03	DATE	10/27/90
DESIGN	Database Latch/Precharge	DESIGNER	103rcw/db/latch
		DATE	11/01/90
		TIME	14:51:49 1993

7.09AA	7.09AB	7.09AC	7.09AD	7.09AE	7.09AF
7.09BA	7.09BB	7.09BC	7.09BD	7.09BE	7.09BF

<b>MICRON</b>		Form 1-81		Date: 10/1/81	
Specialty CASTING, INC.		Job #		Project #	
INTEGRATED CIRCUIT DESIGN		Drawing #		Rev. #	
CONFIDENTIAL INFORMATION		Part #		Lot #	

7.0901AA	7.0901AB	7.0901AC	7.0901AD	7.0901AE
7.0901BA	7.0901BB	7.0901BC	7.0901BD	7.0901BE
7.0901CA	7.0901CB	7.0901CC	7.0901CD	7.0901CE

[illegible]

Fig. 7.0901

007720 20320300

MI40-030

7.090101AA	7.090101AB	7.090101AC	7.090101AD
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II II II II II II II II



3

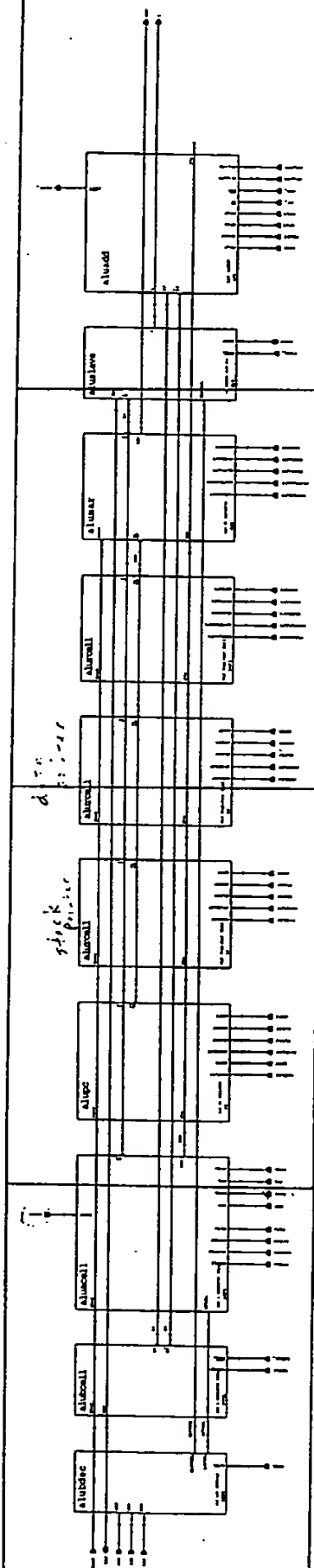
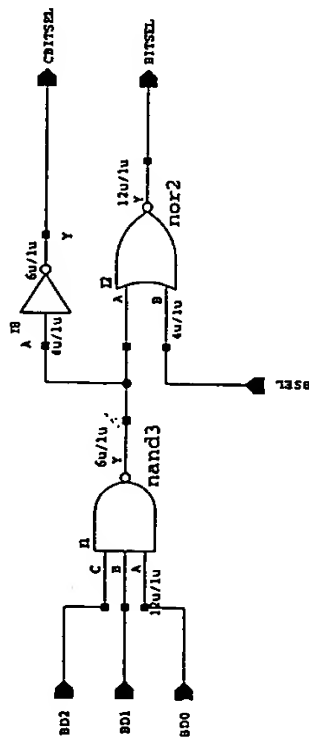


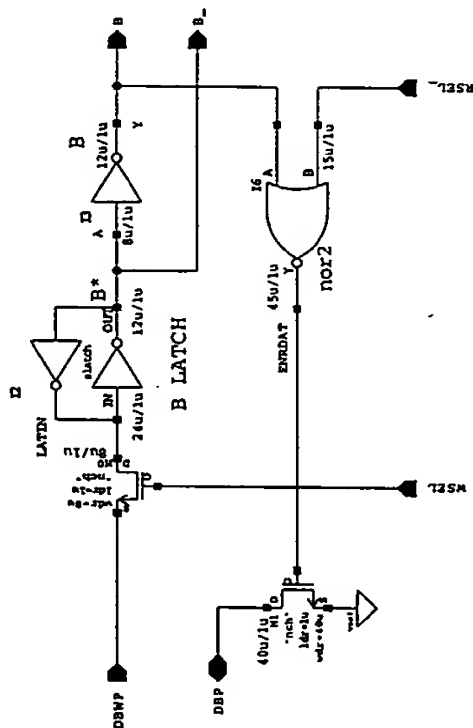
Fig. 7.090101



<b>MICRON</b>		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		UNIT: ALU Bit Decoder Cell	
INTEGRATED CIRCUIT DESIGN		DATE: 103revs/alubdec	REV: -
CONFIDENTIAL INFORMATION		DATE: Sep 29 16:07:43 1993	SIZE: A

FIG. 7.09010101

Fig. 7.09010102

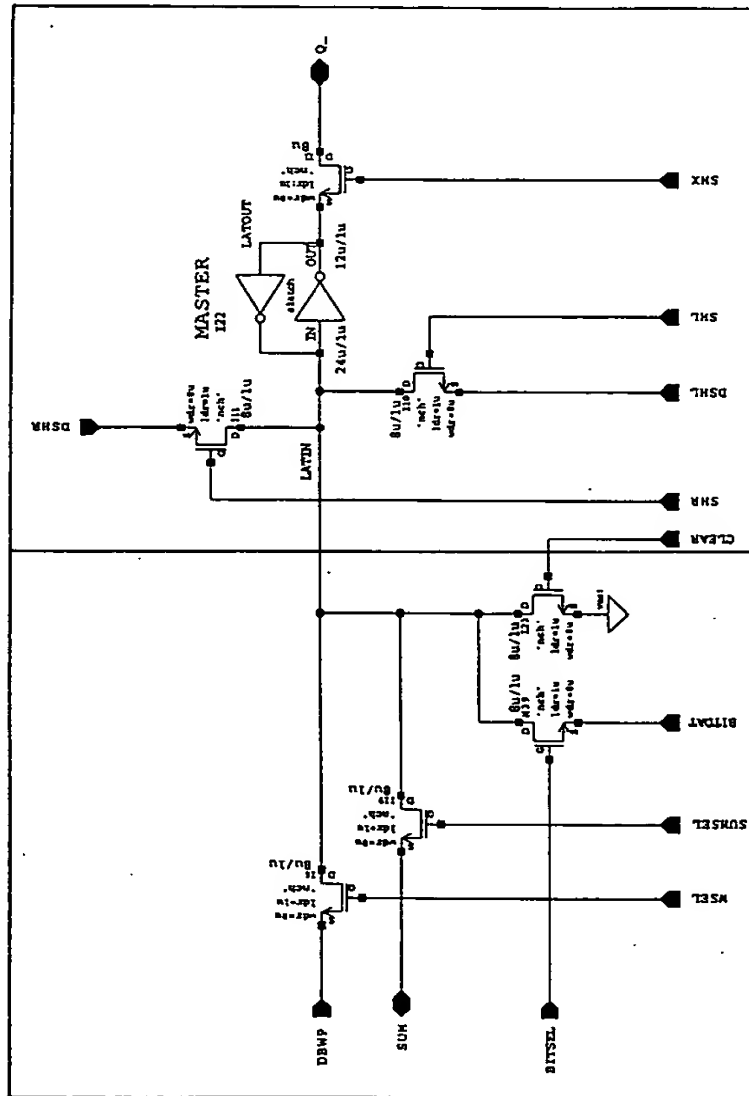


<b>PROJECT:</b> L03		<b>DESIGNER:</b> Rotzoll
<b>TITLE:</b> ALU B Register Cell		
<b>NAME:</b> 103reva/alubcell		
<b>KEY:</b> -	<b>SIZE:</b> A	
<b>DATE:</b> Oct 1 15:32:35 1993		<b>PAGE:</b> 1

7.09010103AA	7.09010103AB
--------------	--------------

7.09010103AB

EDITORS: LEO H. HARRIS



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ALU A Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/aluacell	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 1 15:41:37 1993	ITEM: A



7.09010104AA	7.09010104AB
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7.09010104AB

1111

# FOOTNOTES

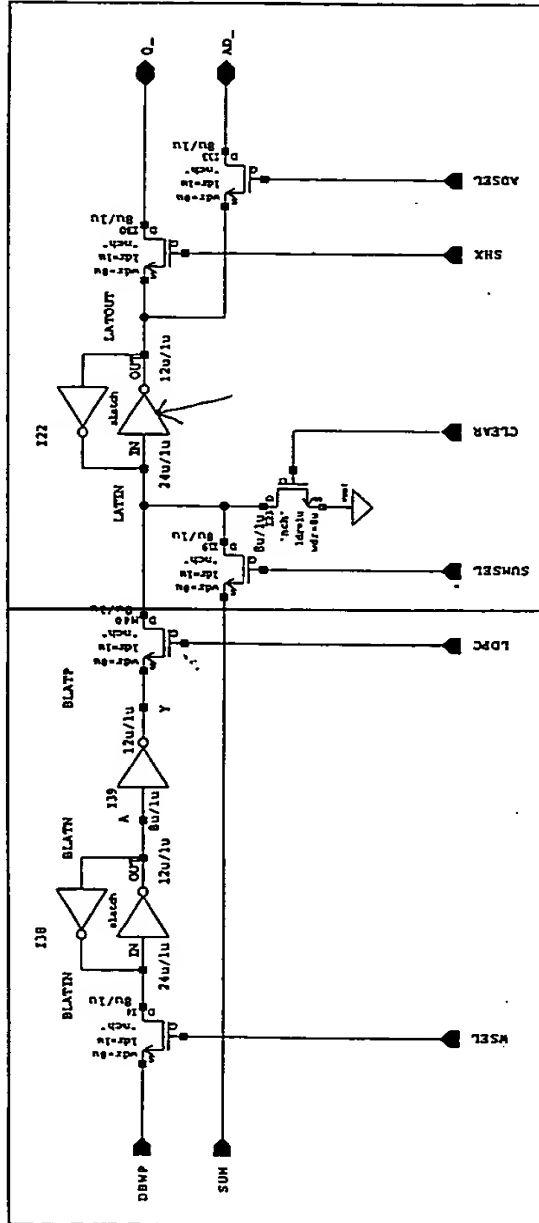
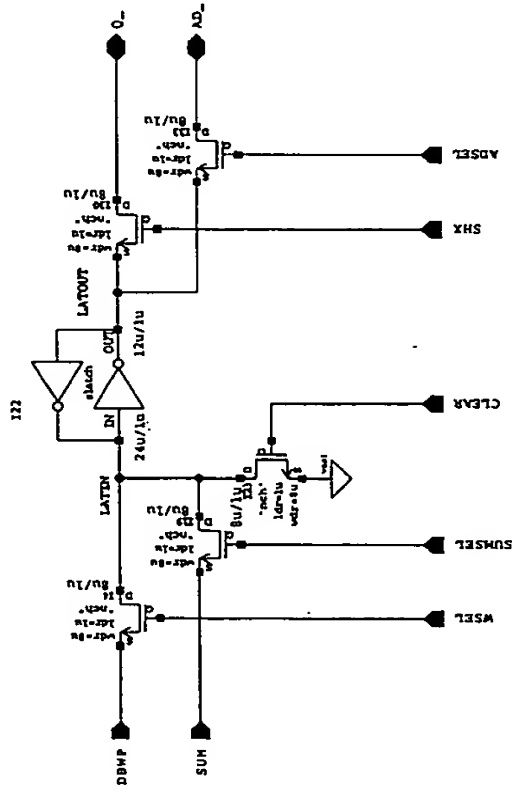


Fig. 7.09010104

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ALU Register Cell			
NAME:	103reva/alupc	REV: -	CTRL: A
DATE:	Oct 1 15:45:48 1993		
MICRON COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			



<div>MICRON</div> <div>COMMUNICATIONS, INC.</div>				PROJECT: L03		DESIGNER: Rotzoll			
				TITLE: ALU Register Cell					
INTEGRATED CIRCUIT DESIGN				NAME: 103reva/alurcell		REV: -		SIZE: A	
CONFIDENTIAL INFORMATION				DATE: Oct 1 15:51:03 1993		SHEET: 1			

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

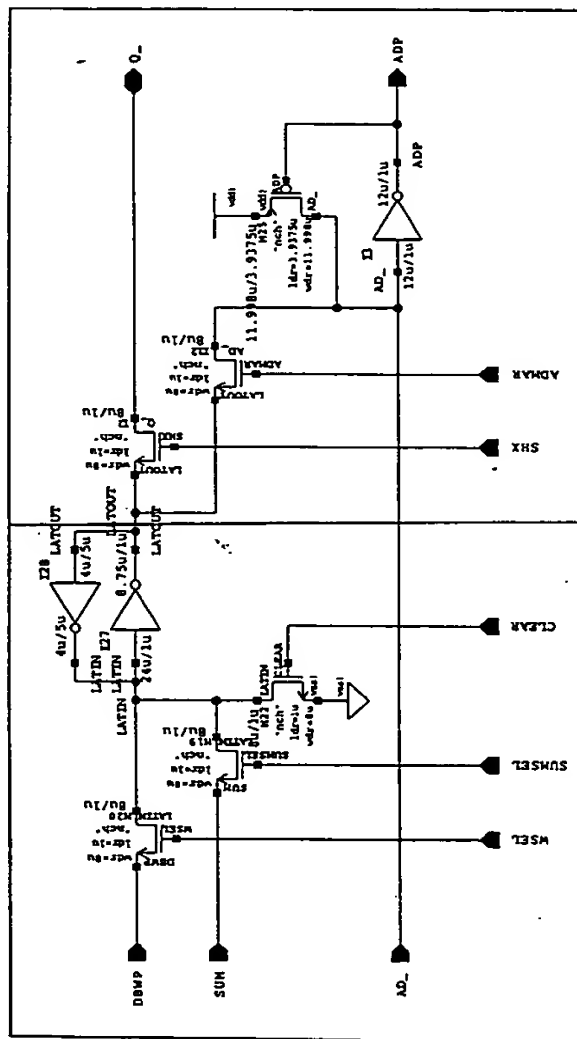


00000000000000000000

MI40-030

7.09010106AA	7.09010106AB
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7.09010106

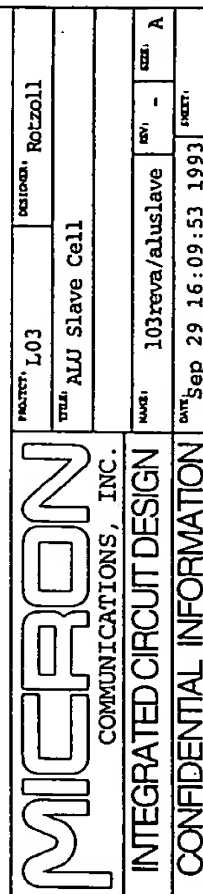


**B8: added pch feedback device**

<b>micron</b>		PROJECT: L03	DESIGNER: JOTOOLE
		TITLE: ALU Memory Address Register	
COMMUNICATIONS, INC.		NAME: 103reva/alumar	REV: B8
INTEGRATED CIRCUIT DESIGN		DATE: Jan 4 10:27:28 1996	SHEET: A
CONFIDENTIAL INFORMATION			

FIG. 7.09010106

FIG. 7.09010107



[illegible]

MI40-030

7.09010108AA	7.09010108AB	7.09010108AC
7.09010108BA	7.09010108BB	7.09010108BC

БРИТОВ. И. И.

BS: move feedback device from I21 to I27

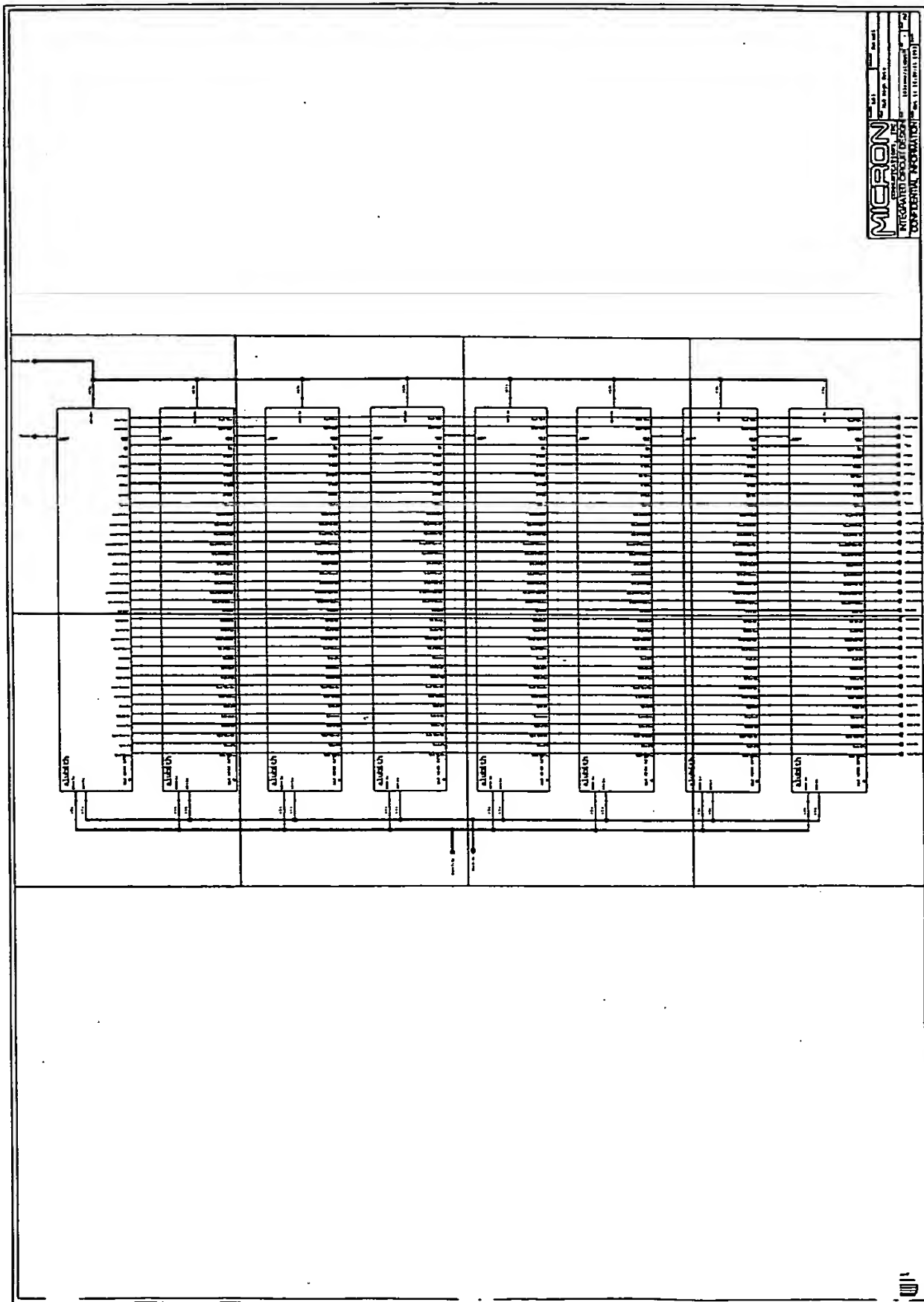
MICRON COMMUNICATIONS, INC.		INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
NAME: 101 roya/aluadd		EXT: B5		DATE: Sep 16 15:48:21 1995	
TEL: ALU addor		EXT: B5		DATE: Sep 16 15:48:21 1995	
FAX: 101		EXT: B5		DATE: Sep 16 15:48:21 1995	

7.0902AA	7.0902AB	7.0902AC	7.0902AD
7.0902BA	7.0902BB	7.0902BC	7.0902BD

SECRET 7.0902

004420 20520500

FIG. 7.0902



007420 00900500

MI40-030

7.090201AA	7.090201AB	7.090201AC
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EX 7.090201



001120" 0090201

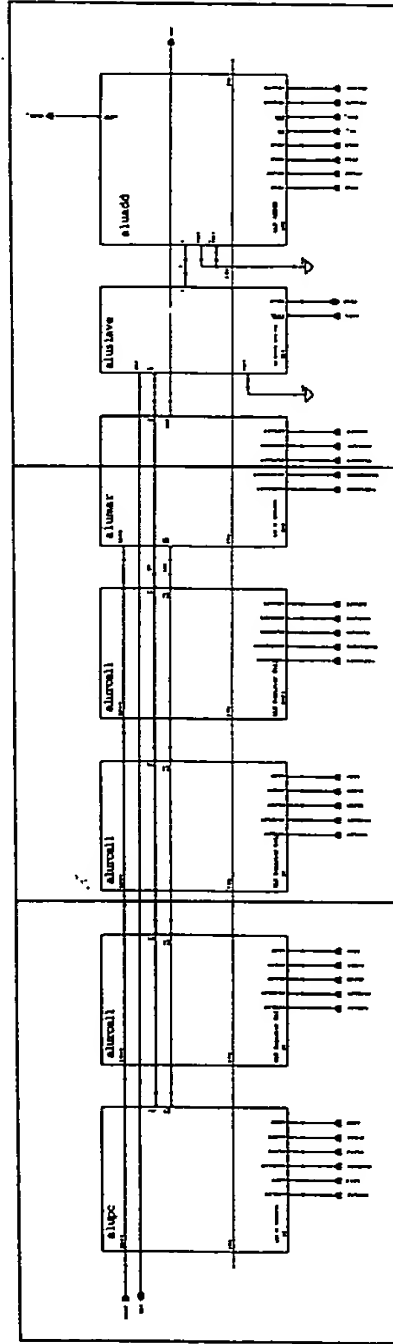
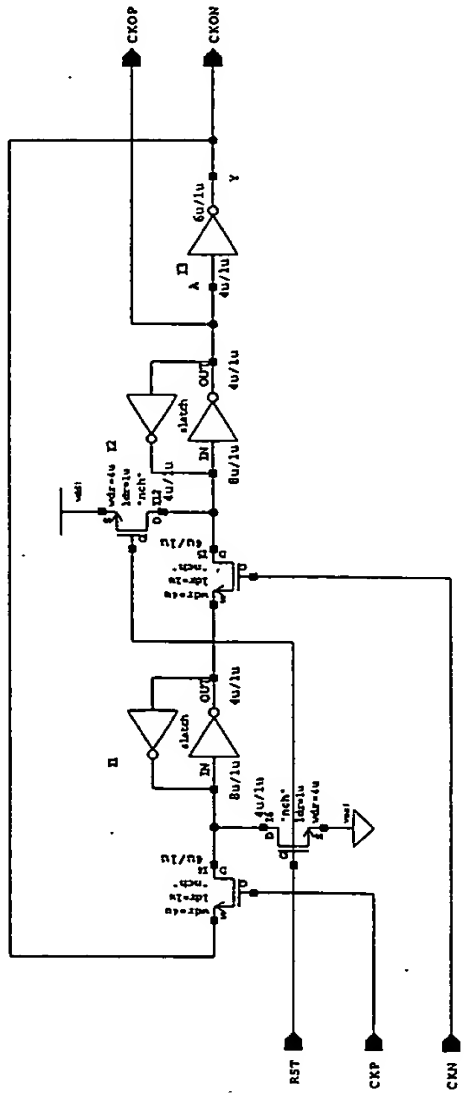


Fig. 7.090201

7.10AA	7.10AB	7.10AC
7.10BA	7.10BB	7.10BC
7.10CA	7.10CB	7.10CC



00120" 00300500



12/29/92

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Timed Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/tldcel	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	ENTRY:

Fig. 7.1001

0014120-00000000

MI40-030

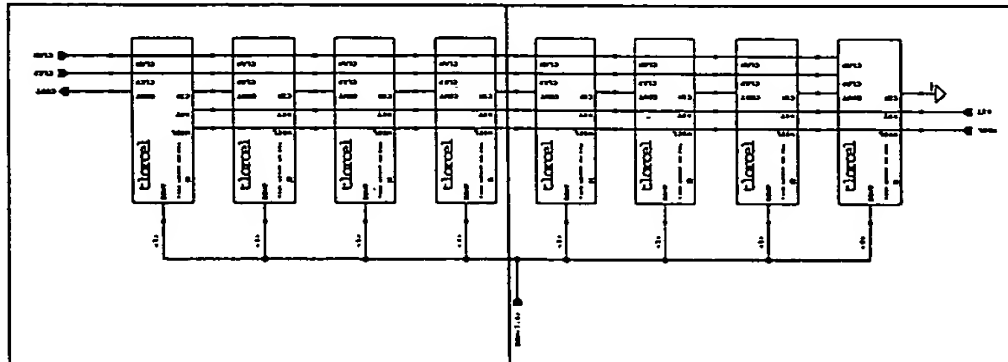
7.11AA

7.11AB

II II II II II II

00440 000000

Fig. 7.11



<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Doc. 1310517-100	Rev. 1
Doc. 1310517-100	Rev. 1
Doc. 1310517-100	Rev. 1
Doc. 1310517-100	Rev. 1







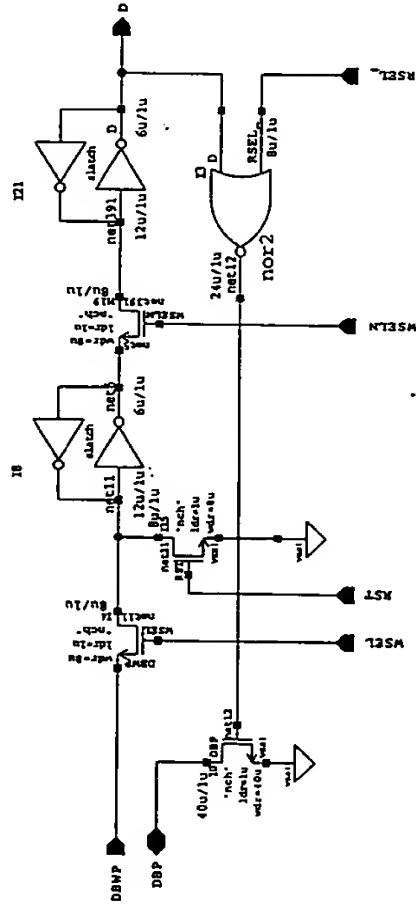
001420" 20920500

MI40-030

7.12AA	7.12AB	7.12AC
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II 09 11 12



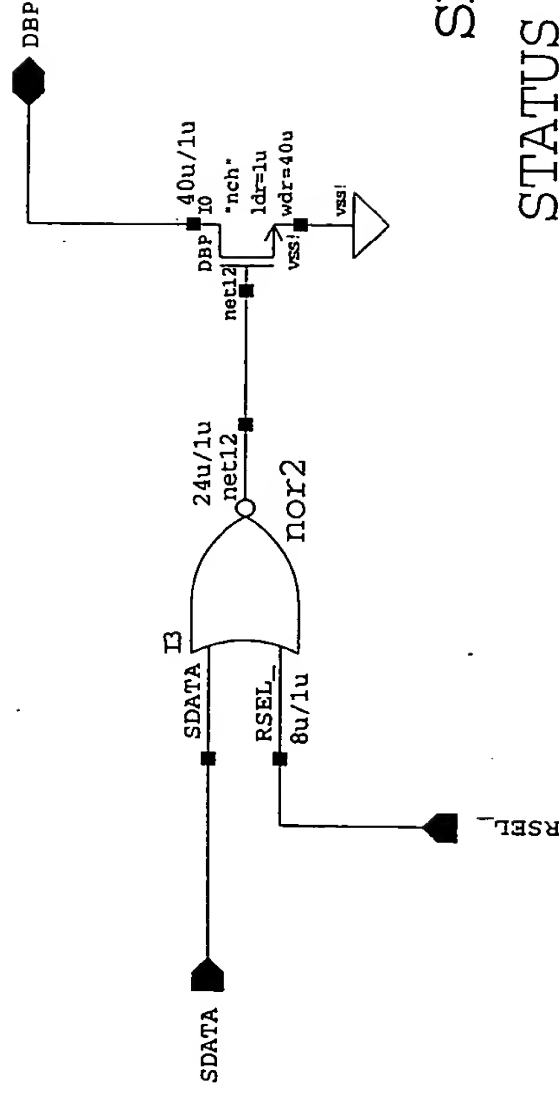


<b>MICRON</b>		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: R/W Control Register Cell	
INTEGRATED CIRCUIT DESIGN		NUMBER: 103reva/regcell	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 12 09:41:36 1993	SHEET: A

Fig. 7.1201







sregcell

STATUS REGISTER CELL

R. Rotzoll

12/8/92

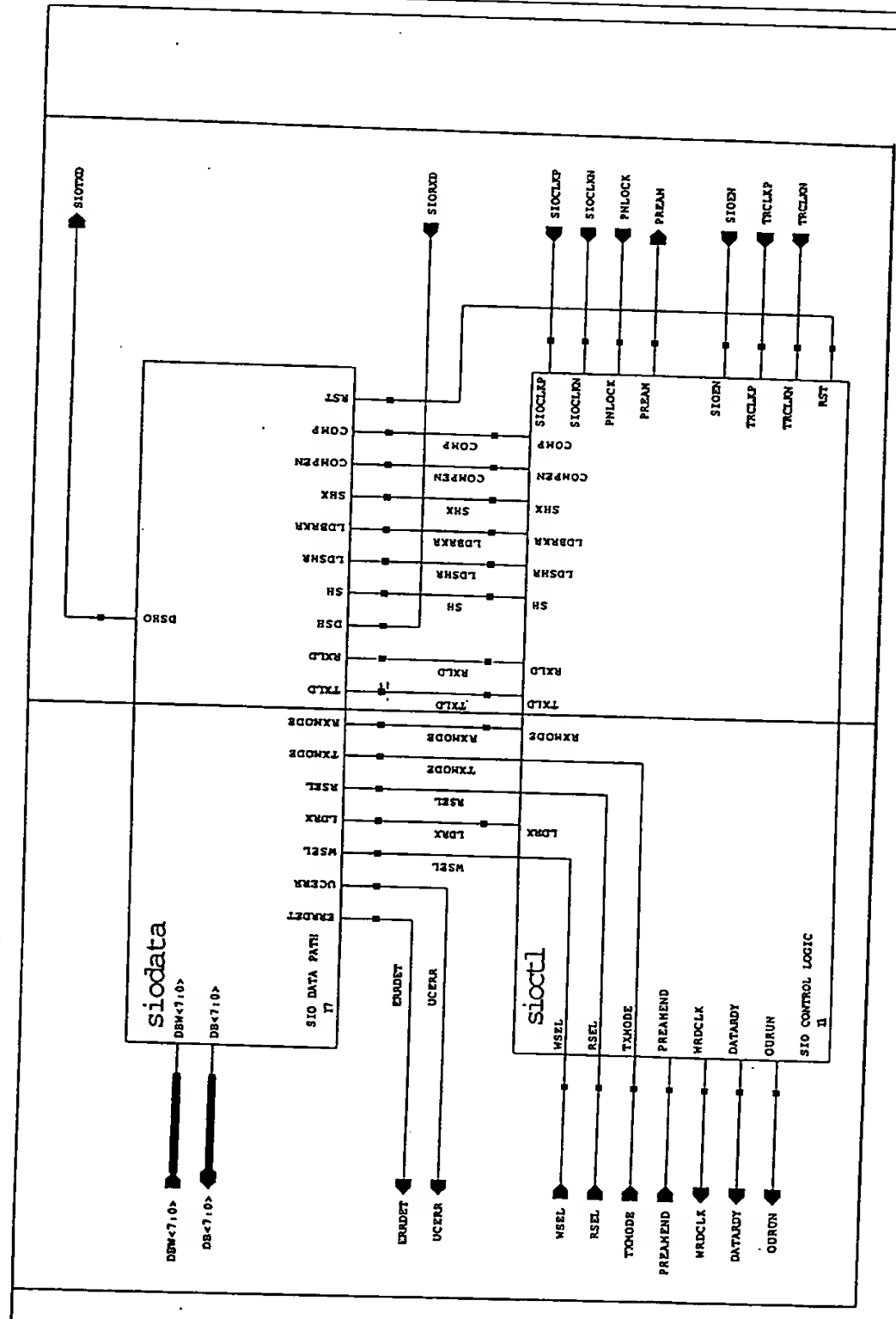
Fig. 7.1301

001120 20000000

7.14AA	7.14AB
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II II II 7.14

Fig. 7.14



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		Serial I/O	
INTEGRATED CIRCUIT DESIGN		Serial/Block Enc/Dec	
CONFIDENTIAL INFORMATION		NAME: 103reva/sio	REV: B11
		DATE: Apr 4 10:16:14 1996	CHG: A



7.1401AA	7.1401AB	7.1401AC	7.1401AD	7.1401AE	7.1401AF
7.1401BA	7.1401BB	7.1401BC	7.1401BD	7.1401BE	7.1401BF
7.1401CA	7.1401CB	7.1401CC	7.1401CD	7.1401CE	7.1401CF
7.1401DA	7.1401DB	7.1401DC	7.1401DD	7.1401DE	7.1401DF
7.1401EA	7.1401EB	7.1401EC	7.1401ED	7.1401EE	7.1401EF
7.1401FA	7.1401FB	7.1401FC	7.1401FD	7.1401FE	7.1401FF
7.1401GA	7.1401GB	7.1401GC	7.1401GD	7.1401GE	7.1401GF

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

DATE: 10/15/88  
 TIME: 10:15 AM  
 BY: [Signature]  
 FOR: [Signature]  
 TO: [Signature]  
 FROM: [Signature]

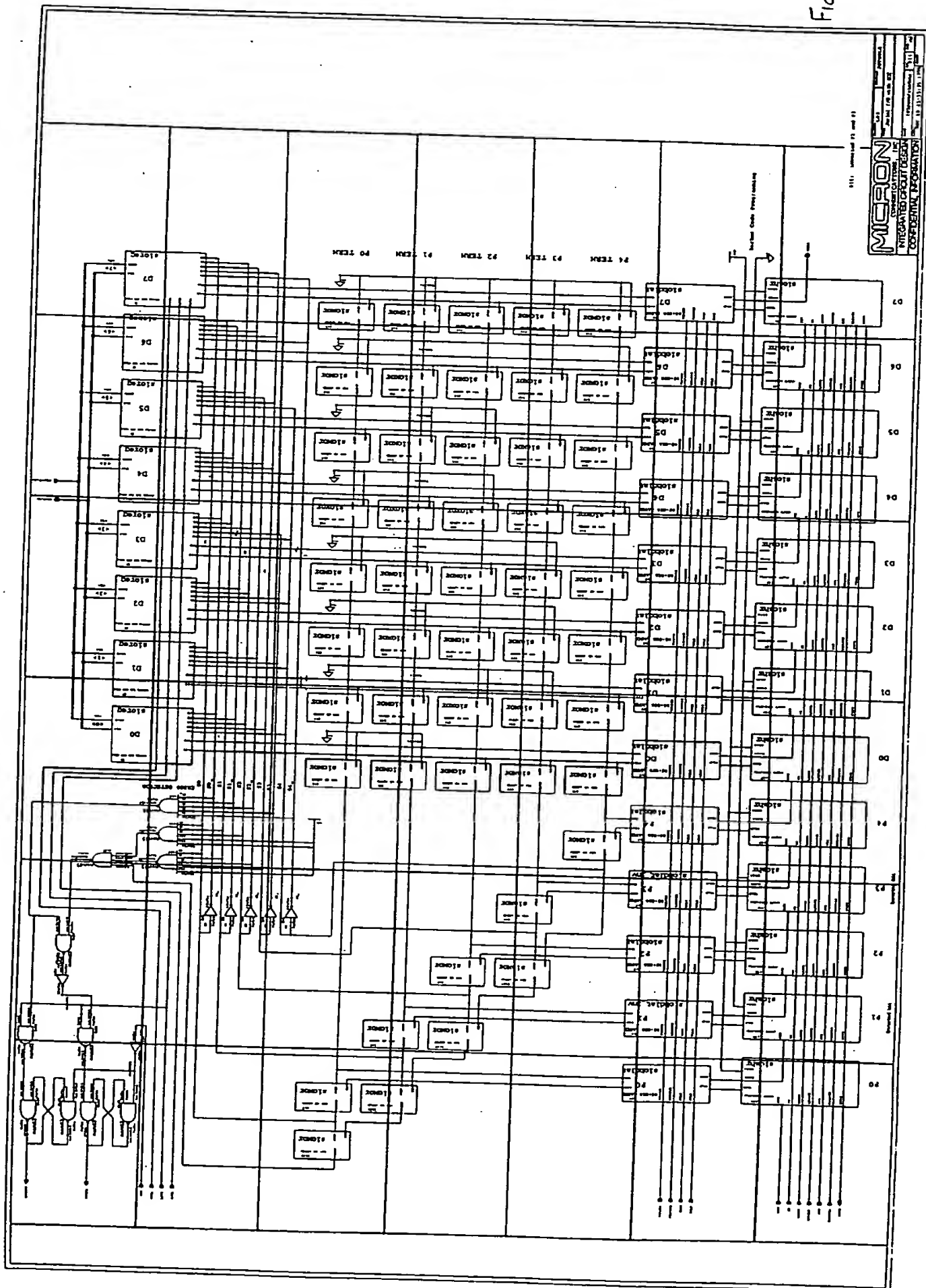


Figure 1. The effect of the concentration of the *Agaricus bisporus* spores on the growth of *Agaricus bisporus* on the substrate. The concentration of the spores was 10<sup>4</sup>, 10<sup>5</sup>, 10<sup>6</sup>, 10<sup>7</sup>, 10<sup>8</sup>, 10<sup>9</sup>, 10<sup>10</sup>, 10<sup>11</sup>, 10<sup>12</sup>, 10<sup>13</sup>, 10<sup>14</sup>, 10<sup>15</sup>, 10<sup>16</sup>, 10<sup>17</sup>, 10<sup>18</sup>, 10<sup>19</sup>, 10<sup>20</sup>, 10<sup>21</sup>, 10<sup>22</sup>, 10<sup>23</sup>, 10<sup>24</sup>, 10<sup>25</sup>, 10<sup>26</sup>, 10<sup>27</sup>, 10<sup>28</sup>, 10<sup>29</sup>, 10<sup>30</sup>, 10<sup>31</sup>, 10<sup>32</sup>, 10<sup>33</sup>, 10<sup>34</sup>, 10<sup>35</sup>, 10<sup>36</sup>, 10<sup>37</sup>, 10<sup>38</sup>, 10<sup>39</sup>, 10<sup>40</sup>, 10<sup>41</sup>, 10<sup>42</sup>, 10<sup>43</sup>, 10<sup>44</sup>, 10<sup>45</sup>, 10<sup>46</sup>, 10<sup>47</sup>, 10<sup>48</sup>, 10<sup>49</sup>, 10<sup>50</sup>, 10<sup>51</sup>, 10<sup>52</sup>, 10<sup>53</sup>, 10<sup>54</sup>, 10<sup>55</sup>, 10<sup>56</sup>, 10<sup>57</sup>, 10<sup>58</sup>, 10<sup>59</sup>, 10<sup>60</sup>, 10<sup>61</sup>, 10<sup>62</sup>, 10<sup>63</sup>, 10<sup>64</sup>, 10<sup>65</sup>, 10<sup>66</sup>, 10<sup>67</sup>, 10<sup>68</sup>, 10<sup>69</sup>, 10<sup>70</sup>, 10<sup>71</sup>, 10<sup>72</sup>, 10<sup>73</sup>, 10<sup>74</sup>, 10<sup>75</sup>, 10<sup>76</sup>, 10<sup>77</sup>, 10<sup>78</sup>, 10<sup>79</sup>, 10<sup>80</sup>, 10<sup>81</sup>, 10<sup>82</sup>, 10<sup>83</sup>, 10<sup>84</sup>, 10<sup>85</sup>, 10<sup>86</sup>, 10<sup>87</sup>, 10<sup>88</sup>, 10<sup>89</sup>, 10<sup>90</sup>, 10<sup>91</sup>, 10<sup>92</sup>, 10<sup>93</sup>, 10<sup>94</sup>, 10<sup>95</sup>, 10<sup>96</sup>, 10<sup>97</sup>, 10<sup>98</sup>, 10<sup>99</sup>, 10<sup>100</sup>, 10<sup>101</sup>, 10<sup>102</sup>, 10<sup>103</sup>, 10<sup>104</sup>, 10<sup>105</sup>, 10<sup>106</sup>, 10<sup>107</sup>, 10<sup>108</sup>, 10<sup>109</sup>, 10<sup>110</sup>, 10<sup>111</sup>, 10<sup>112</sup>, 10<sup>113</sup>, 10<sup>114</sup>, 10<sup>115</sup>, 10<sup>116</sup>, 10<sup>117</sup>, 10<sup>118</sup>, 10<sup>119</sup>, 10<sup>120</sup>, 10<sup>121</sup>, 10<sup>122</sup>, 10<sup>123</sup>, 10<sup>124</sup>, 10<sup>125</sup>, 10<sup>126</sup>, 10<sup>127</sup>, 10<sup>128</sup>, 10<sup>129</sup>, 10<sup>130</sup>, 10<sup>131</sup>, 10<sup>132</sup>, 10<sup>133</sup>, 10<sup>134</sup>, 10<sup>135</sup>, 10<sup>136</sup>, 10<sup>137</sup>, 10<sup>138</sup>, 10<sup>139</sup>, 10<sup>140</sup>, 10<sup>141</sup>, 10<sup>142</sup>, 10<sup>143</sup>, 10<sup>144</sup>, 10<sup>145</sup>, 10<sup>146</sup>, 10<sup>147</sup>, 10<sup>148</sup>, 10<sup>149</sup>, 10<sup>150</sup>, 10<sup>151</sup>, 10<sup>152</sup>, 10<sup>153</sup>, 10<sup>154</sup>, 10<sup>155</sup>, 10<sup>156</sup>, 10<sup>157</sup>, 10<sup>158</sup>, 10<sup>159</sup>, 10<sup>160</sup>, 10<sup>161</sup>, 10<sup>162</sup>, 10<sup>163</sup>, 10<sup>164</sup>, 10<sup>165</sup>, 10<sup>166</sup>, 10<sup>167</sup>, 10<sup>168</sup>, 10<sup>169</sup>, 10<sup>170</sup>, 10<sup>171</sup>, 10<sup>172</sup>, 10<sup>173</sup>, 10<sup>174</sup>, 10<sup>175</sup>, 10<sup>176</sup>, 10<sup>177</sup>, 10<sup>178</sup>, 10<sup>179</sup>, 10<sup>180</sup>, 10<sup>181</sup>, 10<sup>182</sup>, 10<sup>183</sup>, 10<sup>184</sup>, 10<sup>185</sup>, 10<sup>186</sup>, 10<sup>187</sup>, 10<sup>188</sup>, 10<sup>189</sup>, 10<sup>190</sup>, 10<sup>191</sup>, 10<sup>192</sup>, 10<sup>193</sup>, 10<sup>194</sup>, 10<sup>195</sup>, 10<sup>196</sup>, 10<sup>197</sup>, 10<sup>198</sup>, 10<sup>199</sup>, 10<sup>200</sup>, 10<sup>201</sup>, 10<sup>202</sup>, 10<sup>203</sup>, 10<sup>204</sup>, 10<sup>205</sup>, 10<sup>206</sup>, 10<sup>207</sup>, 10<sup>208</sup>, 10<sup>209</sup>, 10<sup>210</sup>, 10<sup>211</sup>, 10<sup>212</sup>, 10<sup>213</sup>, 10<sup>214</sup>, 10<sup>215</sup>, 10<sup>216</sup>, 10<sup>217</sup>, 10<sup>218</sup>, 10<sup>219</sup>, 10<sup>220</sup>, 10<sup>221</sup>, 10<sup>222</sup>, 10<sup>223</sup>, 10<sup>224</sup>, 10<sup>225</sup>, 10<sup>226</sup>, 10<sup>227</sup>, 10<sup>228</sup>, 10<sup>229</sup>, 10<sup>230</sup>, 10<sup>231</sup>, 10<sup>232</sup>, 10<sup>233</sup>, 10<sup>234</sup>, 10<sup>235</sup>, 10<sup>236</sup>, 10<sup>237</sup>, 10<sup>238</sup>, 10<sup>239</sup>, 10<sup>240</sup>, 10<sup>241</sup>, 10<sup>242</sup>, 10<sup>243</sup>, 10<sup>244</sup>, 10<sup>245</sup>, 10<sup>246</sup>, 10<sup>247</sup>, 10<sup>248</sup>, 10<sup>249</sup>, 10<sup>250</sup>, 10<sup>251</sup>, 10<sup>252</sup>, 10<sup>253</sup>, 10<sup>254</sup>, 10<sup>255</sup>, 10<sup>256</sup>, 10<sup>257</sup>, 10<sup>258</sup>, 10<sup>259</sup>, 10<sup>260</sup>, 10<sup>261</sup>, 10<sup>262</sup>, 10<sup>263</sup>, 10<sup>264</sup>, 10<sup>265</sup>, 10<sup>266</sup>, 10<sup>267</sup>, 10<sup>268</sup>, 10<sup>269</sup>, 10<sup>270</sup>, 10<sup>271</sup>, 10<sup>272</sup>, 10<sup>273</sup>, 10<sup>274</sup>, 10<sup>275</sup>, 10<sup>276</sup>, 10<sup>277</sup>, 10<sup>278</sup>, 10<sup>279</sup>, 10<sup>280</sup>, 10<sup>281</sup>, 10<sup>282</sup>, 10<sup>283</sup>, 10<sup>284</sup>, 10<sup>285</sup>, 10<sup>286</sup>, 10<sup>287</sup>, 10<sup>288</sup>, 10<sup>289</sup>, 10<sup>290</sup>, 10<sup>291</sup>, 10<sup>292</sup>, 10<sup>293</sup>, 10<sup>294</sup>, 10<sup>295</sup>, 10<sup>296</sup>, 10<sup>297</sup>, 10<sup>298</sup>, 10<sup>299</sup>, 10<sup>300</sup>, 10<sup>301</sup>, 10<sup>302</sup>, 10<sup>303</sup>, 10<sup>304</sup>, 10<sup>305</sup>, 10<sup>306</sup>, 10<sup>307</sup>, 10<sup>308</sup>, 10<sup>309</sup>, 10<sup>310</sup>, 10<sup>311</sup>, 10<sup>312</sup>, 10<sup>313</sup>, 10<sup>314</sup>, 10<sup>315</sup>, 10<sup>316</sup>, 10<sup>317</sup>, 10<sup>318</sup>, 10<sup>319</sup>, 10<sup>320</sup>, 10<sup>321</sup>, 10<sup>322</sup>, 10<sup>323</sup>, 10<sup>324</sup>, 10<sup>325</sup>, 10<sup>326</sup>, 10<sup>327</sup>, 10<sup>328</sup>, 10<sup>329</sup>, 10<sup>330</sup>, 10<sup>331</sup>, 10<sup>332</sup>, 10<sup>333</sup>, 10<sup>334</sup>, 10<sup>335</sup>, 10<sup>336</sup>, 10<sup>337</sup>, 10<sup>338</sup>, 10<sup>339</sup>, 10<sup>340</sup>, 10<sup>341</sup>, 10<sup>342</sup>, 10<sup>343</sup>, 10<sup>344</sup>, 10<sup>345</sup>, 10<sup>346</sup>, 10<sup>347</sup>, 10<sup>348</sup>, 10<

007723 66920500

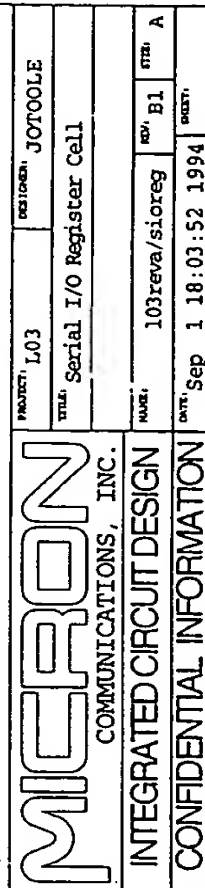
MI40-030

7.140101AA

7.140101AB

II II II II II II II II II II

Fig. 7.140101



NOTE: Databus ends here.  
Use standard databus pitch.

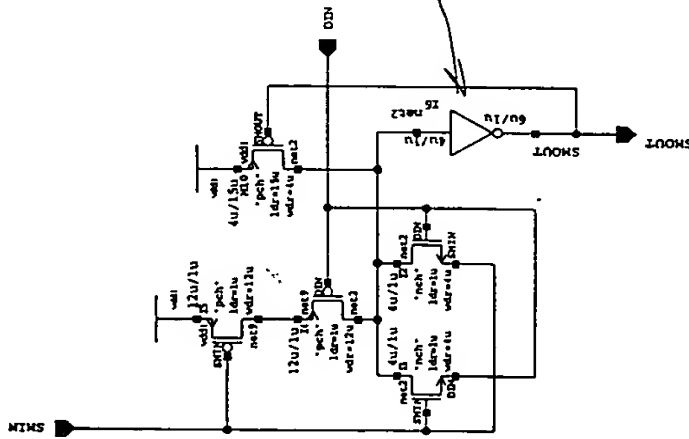


Fig. 7.140102

PROJECT: L03		DESIGN: JOTOOLE	
TITLE: SIO XOR			
NAME: 103reva/sioxor		REV: B1	SIZE: A
DATE: Sep 1 18:07:22 1994		EJECT:	

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

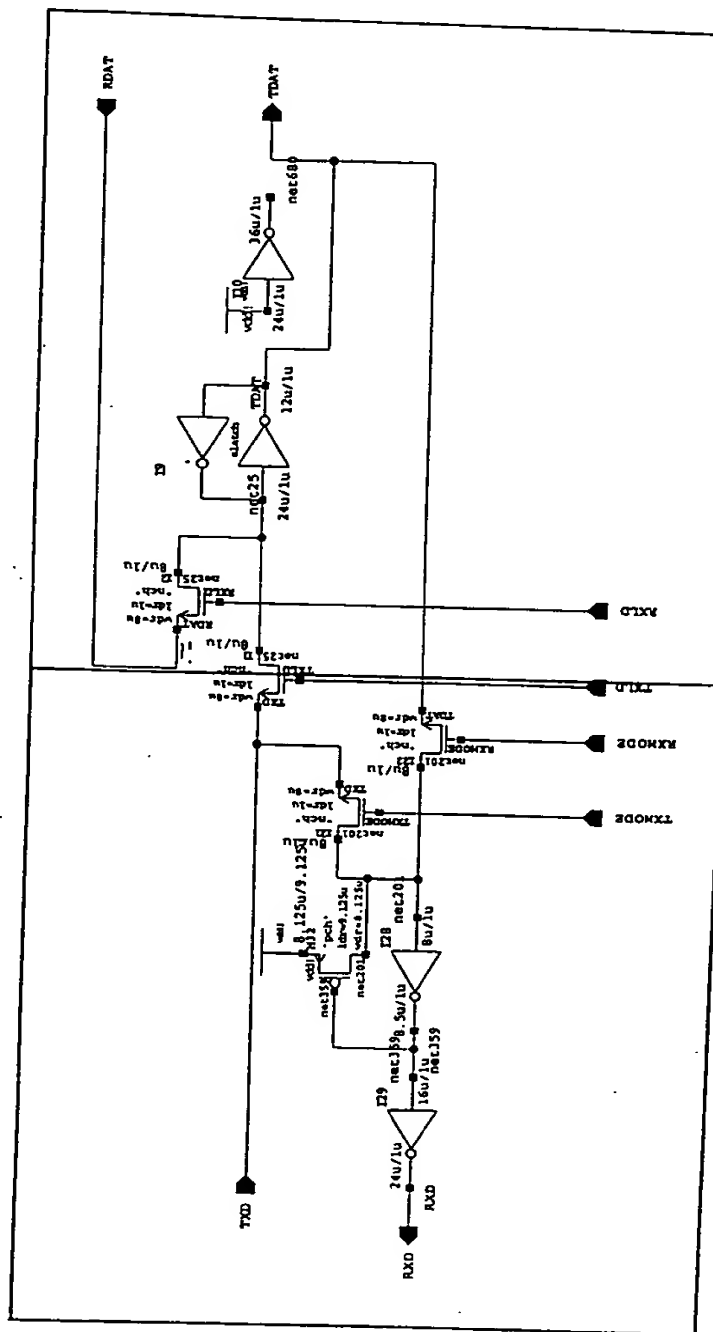
007720 20920500

MI40-030

7.140103AA	7.140103AB
------------	------------

EX 07 11.11.11.11.11

Fig. 7.140103



**B11: inverted bit**

# NOIR

INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

PROJECT: L03	DESIGNER: JOTOOLE
--------------	-------------------

STO Bidirectional Latch

103reva/siobdlat_inv	AS/ B11	size A
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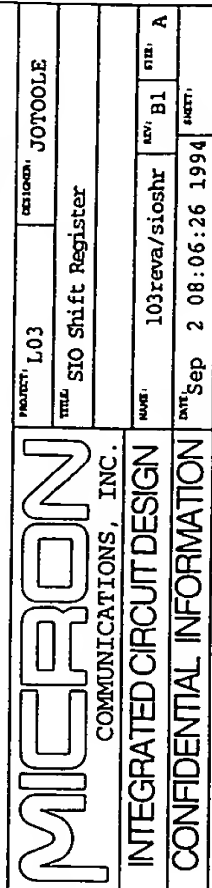
DATE:	Apr 10 15:13:59 1996	PORT:
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7.140104AA	7.140104AB
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Πολύτιμοι, Ευχαριστώ



FIG. 7.140004



**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

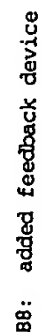
0014120" 00900500

MI40-030

7.140105AA	7.140105AB
------------	------------

II II 7.140105

Fig. 7.140105



MICRON		PROJECT: L03	DESIGN: J0700LE
COMMUNICATIONS, INC.		TITLE: STO Bidirectional Latch	
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
NAME:	103reva/siobdlat	REV:	B8
DATE:	Jan 8 11:04:57 1996	SIZE:	A

2007.11.16

**THE UNIVERSITY OF CHICAGO**



00: add input to the path  
 01: remove input from path  
 02: add input to the path  
 03: remove input from path  
 04: add input to the path  
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 06: add input to the path  
 07: remove input from path  
 08: add input to the path  
 09: remove input from path  
 10: add input to the path  
 11: remove input from path  
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 14: add input to the path  
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 56: add input to the path  
 57: remove input from path  
 58: add input to the path  
 59: remove input from path  
 60: add input to the path  
 61: remove input from path  
 62: add input to the path  
 63: remove input from path  
 64: add input to the path  
 65: remove input from path  
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 88: add input to the path  
 89: remove input from path  
 90: add input to the path  
 91: remove input from path  
 92: add input to the path  
 93: remove input from path  
 94: add input to the path  
 95: remove input from path  
 96: add input to the path  
 97: remove input from path  
 98: add input to the path  
 99: remove input from path

**MICRON**  
COMMERCIAL DESIGN, INC.  
INTEGRATED C-FOOT DESIGN  
CONFIDENTIAL INFORMATION  
CONFIDENTIAL INFORMATION

DATE	7-2-81	DATE	7-2-81
BY	Pat. Int. Admin.	BY	Pat. Int. Admin.
FILED	7-2-81	FILED	7-2-81

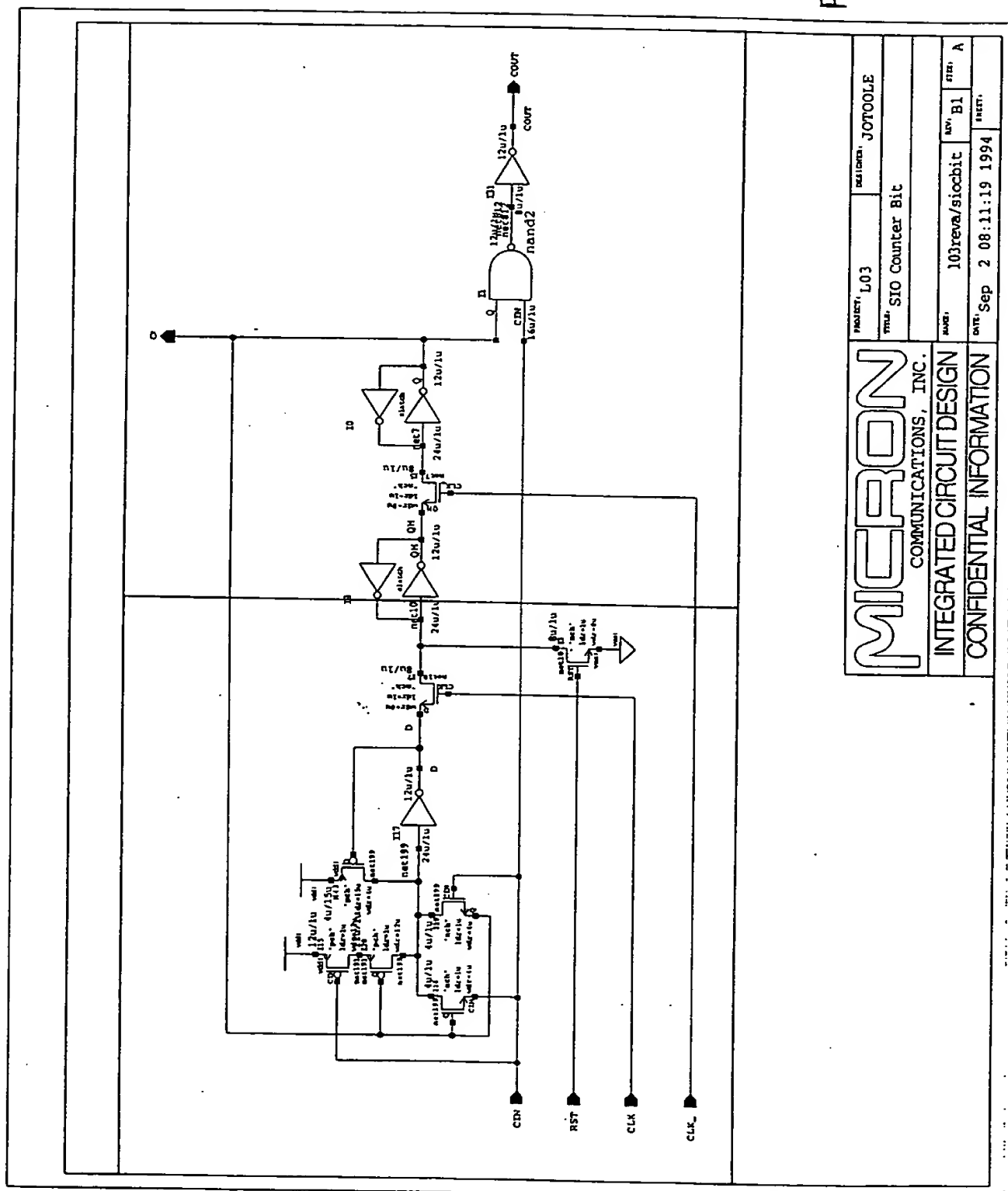
001420 26920500

MI40-030

7.140201AA	7.140201AB
------------	------------

EX 11.140201

201  
FIG. 7.190004



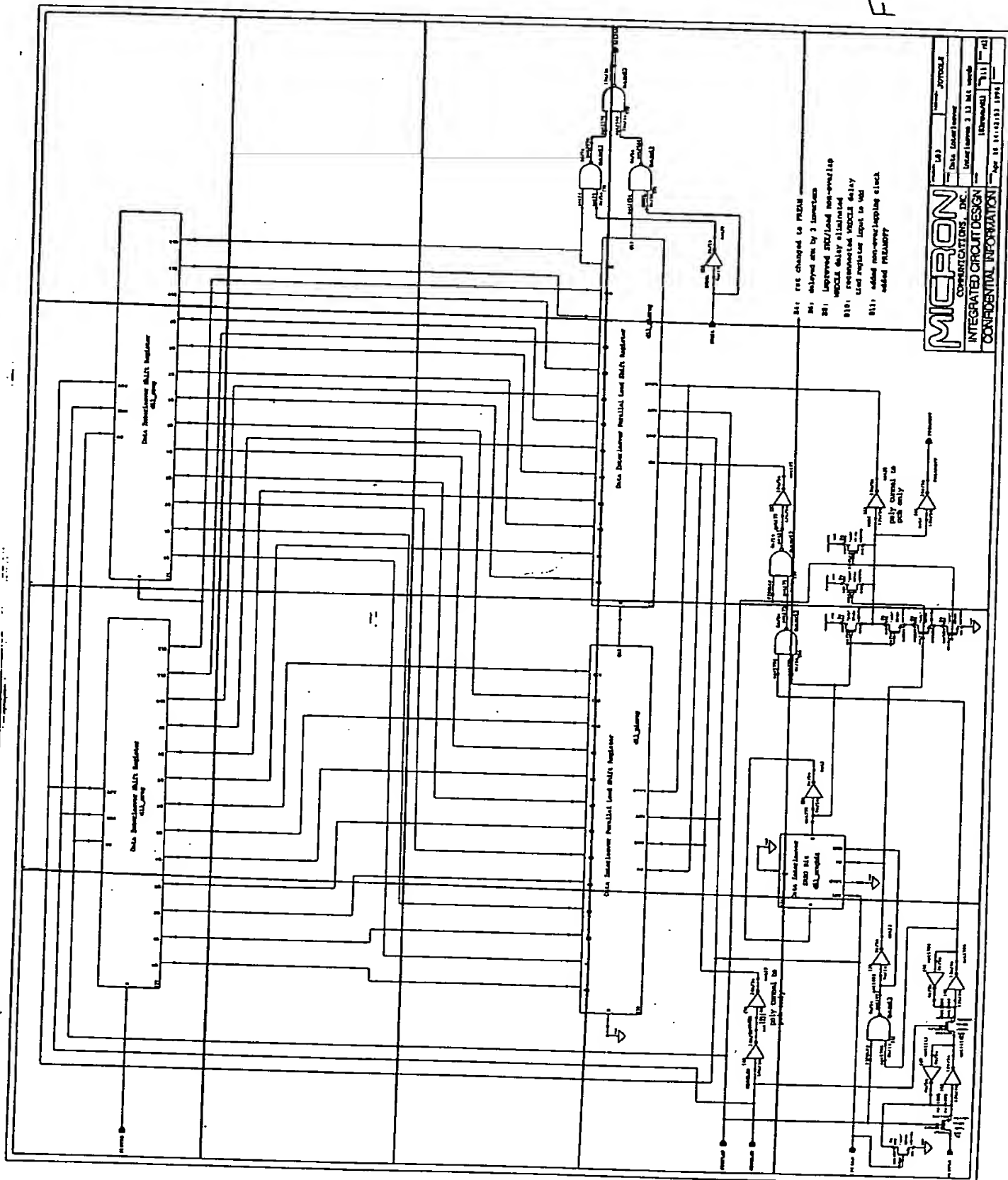
三

7.15AA	7.15AB	7.15AC	7.15AD
7.15BA	7.15BB	7.15BC	7.15BD
7.15CA	7.15CB	7.15CC	7.15CD
7.15DA	7.15DB	7.15DC	7.15DD
7.15EA	7.15EB	7.15EC	



004720-20300000

Fig. 7.15



- B1: EE changed to P10M
- B2: delayed and by 2 times
- B3: improved DQ/Load non-sampling
- B10: reclocked delay eliminated
- B11: added non-sampling clock
- B12: added PREAUX

MICRON	
CORPORATION, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	20700A
Device	Data Input/Output Register
Unit Load	3.3 Mbit/sec
Package	16-pin DIP
Temp. Range	-55 to +125°C

7.1501AA
7.1501BA
7.1501CA

*7.1501BA*

7.1501CA

**SECRET**

004420-01400

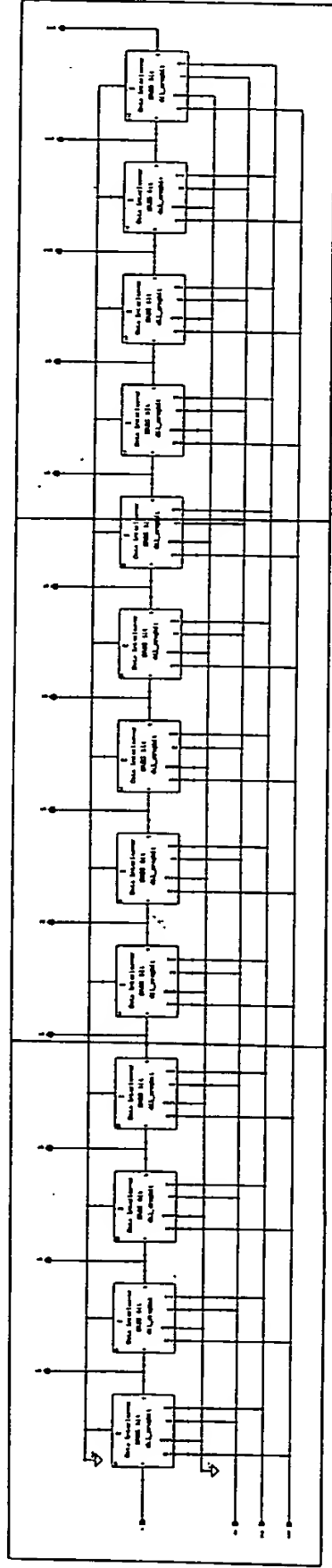


Fig. 7.1501

7.1502AA

7.1502BA

7.1502CA

Fig 7.1502

00502603 : 004400  
007400 : 009200

00440: 60900000

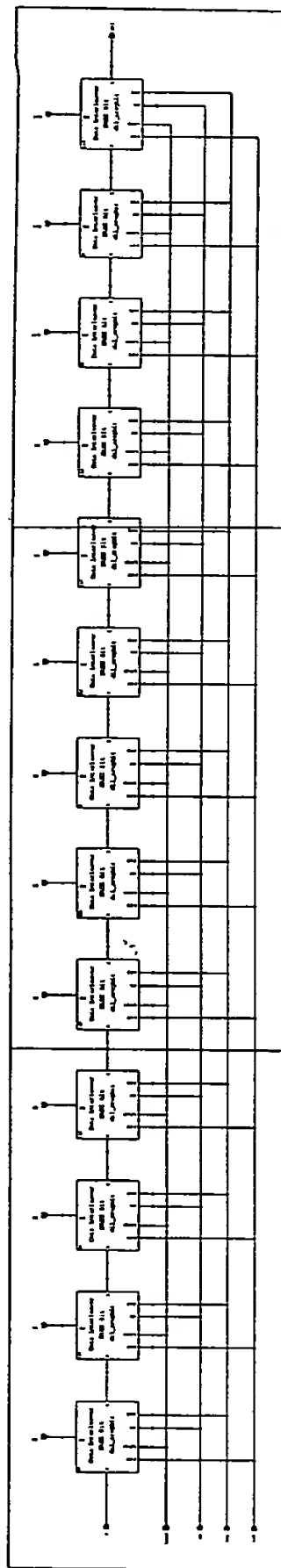
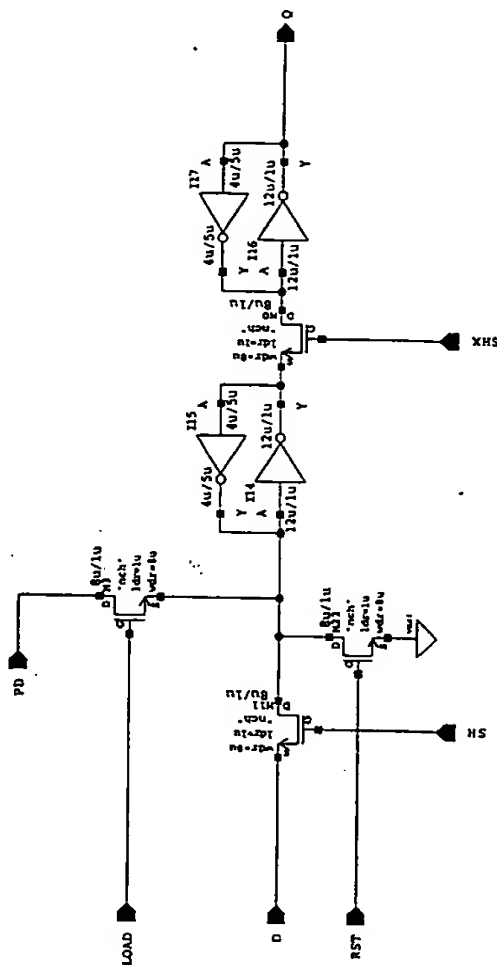


Fig. 7.1502

MICRON	
INTEGRATED CIRCUIT (IC)	
CONFIDENTIAL INFORMATION	
Part No.	71502
Rev.	1.0
Doc. No.	71502-1.0
Rev. Description	Initial Release
Rev. Date	1971-11-15

Fig. 7.150201

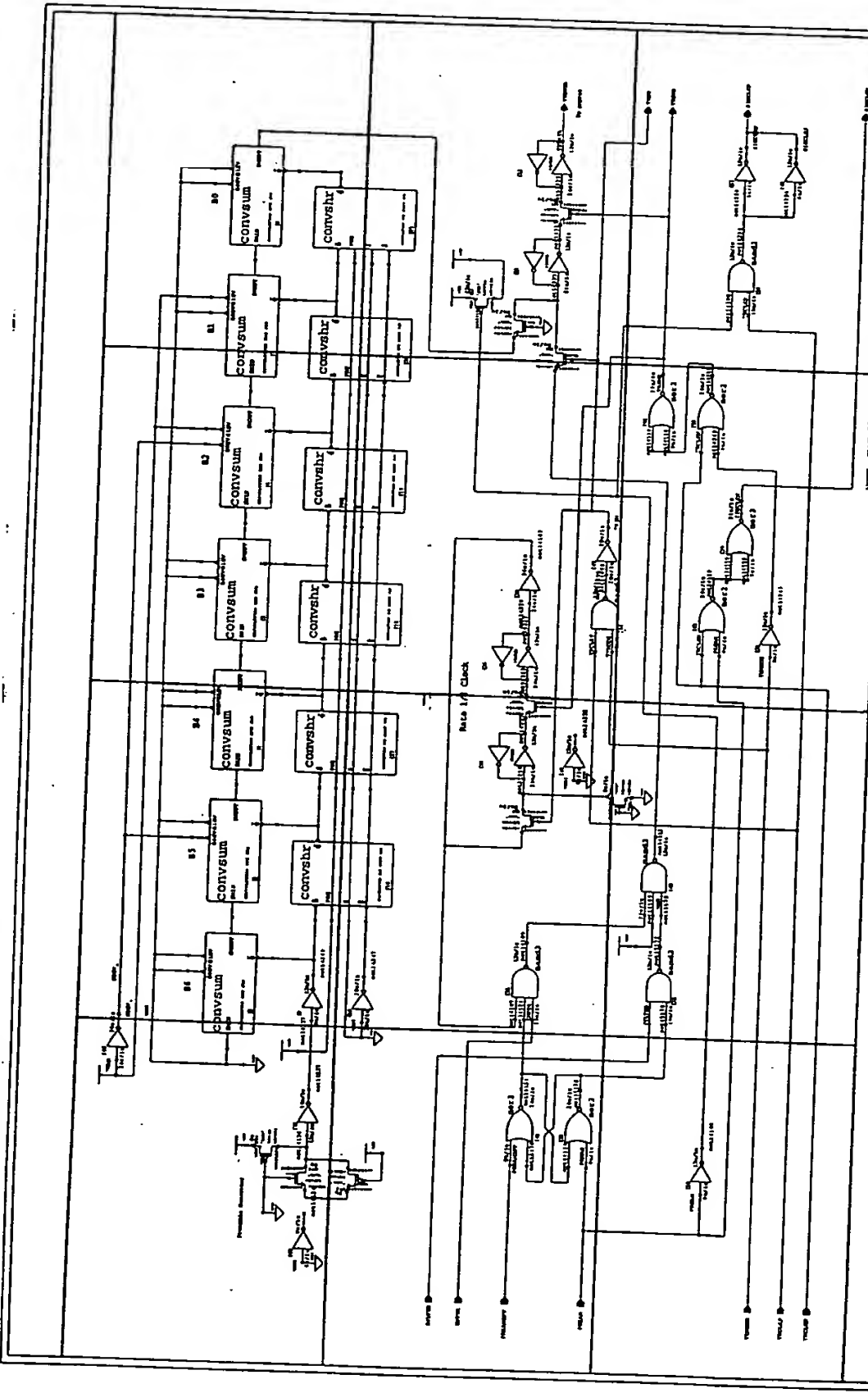


<b>MICRON</b>		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Data Interleaver Shift	
INTEGRATED CIRCUIT DESIGN		Register Bit	
CONFIDENTIAL INFORMATION		WAVE: 103reva/dil_sregbit	REV: B1
		DATE: Sep 27 10:25:07 1994	SIZE: A
		SHEET: 1	

7.16AA	7.16AB	7.16AC	7.16AD
7.16BA	7.16BB	7.16BC	7.16BD
7.16CA	7.16CB	7.16CC	7.16CD

SECRET

Fig. 7.16



NOTE: SOME FITS AS SHOWN  
HORIZES ROUTE ON STOOD  
AND STOOD.  
ALL CONTROL STOODS ROUTE  
TO #10 EXCEPT TUCK (FROM PREAMBLE).

ELL: needed to provide 100% preamble  
and non-overlapping bit rate clocks  
Note: computational encoding is disabled

<b>MICRON</b>	
143	JOTODLE
Preamble Generator and Bit Rate	
Clock Generator	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Apr 18 16:12:13 1971	



SECRET

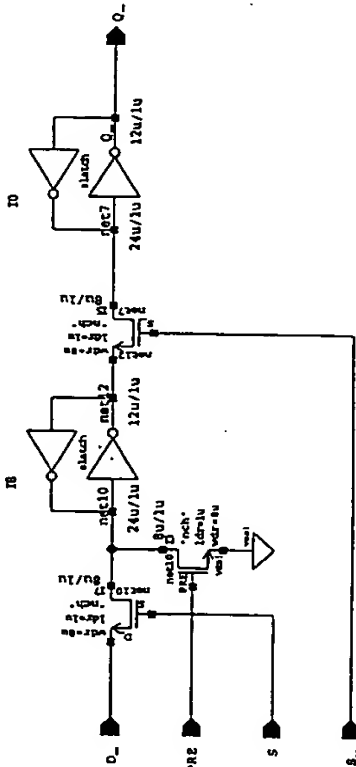


Fig. 7.1601

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: Convolutional Encoder Shift		REGISTER: 103reva/convshr	
REGISTER: 103reva/convshr		DATE: Sep 2 10:34:27 1994	
REGISTER: 103reva/convshr		PAGE: A	
REGISTER: 103reva/convshr		PAGE: A	
REGISTER: 103reva/convshr		PAGE: A	

MICRON COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

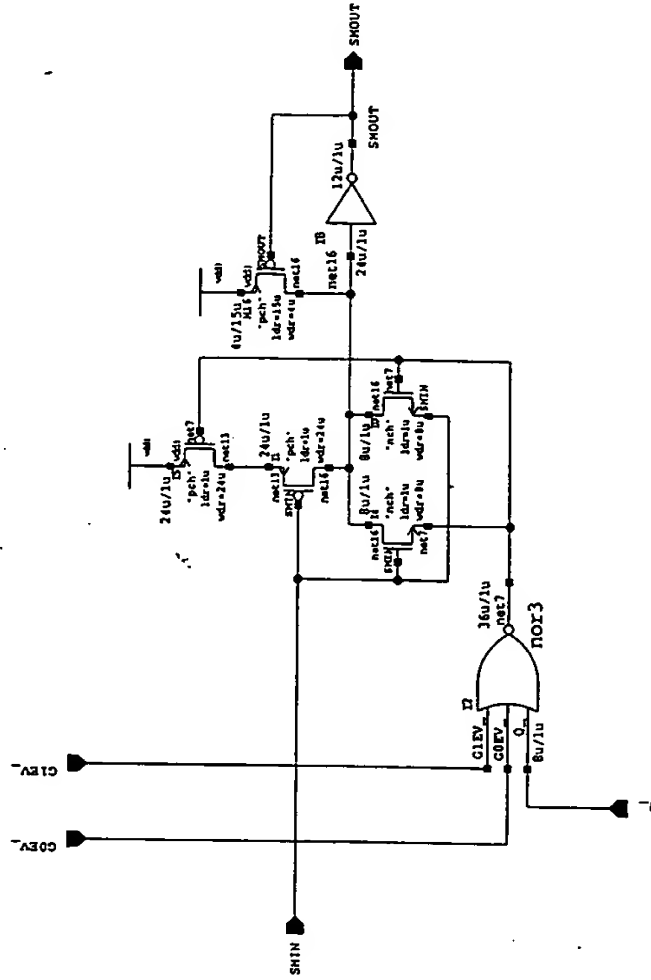


Fig. 7.1602

PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Convolutional Encoder Summer		DATE: 103rava/convsum	
NAME: 103rava/convsum		REV: B1	
DATE: Sep 2 10:32:17 1994		PAGE: A	

MICRON

COMMUNICATIONS, INC.

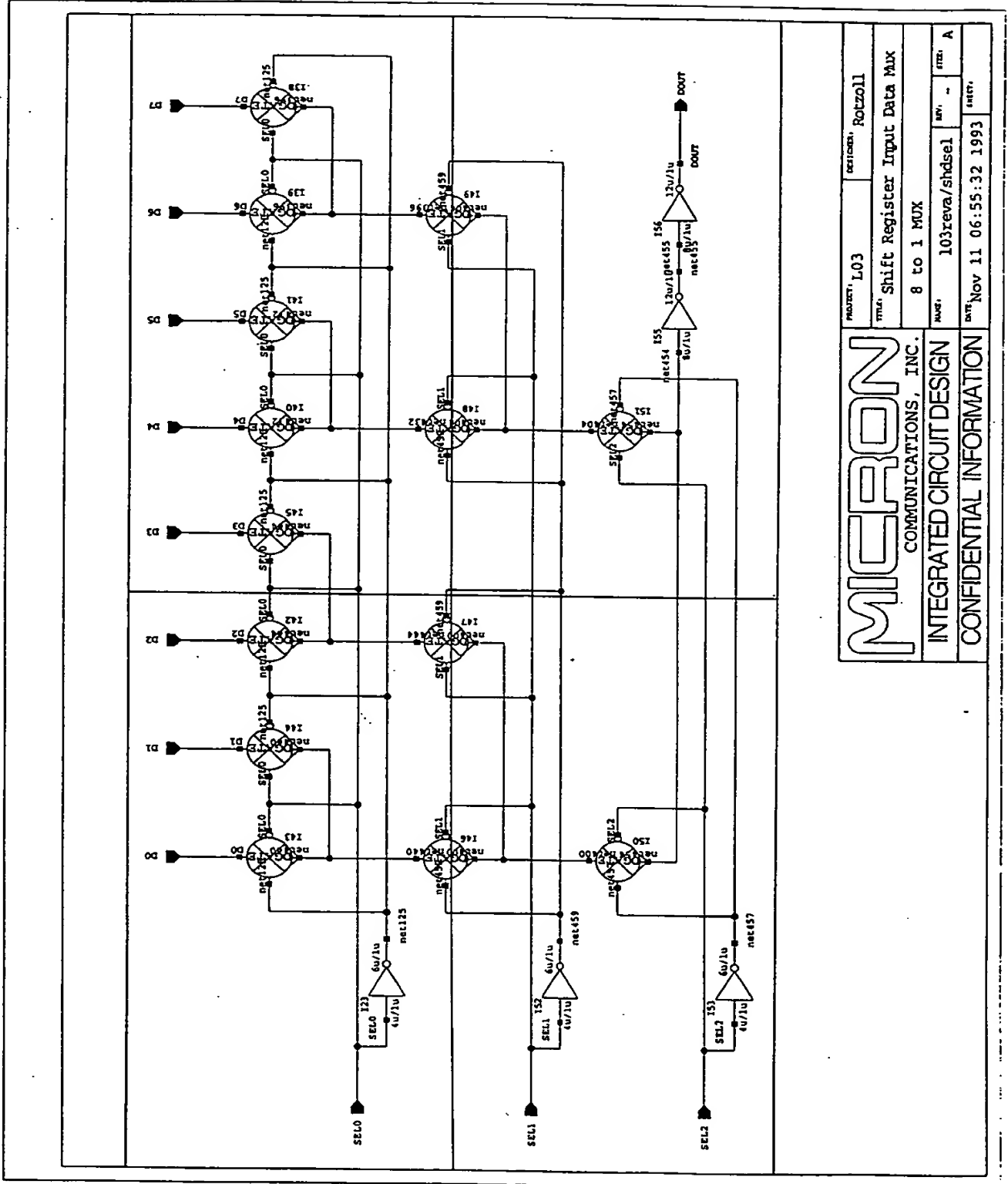
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

7.17AA	7.17AB
7.17BA	7.17BB

004720 00302000

Fig. 7.17



7.18AA	7.18AB	7.18AC
7.18BA	7.18BB	7.18BC
7.18CA	7.18CB	7.18CC

001400-00000000

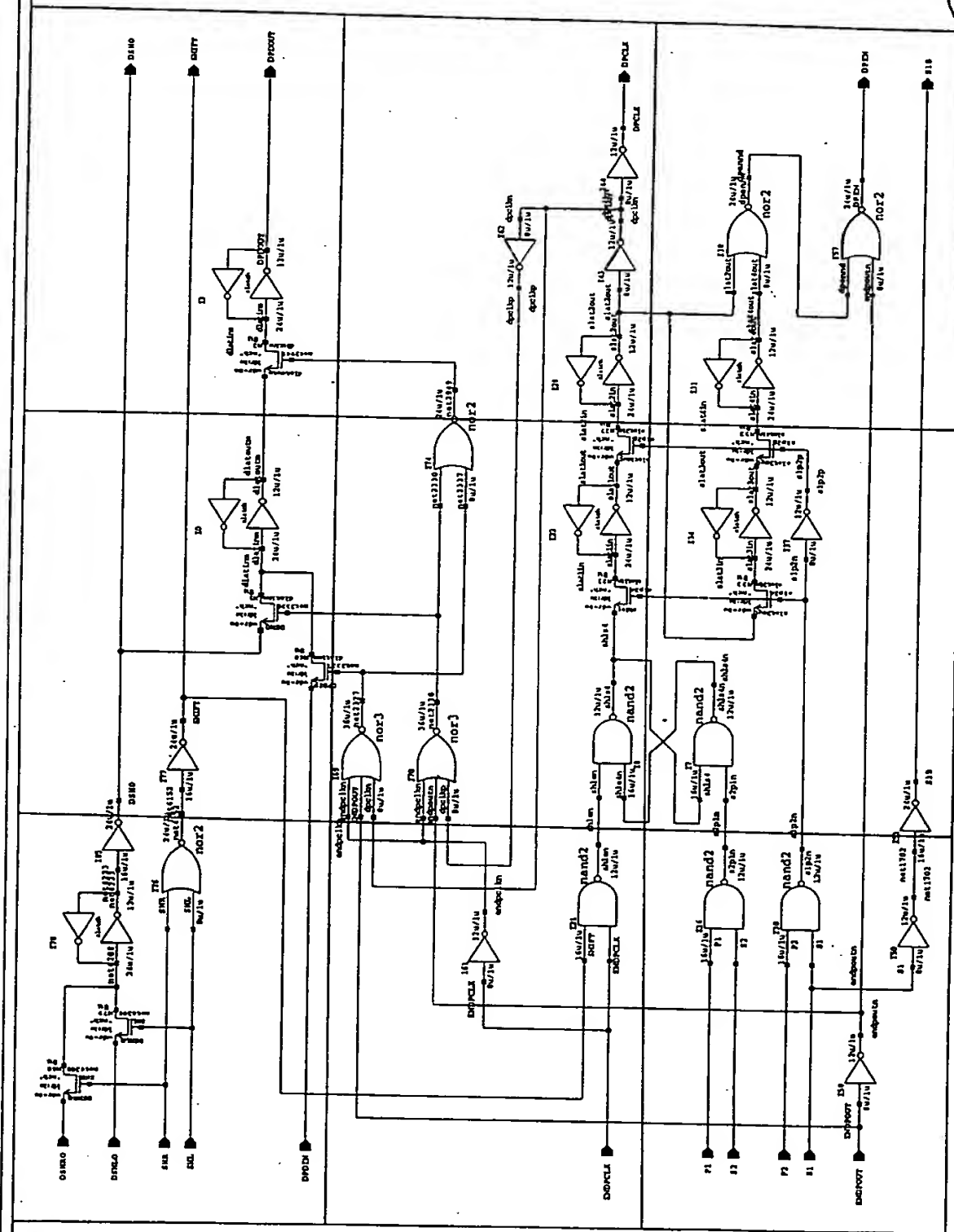
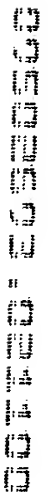


Fig. 7.18

MICRON		PROJECT: L03	REVISION: R000011
COMMUNICATIONS, INC.		Title: Digital Port Output Controller	
INTEGRATED CIRCUIT DESIGN		DATE: 103revs/doutport	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 12 10:05:40 1993	REV: rdl

*(The page contains faint, illegible markings or bleed-through from the reverse side.)*





8.01AA	8.01AB	8.01AC	8.01AD	8.01AE
8.01BA	8.01BB	8.01BC	8.01BD	8.01BE
8.01CA	8.01CB	8.01CC	8.01CD	8.01CE
8.01DA	8.01DB	8.01DC	8.01DD	8.01DE

001400 00300000

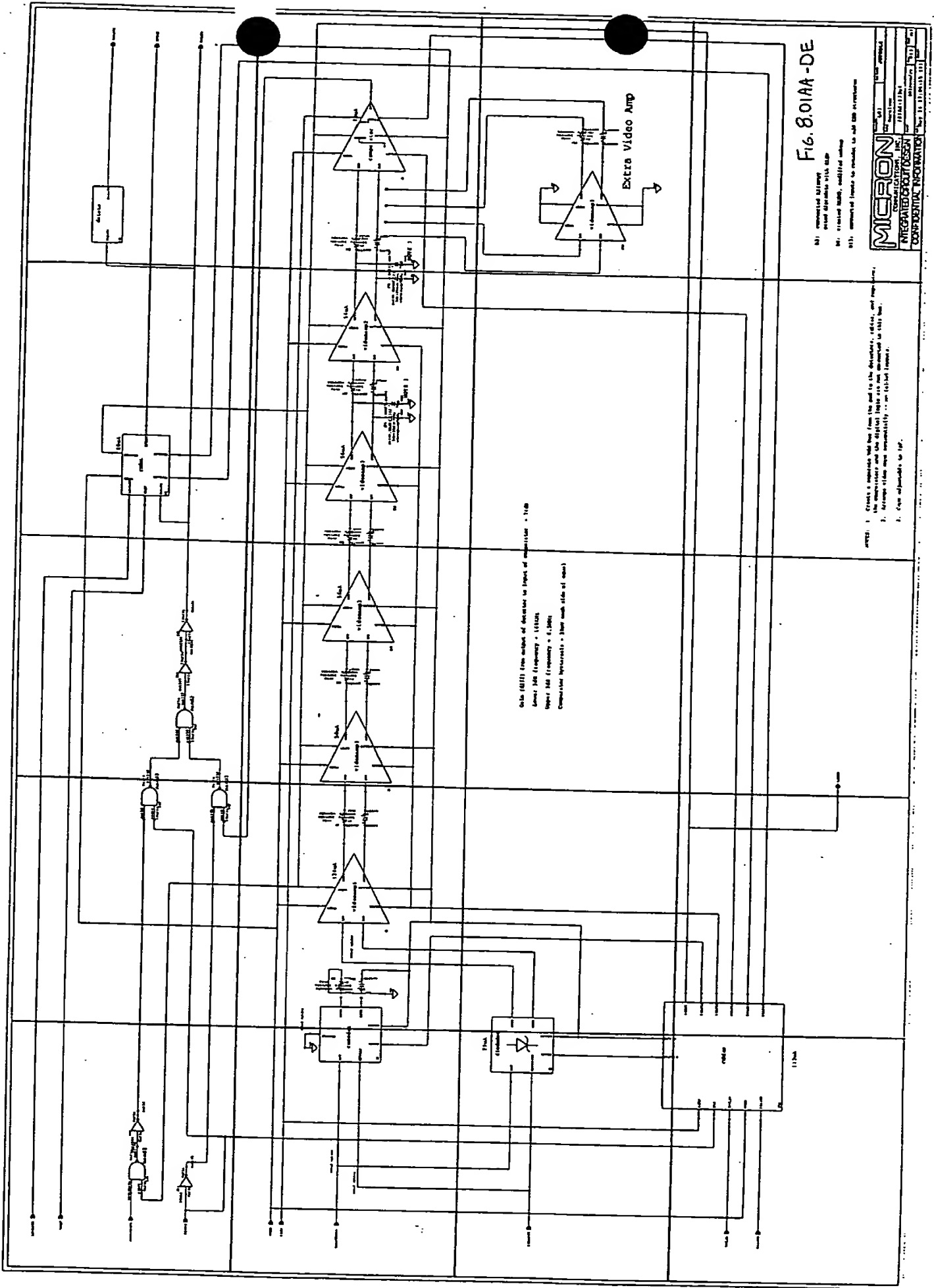


Fig. 8.01AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

801AA-DE

И П В. И. П.

00740-6030303

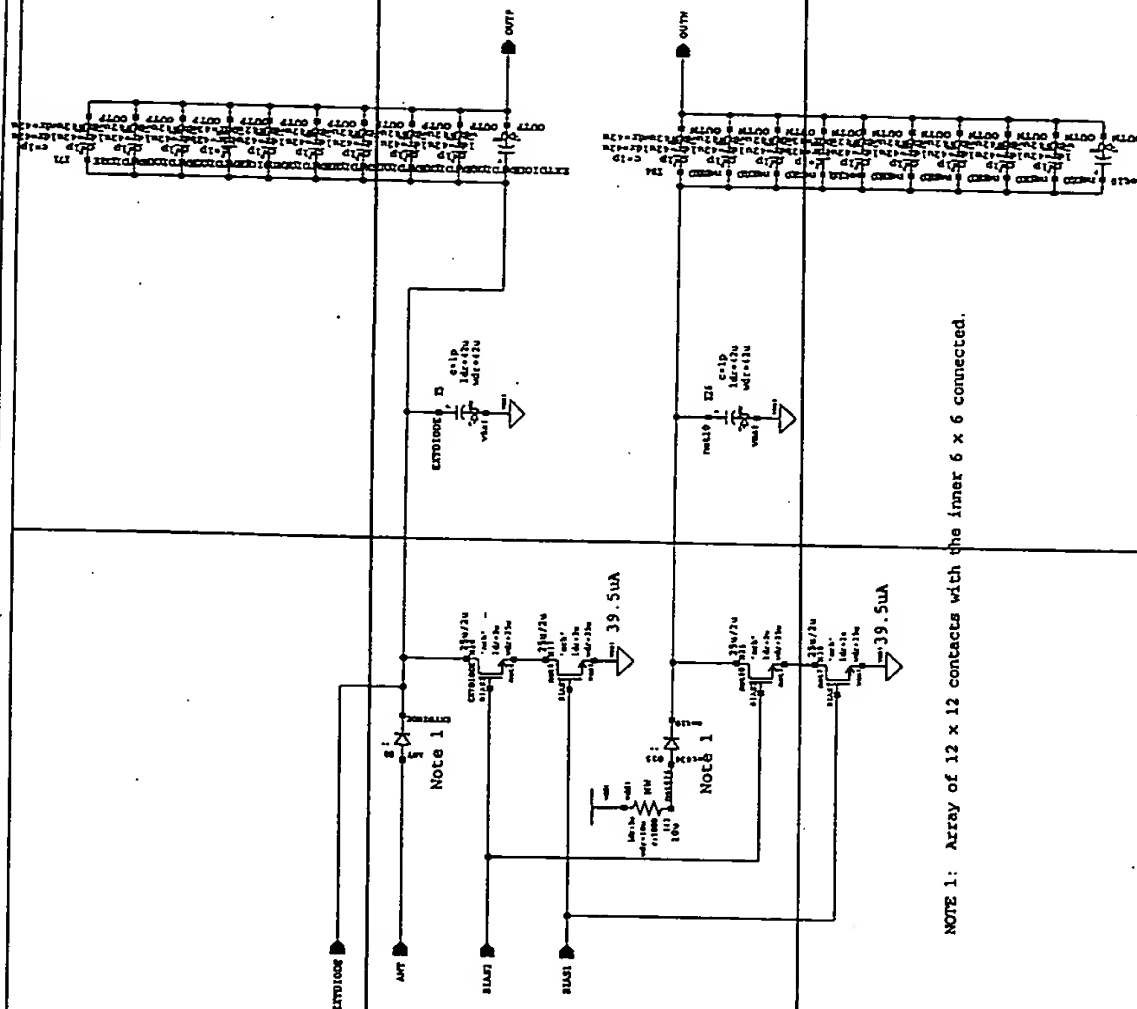


FIG. 8.0101AA-CB

MICRON		PROJECT: L03	REV: 001	J0700LE
COMMUNICATIONS, INC.		Schottky diode Detector		
INTEGRATED CIRCUIT DESIGN		IBIAS=79uA		
CONFIDENTIAL INFORMATION		DATE: 10/rev/diodeset	BY: B13	DATE: May 24 13:54:28 1996

- B2: connected EXTIOIDE line  
B6: schottky array changed to 6x6  
rf cap reduced to 1pF  
B8: increased Cc to 10pF; decreased Crf to 1pF  
B13: added 1K resistor in series with dummy diode for ESD

8.0102AA	8.0102AB	8.0102AC	8.0102AD
8.0102BA	8.0102BB	8.0102BC	

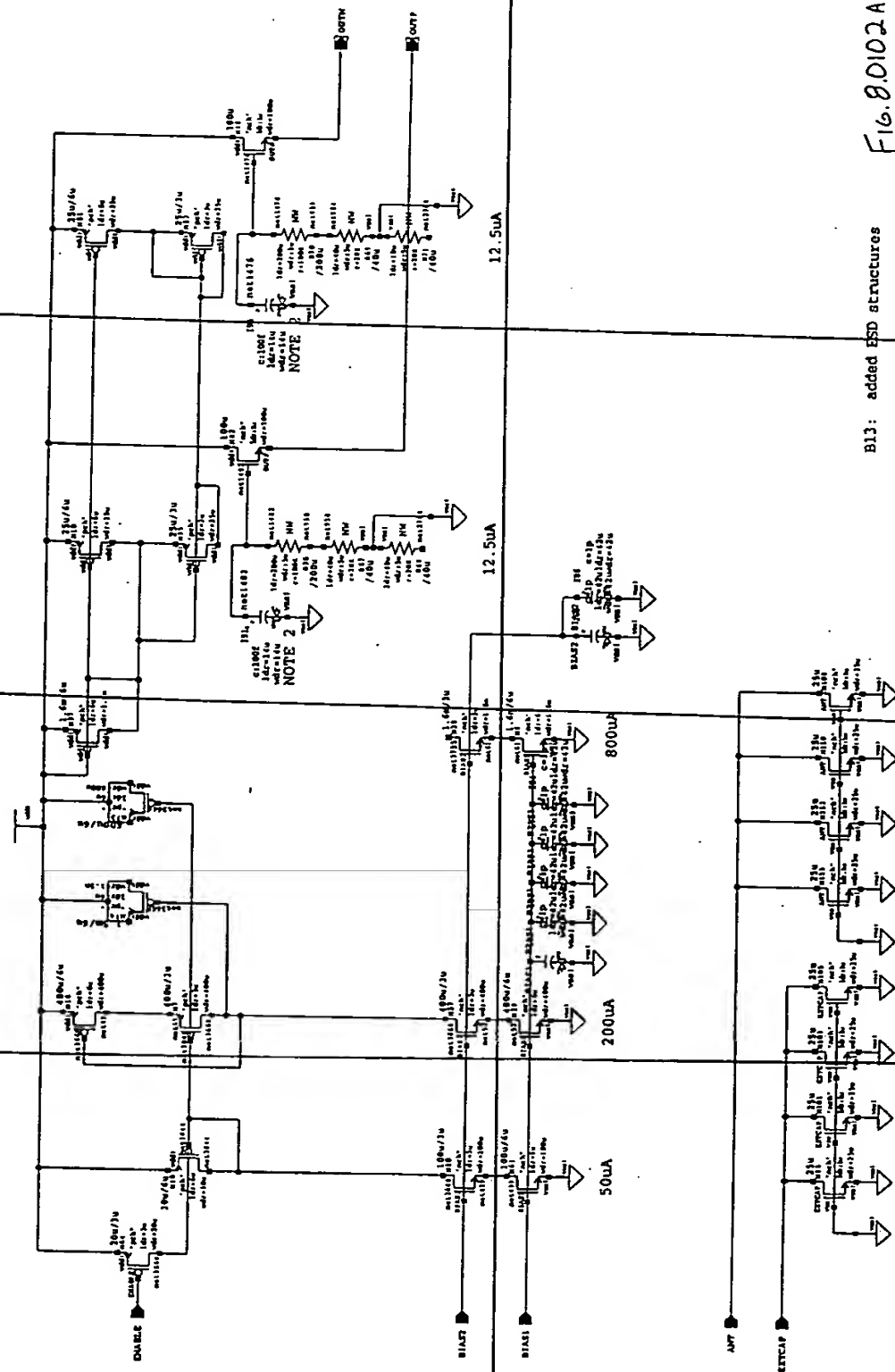
00720 000000

FIG. 8.0102 AA-BC

B13: added ESD structures

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	REVISION: J0700LE
TITLE: CHOS Square Law Detector	
IBIAS=1.175mA	
103rova/cmosdet	B13
May 25 14:10:58 1996	rdl

- NOTES: 1. All devices with W's > 100 are to be laid out with 100u segments.  
2. Cap adjustable to 300kF (23/23)



ЕВРОПЕЙСКИ

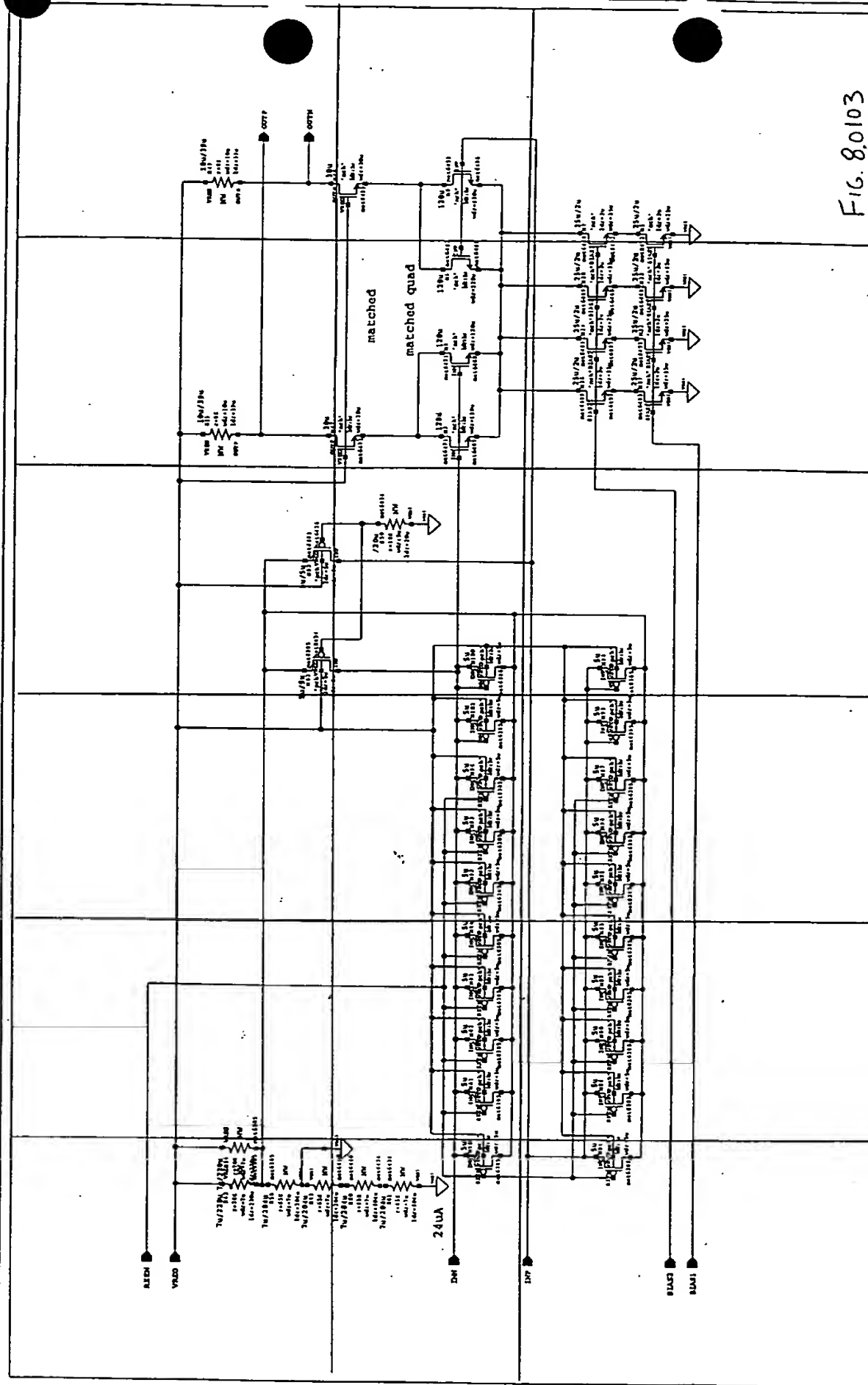


FIG. 8.0103

MICRON		PROJECT: L03	REVISION: J070010E
INTEGRATED CIRCUIT DESIGN		Title: Video Amplifier 1, B10a=124uA	
CONFIDENTIAL INFORMATION		Av(dB)=4.8 (13.7dB)	
NAME	103reva/videoamp1	SPR	RD1
DATE	Dec 16 13:40:53 1999		

- NOTES:
1. resistors are to be layed out at 50/10 and adjusted down to 39/10 with n+
  2. p-channels are adjustable up to 5/100; adjustment is on source side
- B8: Added quick bias transistors  
Reduced L of pch bias devices



00700000000000000000

8.0104AA	8.0104AB	8.0104AC
8.0104BA	8.0104BB	8.0104BC

II II II II II II II II

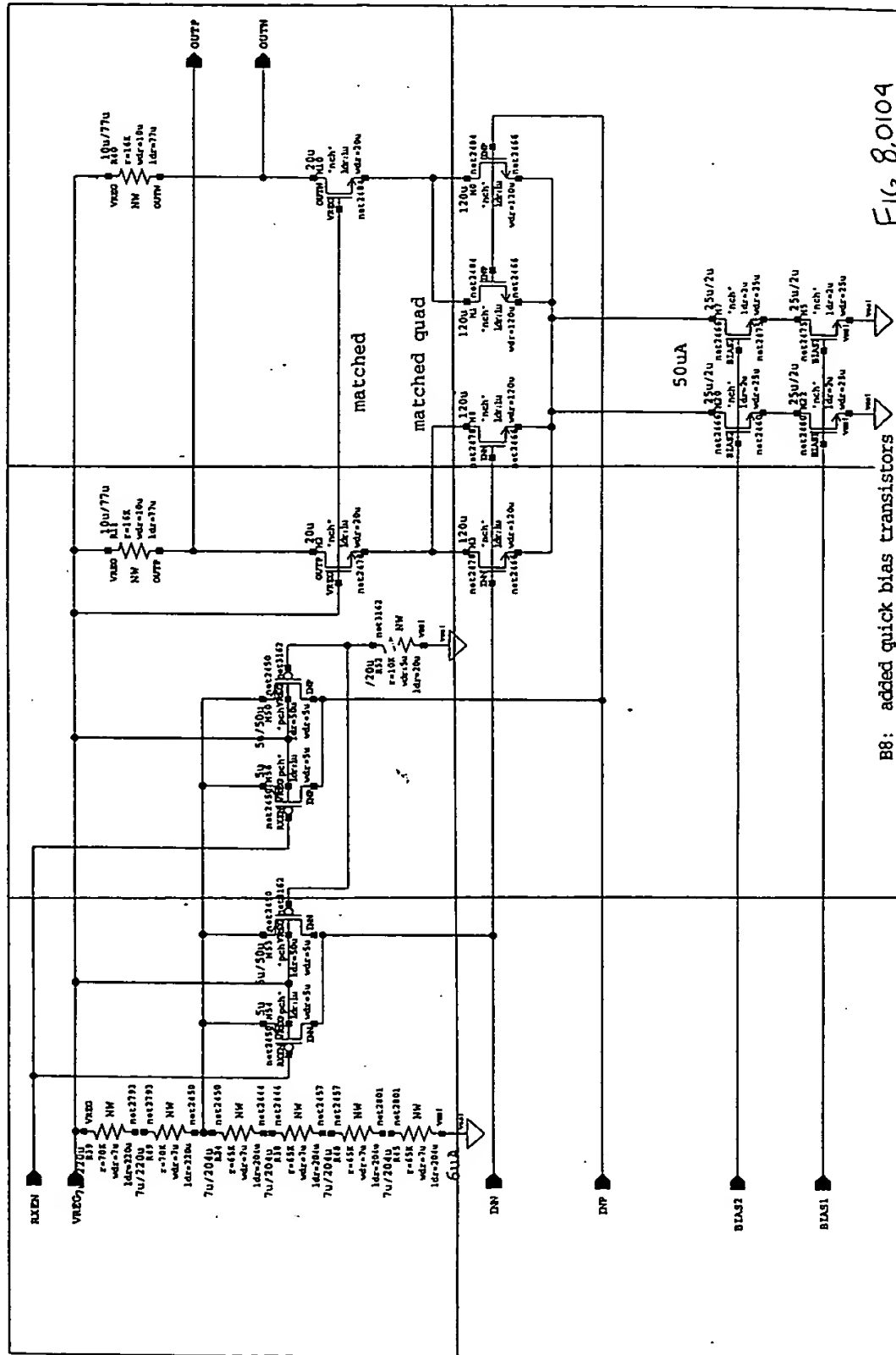


FIG 8.0104

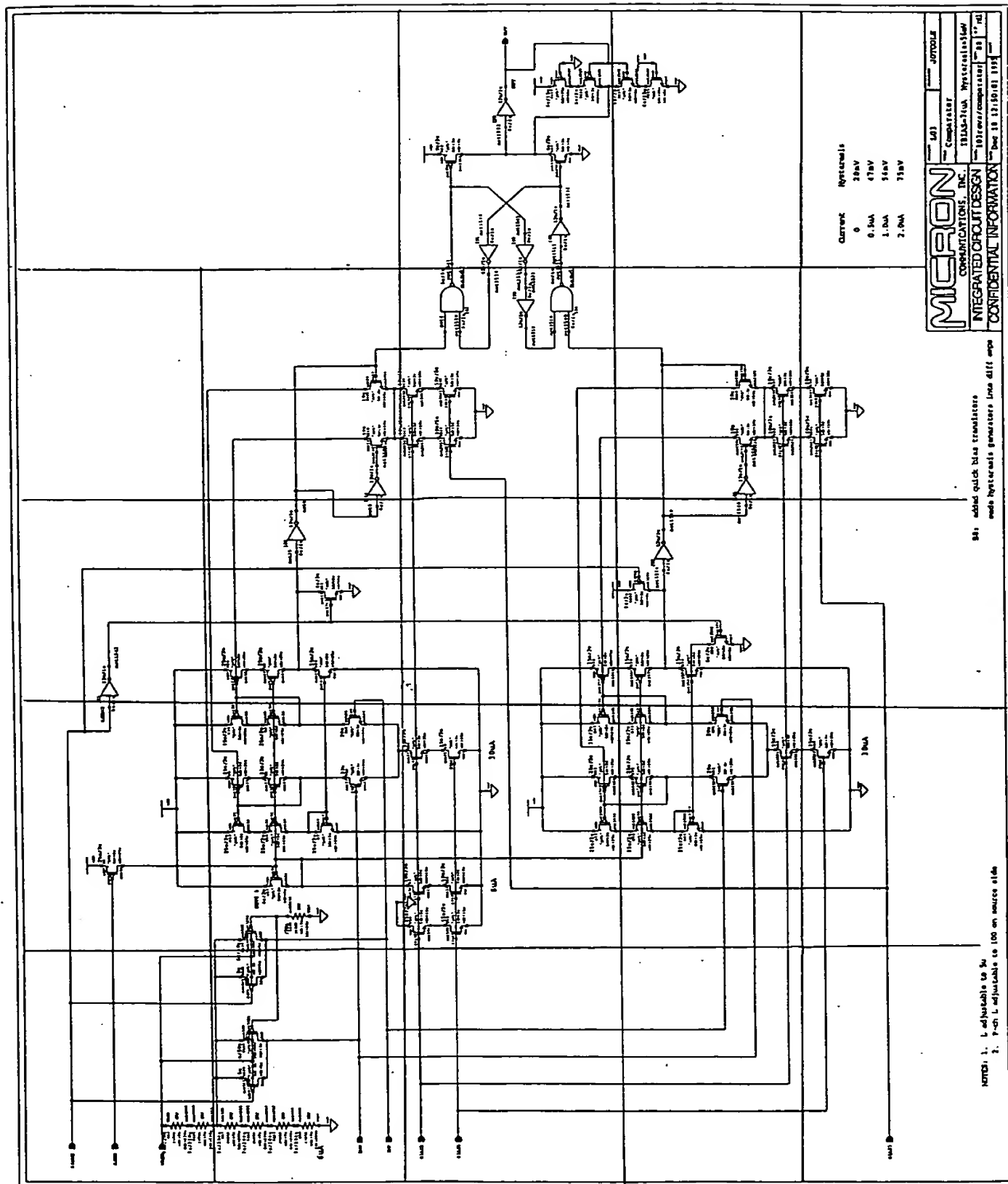
B8: added quick bias transistors

- NOTES:
1. resistors are to be layed out at 100/10 and adjusted down to 77/10 with n+
  2. p-channels are adjustable up to 5/100; adjustment is on the source side.

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		Title Video Amplifier 2, IBias=56uA	
INTEGRATED CIRCUIT DESIGN		AV(diff)=5.6 (15dB)	
CONFIDENTIAL INFORMATION		NAME: 103reva/videoamp2	REV: B8
		DATE: Dec 16 13:42:25 1995	EXT: A

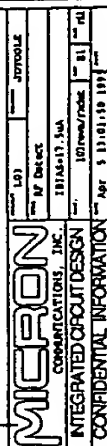
И. И. Б. 88.001.005

Fig. 8.0105



И. П. Г. 8.11.06

FIG. 8.0106



E E 8.0107

<b>MICRON</b>		DATE: 10/1/82		BY: J. J. J.	
TECHNICAL DIVISION, INC.		PROJECT: 1000		REV: 1.0	
INTEGRATED CIRCUIT DESIGN		SHEET: 1 OF 1		DATE: 10/1/82	
CONFIDENTIAL INFORMATION		DRAWING: 1000		REV: 1.0	

09. disappoint dislike dis  
 08. addict add addict  
 07. surround sur surround  
 06. measure meas measure

[illegible]



007420 6030300

8.0108AA	8.0108AB	8.0108AC
----------	----------	----------

EE. 0011000

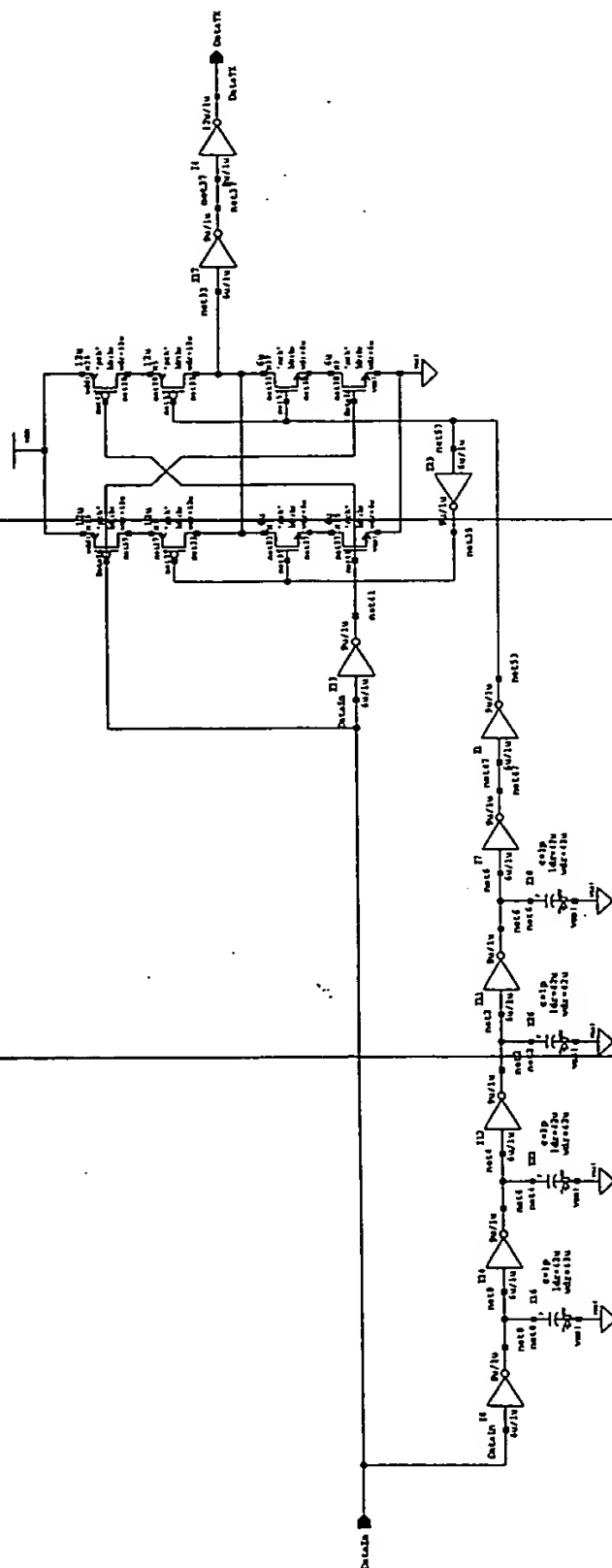
[illegible]

FIG. 8.0108

**micron**  
COMMUNICATIONS INC.

COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
part:	103 roma/databox
pn, B1	7124 ml
Output Pulse Width = 40ns (nom)	

CONFIDENTIAL INFORMATION Dec 14 17:11:15 1994

2026.02.11

# normis

B10: 1st divider stage bypassed

MICRON		PRODUCT	L03	part name	JOTFOOLLE
COMMUNICATIONS, INC.		TYPE	Low Power FLA (lab=55nA typ)		
INTEGRATED CIRCUIT DESIGN		VALUE	f1n=9.5375MHz/fout=8000Hz		
CONFIDENTIAL INFORMATION		DATE	10/28/96/lpdl		
		DATE	Mar 26 16:14:18 1996		

8.0201AB

И. П. Павлов

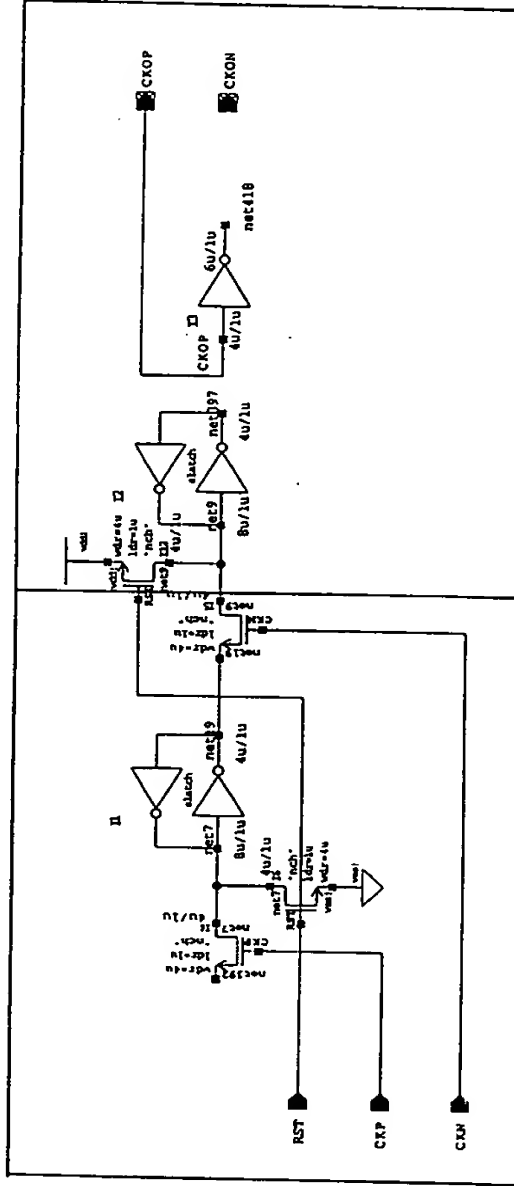


Fig. 8.0201

PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Timed Lockout Divider Cell			
PART: 103reva/tldcel_bypass		REV: B10	SIZE: A
DATE: Mar 26 13:54:47 1996		PAGE: 1	

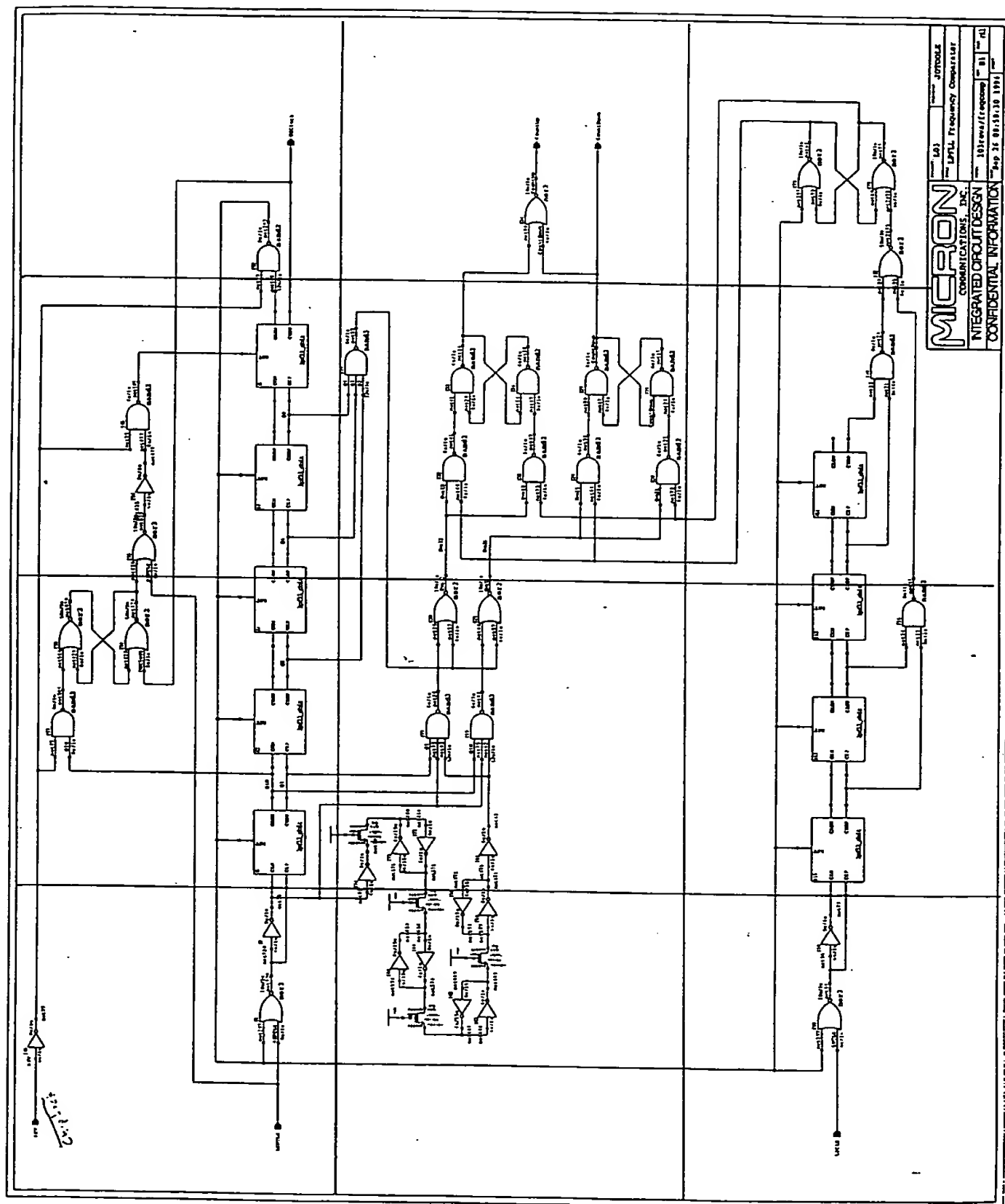
**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

B10: new cell to bypass 1st counter stage

8.0202AA	8.0202AB	8.0202AC	8.0202AD
8.0202BA	8.0202BB	8.0202BC	8.0202BD
8.0202CA	8.0202CB	8.0202CC	8.0202CD

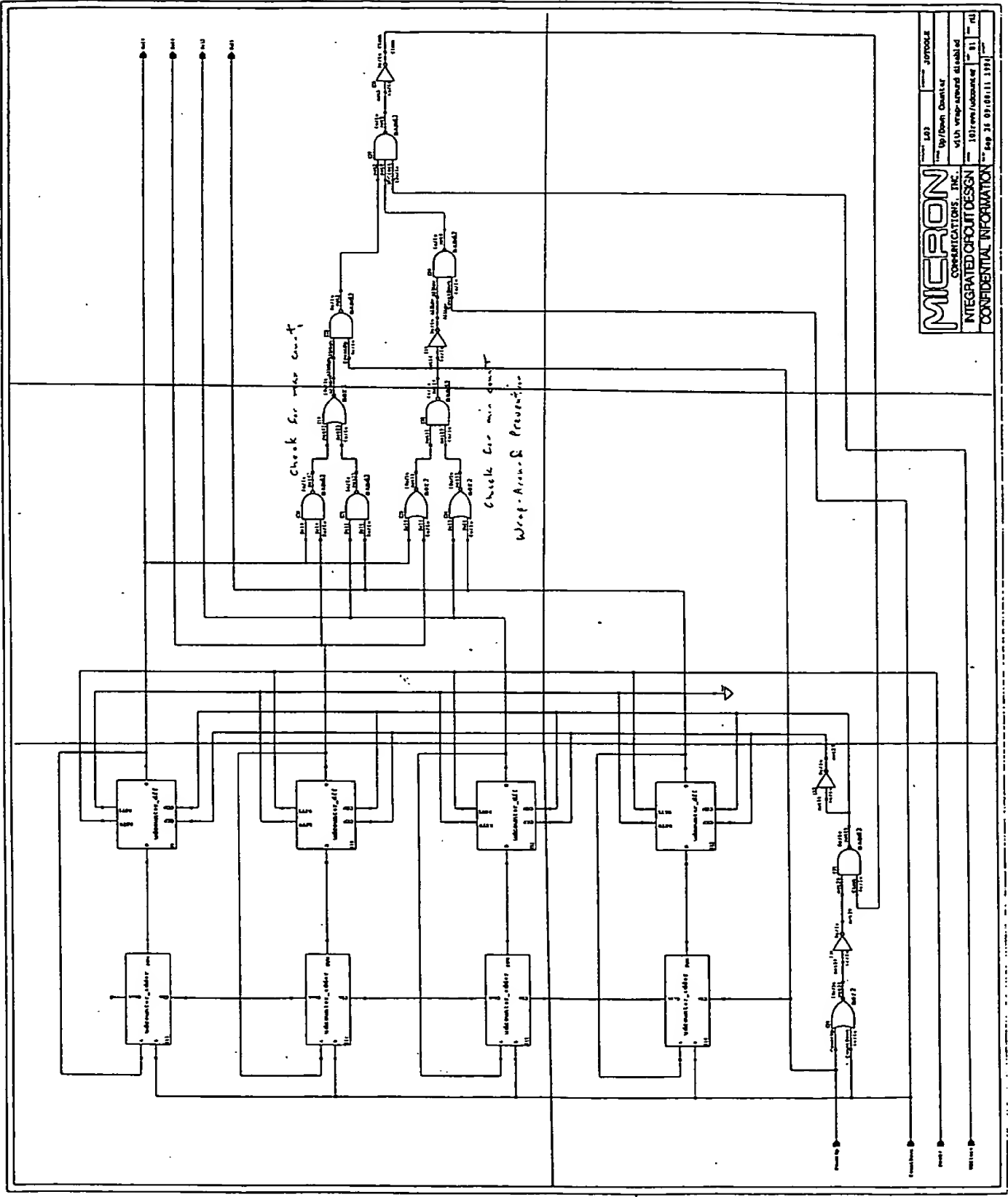
Fig. 8.0202





8.0203AA	8.0203AB	8.0203AC
8.0203BA	8.0203BB	8.0203BC

004420" 00000000



MICRON		143	2070044
COMMUNICATIONS, INC.		Up/Down Counter	
INTEGRATED CIRCUIT DESIGN		4-bit wrap-around disabled	
CONFIDENTIAL INFORMATION		1017 rev 1/80	
		Aug 24, 01/01/11 1971	

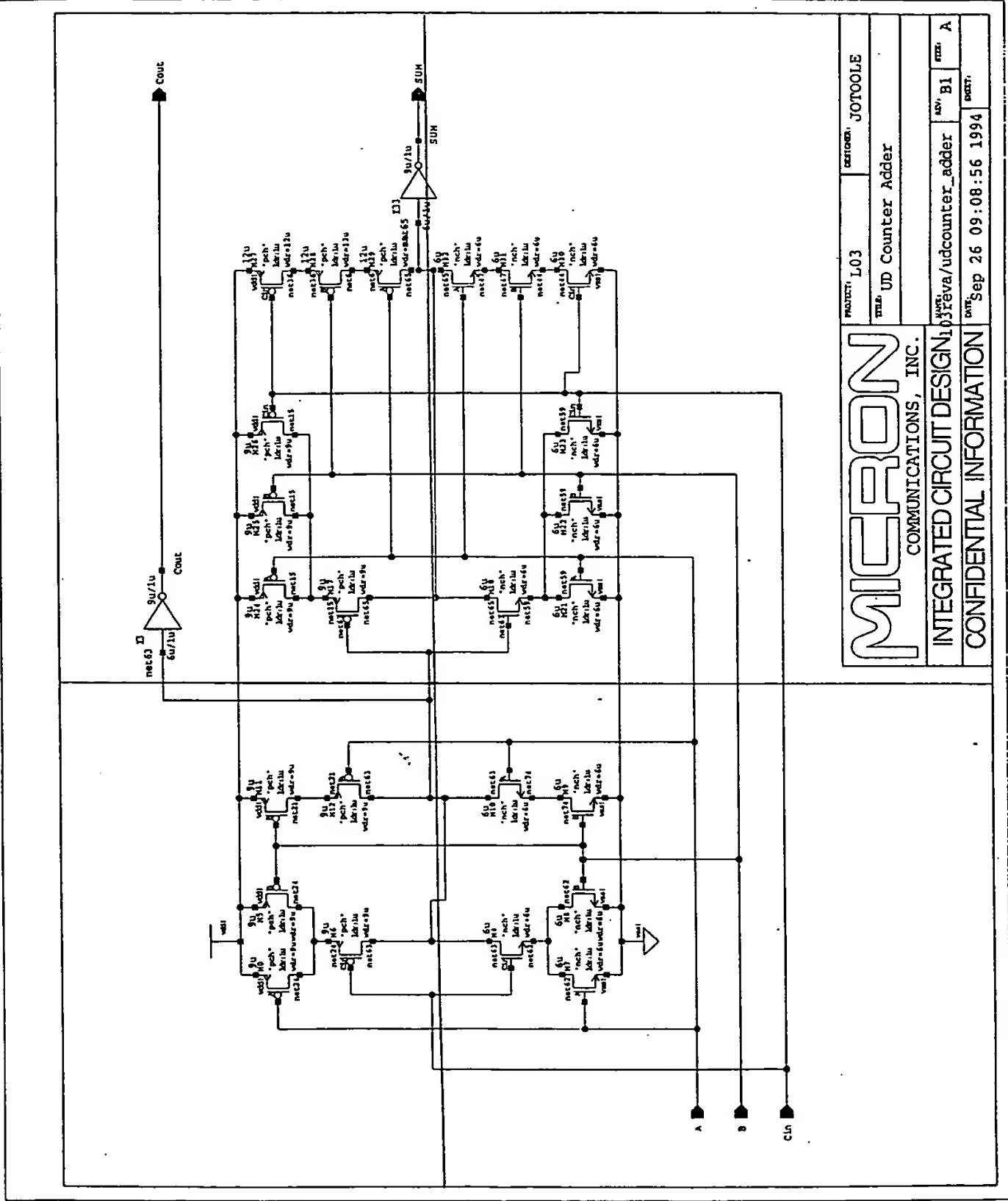
FIG. 8.0203

007420 00000000

MI40-030

8.020301AA	8.020301AB
8.020301BA	8.020301BB

11.11.11 11.11.11.11.11.11



MICRON		PROJECT: L03	DESIGN: J0700LE
COMMUNICATIONS, INC.		TITLE: UD Counter Adder	
INTEGRATED CIRCUIT DESIGN		DATE: Sep 26 09:08:56 1994	
CONFIDENTIAL INFORMATION		PART: A	

0014120 20920500

8.020302AB

8.020302AA

II II 88.020302



007420" 00900500

MI40-030

8.0204AA	8.0204AB	8.0204AC	8.0204AD	8.0204AE	8.0204AF	8.0204AG	8.0204AH	8.0204AI	8.0204AJ
8.0204BA	8.0204BB	8.0204BC	8.0204BD	8.0204BE	8.0204BF	8.0204BG	8.0204BH	8.0204BI	8.0204BJ
8.0204CA	8.0204CB	8.0204CC	8.0204CD	8.0204CE	8.0204CF	8.0204CG	8.0204CH	8.0204CI	
8.0204DA	8.0204DB	8.0204DC	8.0204DD	8.0204DE	8.0204DF	8.0204DG	8.0204DH	8.0204DI	
8.0204EA	8.0204EB	8.0204EC	8.0204ED	8.0204EE	8.0204EF	8.0204EG	8.0204EH	8.0204EI	8.0204EJ

II II 00 88.000000

[illegible]

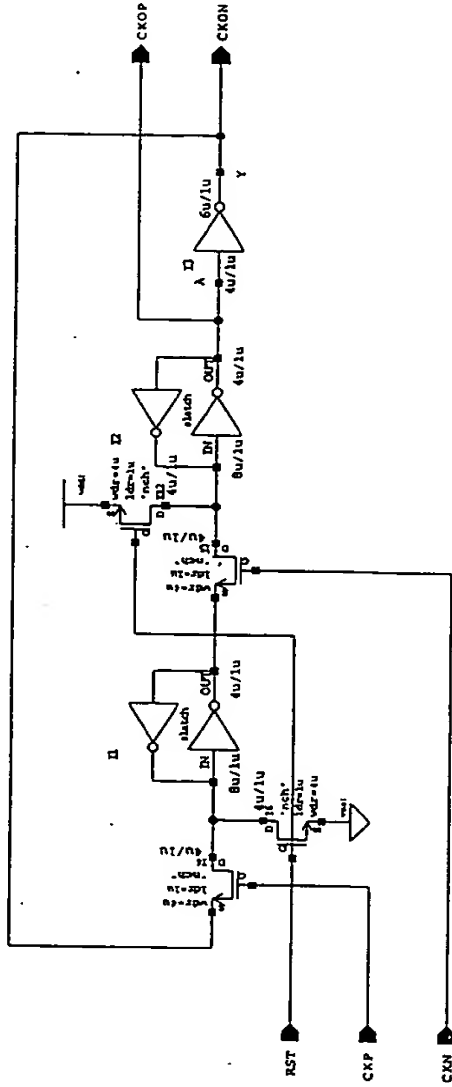
**MICRON**  
 COMPUTATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

DATE	REV	DESCRIPTION
10/10/80	1	INITIAL DESIGN
11/10/80	2	REVISION
12/10/80	3	FINAL DESIGN

F.G. 20204AA-EJ



0014120 00900500



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Titled Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		NUM: 103revA/ldcal	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	INSTR:

~~FILED~~  
F1680205

8.03AB	
8.03AA	

8.03AA

10.8 10.8

Country	Year	Value	Unit
Algeria	1980	1.00	1000
Algeria	1981	1.00	1000
Algeria	1982	1.00	1000
Algeria	1983	1.00	1000
Algeria	1984	1.00	1000
Algeria	1985	1.00	1000
Algeria	1986	1.00	1000
Algeria	1987	1.00	1000
Algeria	1988	1.00	1000
Algeria	1989	1.00	1000
Algeria	1990	1.00	1000
Algeria	1991	1.00	1000
Algeria	1992	1.00	1000
Algeria	1993	1.00	1000
Algeria	1994	1.00	1000
Algeria	1995	1.00	1000
Algeria	1996	1.00	1000
Algeria	1997	1.00	1000
Algeria	1998	1.00	1000
Algeria	1999	1.00	1000
Algeria	2000	1.00	1000
Algeria	2001	1.00	1000
Algeria	2002	1.00	1000
Algeria	2003	1.00	1000
Algeria	2004	1.00	1000
Algeria	2005	1.00	1000
Algeria	2006	1.00	1000
Algeria	2007	1.00	1000
Algeria	2008	1.00	1000
Algeria	2009	1.00	1000
Algeria	2010	1.00	1000
Algeria	2011	1.00	1000
Algeria	2012	1.00	1000
Algeria	2013	1.00	1000
Algeria	2014	1.00	1000
Algeria	2015	1.00	1000
Algeria	2016	1.00	1000
Algeria	2017	1.00	1000
Algeria	2018	1.00	1000
Algeria	2019	1.00	1000
Algeria	2020	1.00	1000
Algeria	2021	1.00	1000
Algeria	2022	1.00	1000
Algeria	2023	1.00	1000
Algeria	2024	1.00	1000
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Algeria	2026	1.00	1000
Algeria	2027	1.00	1000
Algeria	2028	1.00	1000
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Algeria	2030	1.00	1000
Algeria	2031	1.00	1000
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Algeria	2035	1.00	1000
Algeria	2036	1.00	1000
Algeria	2037	1.00	1000
Algeria	2038	1.00	1000
Algeria	2039	1.00	1000
Algeria	2040	1.00	1000
Algeria	2041	1.00	1000
Algeria	2042	1.00	1000
Algeria	2043	1.00	1000
Algeria	2044	1.00	1000
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Algeria	2046	1.00	1000
Algeria	2047	1.00	1000
Algeria	2048	1.00	1000
Algeria	2049	1.00	1000
Algeria	2050	1.00	1000
Algeria	2051	1.00	1000
Algeria	2052	1.00	1000
Algeria	2053	1.00	1000
Algeria	2054	1.00	1000
Algeria	2055	1.00	1000
Algeria	2056	1.00	1000
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Algeria	2058	1.00	1000
Algeria	2059	1.00	1000
Algeria	2060	1.00	1000
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Algeria	2063	1.00	1000
Algeria	2064	1.00	1000
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Algeria	2066	1.00	1000
Algeria	2067	1.00	1000

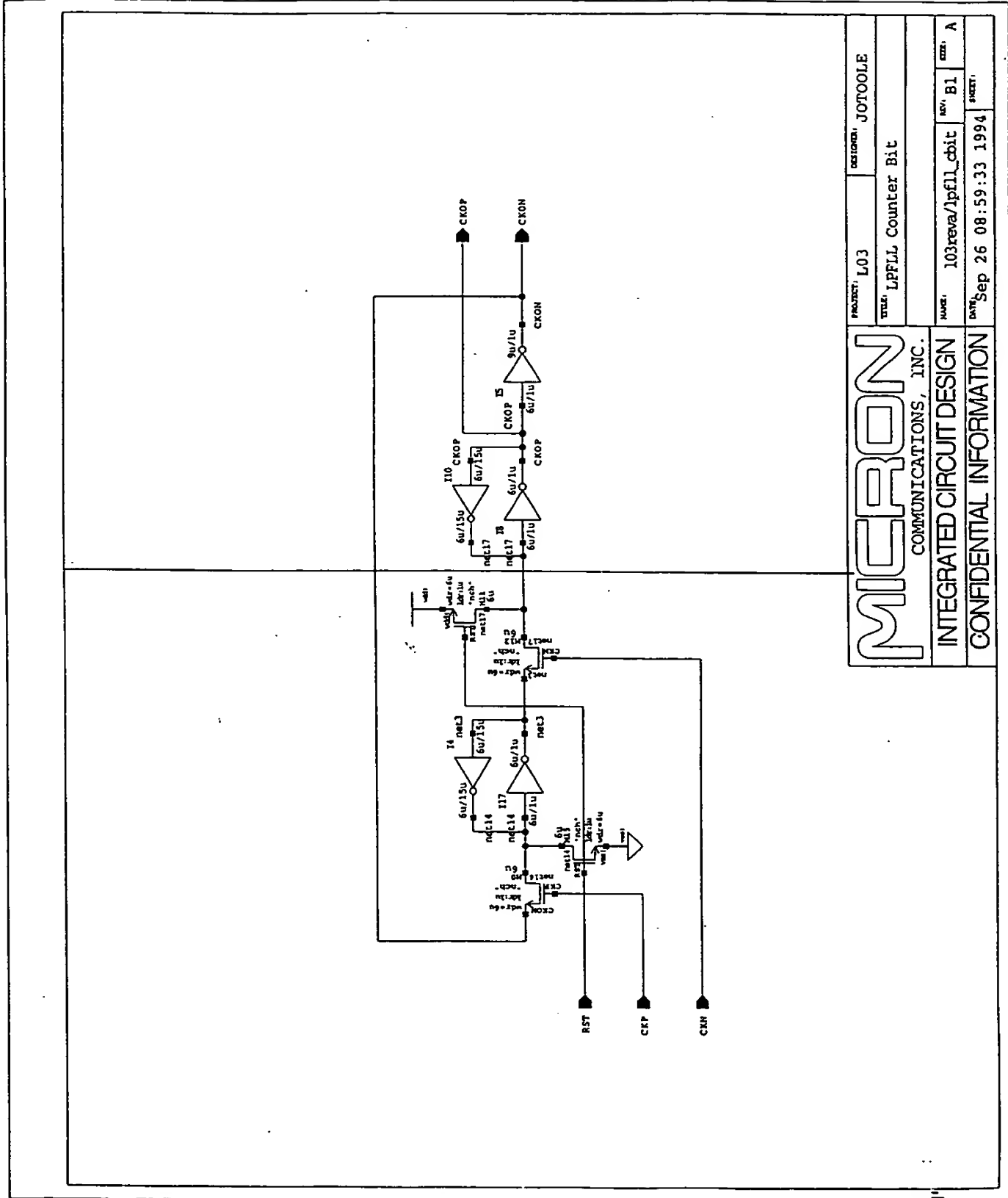


Fig. 8.03

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: LPFLL Counter Bit	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/lpfll_cbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 08:59:33 1994	SHEET: A

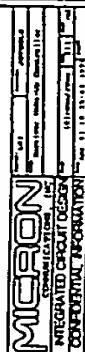
DOTED' 20920500

MI40-030

8.04AA	8.04AB	8.04AC	8.04AD	8.04AE	8.04AF
8.04BA	8.04BB	8.04BC	8.04BD	8.04BE	8.04BF
8.04CA	8.04CB	8.04CC	8.04CD	8.04CE	8.04CF
8.04DA	8.04DB	8.04DC	8.04DD	8.04DE	
8.04EA	8.04EB	8.04EC	8.04ED	8.04EE	

IL BB.0000

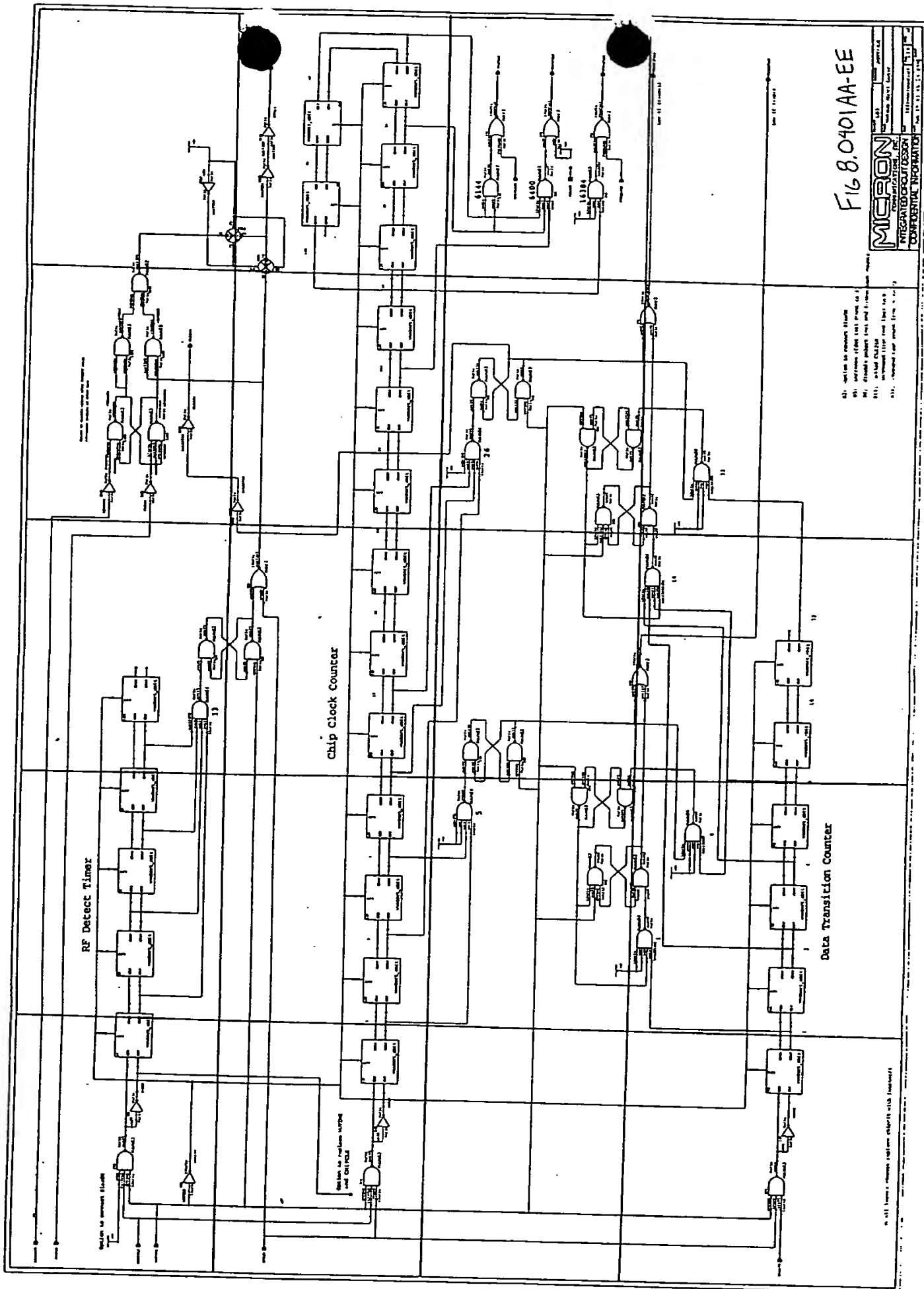
F16.804



STUDY PAGE

8.0401AA	8.0401AB	8.0401AC	8.0401AD	8.0401AE
8.0401BA	8.0401BB	8.0401BC	8.0401BD	8.0401BE
8.0401CA	8.0401CB	8.0401CC	8.0401CD	8.0401CE
8.0401DA	8.0401DB	8.0401DC	8.0401DD	8.0401DE

NOTED: 00920500

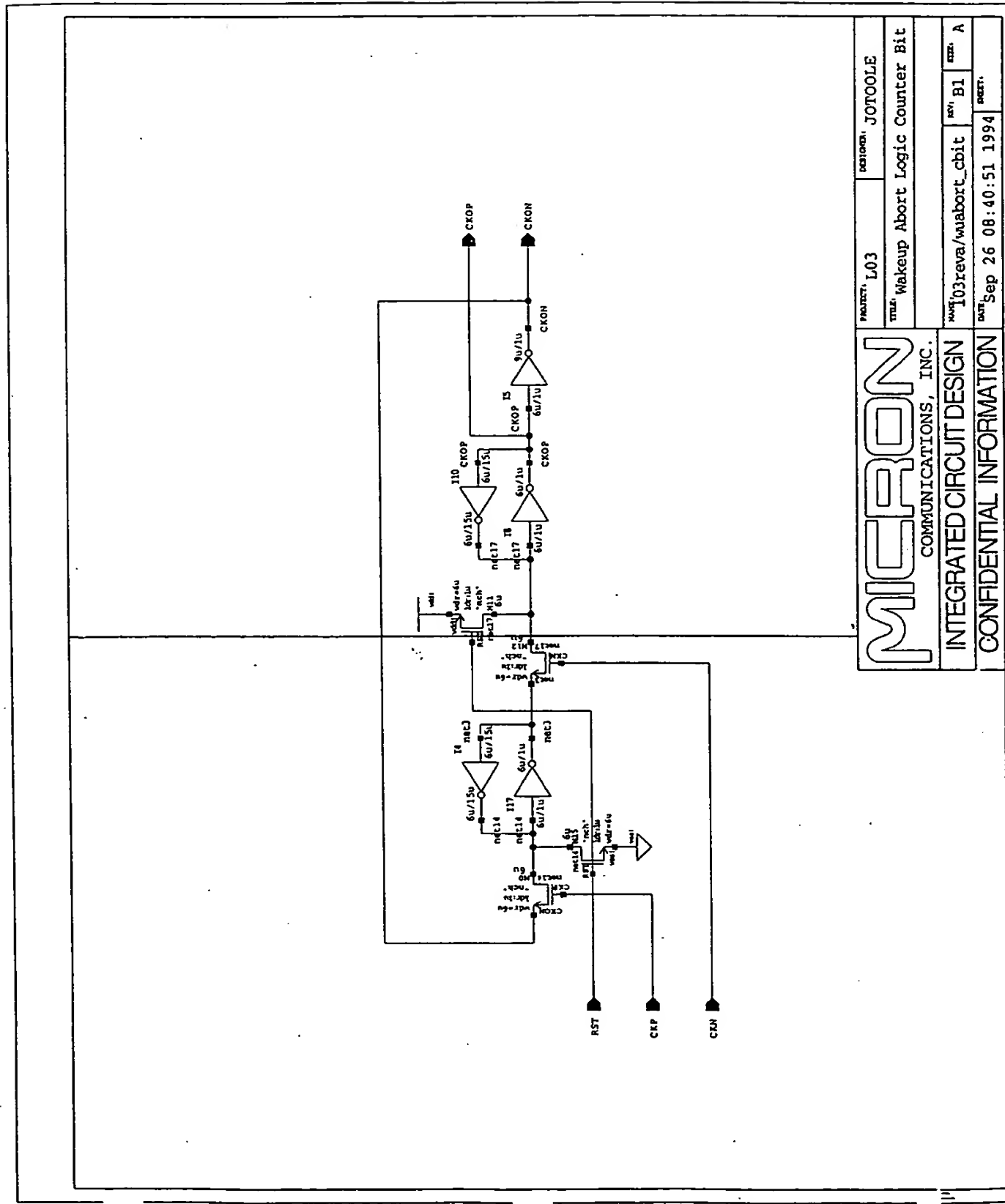


007720 26560500

8.040101AA	8.040101AB
------------	------------

II 8.040101II





<b>MICRON</b>		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Wakeup Abort Logic Counter Bit	
INTEGRATED CIRCUIT DESIGN		WAVE: i03reva/wuabort_cbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 08:40:51 1994	DESIGN: A

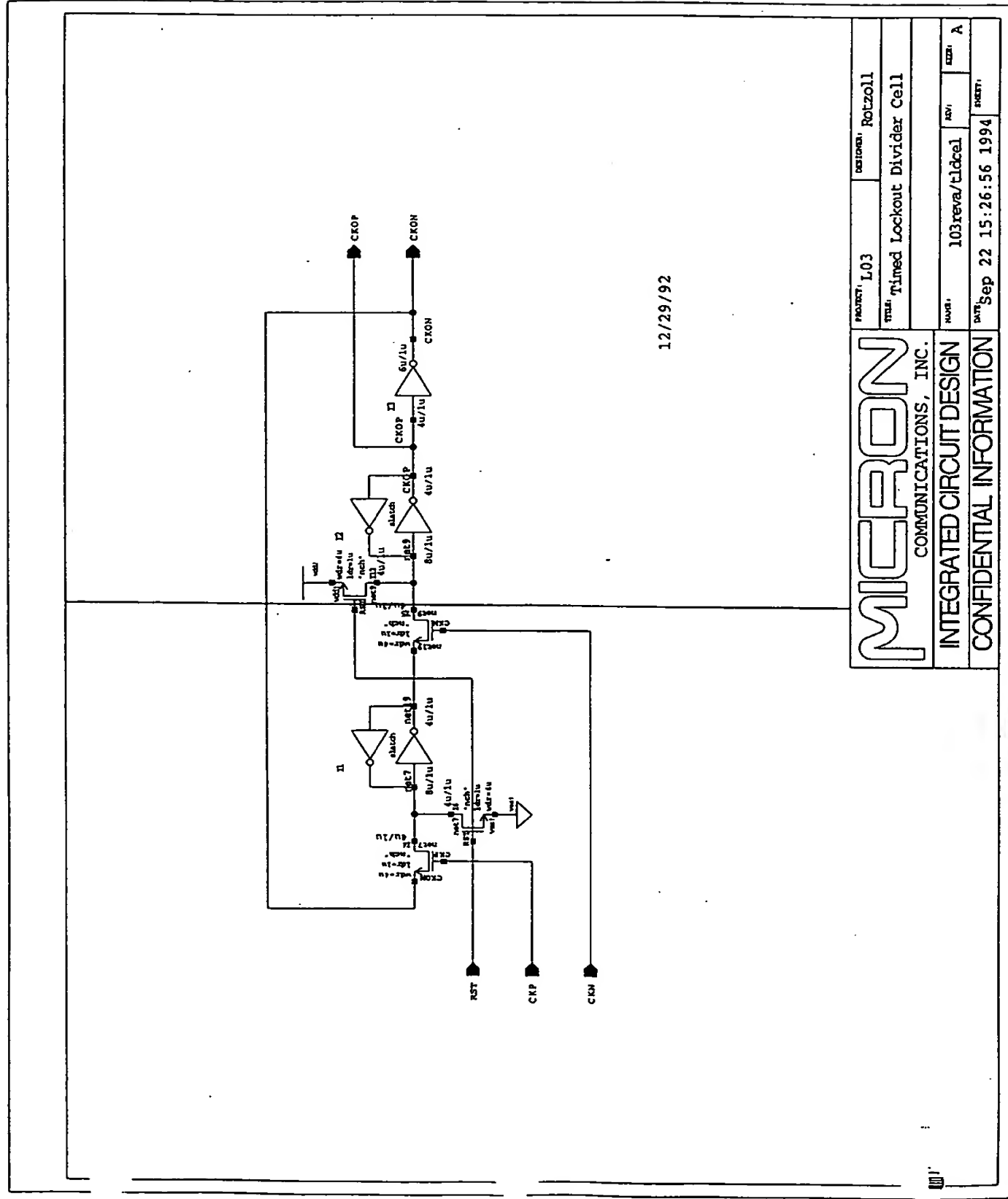
Fig. 8.090101

007703 04520500

8.0402AB

8.0402AA

8.0402



MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
PROJECT: L03	DESIGNER: Rotzoll		
TITLE: Timed Lockout Divider Cell			
AVR: 103revA/tldcel	REV: A		
DATE: Sep 22 15:26:56 1994		SHEET: 1	

8.05AA	8.05AB	8.05AC	8.05AD	8.05AE
8.05BA	8.05BB	8.05BC	8.05BD	8.05BE
8.05CA	8.05CB	8.05CC	8.05CD	8.05CE
8.05DA	8.05DB	8.05DC	8.05DD	8.05DE

[illegible]

F16.8.05

<b>MICRON</b>	NAME	DOB	ADDRESS
SPECIALIZATION DIV. INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
	RELATIONSHIP TO SUBJECT AND SOURCE INFORMATION	DATE	BY
		10-10-83	111
		Page 1	10-10-83 1983

8.0501AA	8.0501AB	8.0501AC	8.0501AD	8.0501AE	8.0501BE
8.0501BA	8.0501BB	8.0501BC	8.0501BD		

11



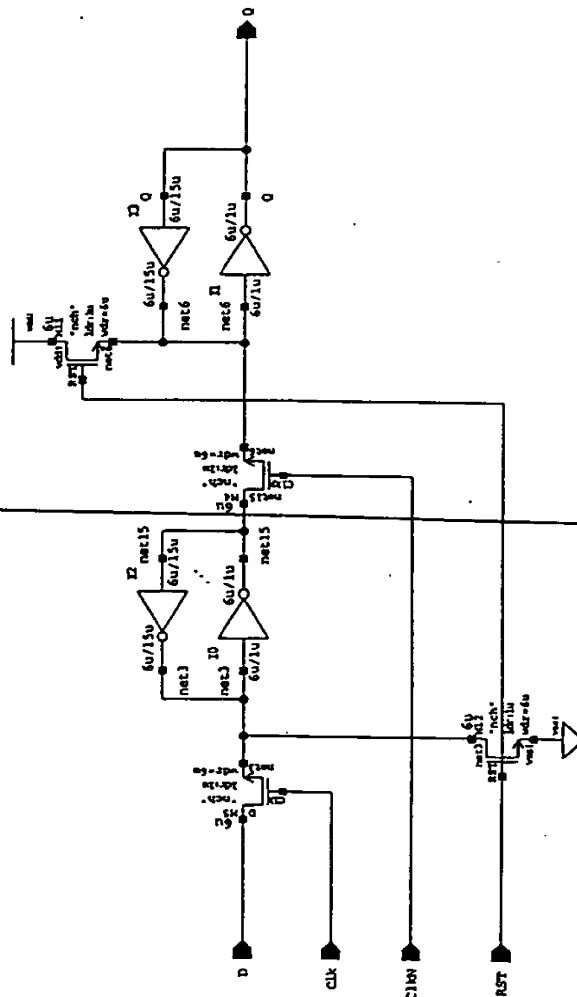
**MICRON**  
CORPORATION, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

8.050101AB

II II II      88.05070711



FIG. 8.050101



# NOBIS

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

PROJECT: L03	DESIGNER: JOTOOLE
--------------	-------------------

mm	Shift Register Cell
0	0
1	0
2	0
3	0
4	0
5	0
6	0
7	0
8	0
9	0
10	0
11	0
12	0
13	0
14	0
15	0
16	0
17	0
18	0
19	0
20	0
21	0
22	0
23	0
24	0
25	0
26	0
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28	0
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167	0
168	0
169	0
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171	0
172	0
173	0
174	0
175	0
176	0
177	0
178	0
179	0
180	0

NAME:	103reva/dcr_sreg	REV: B1	SIZE: A
-------	------------------	---------	---------

DATE	Aug 31 14:25:03 1994	SHEET
------	----------------------	-------

001120 20920900

8.050102AA

8.050102AB

8.050102

00000000000000000000

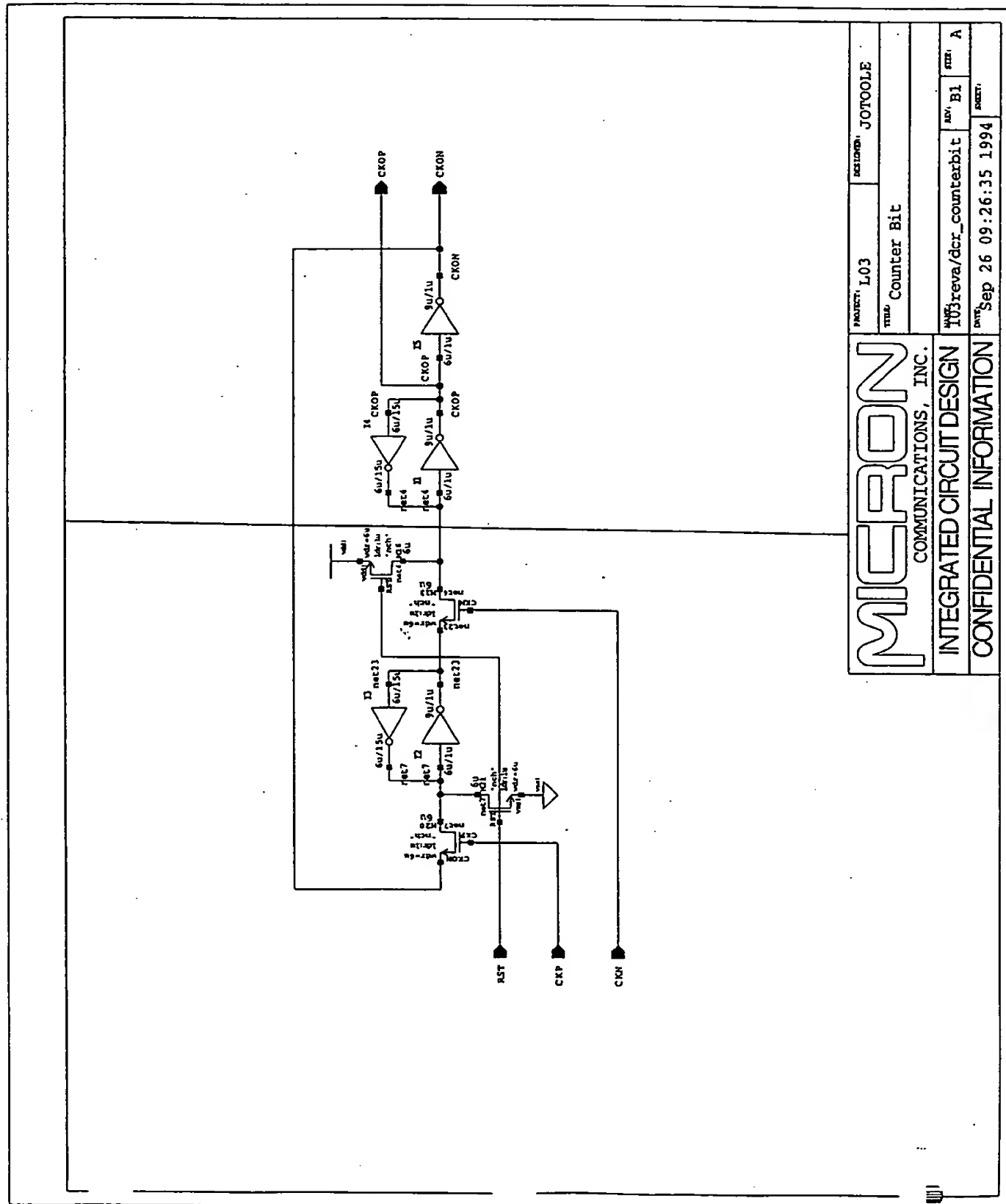


Fig. 8.050102

8.0502AA	8.0502AB	8.0502AC	8.0502AD
8.0502BA	8.0502BB	8.0502BC	8.0502BD
8.0502CA	8.0502CB	8.0502CC	8.0502CD

00502673 001400

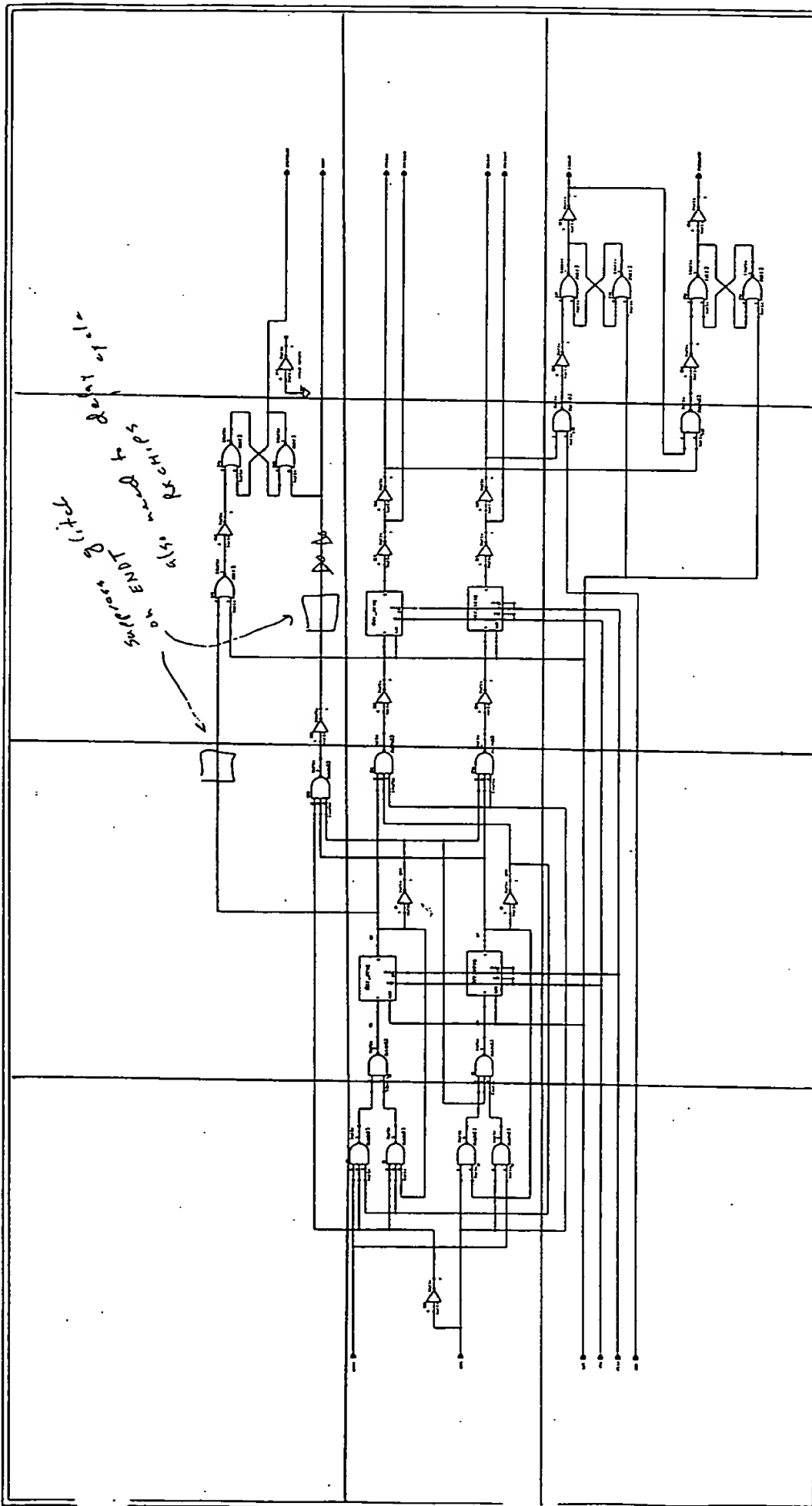
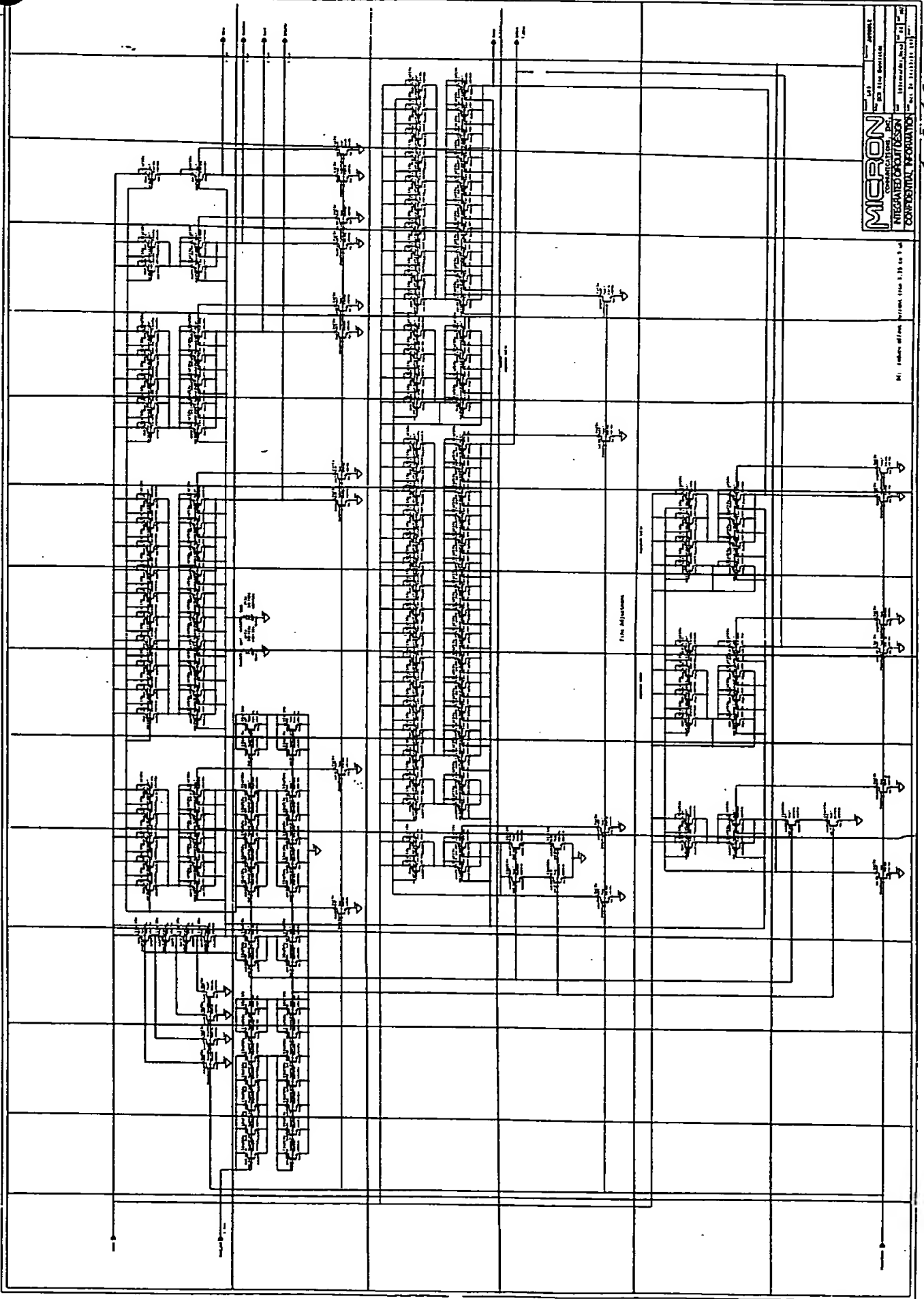


FIG 8.0502

<b>MICRON</b>	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	8.0502
Rev.	1
Date	11/11/73
By	J. L. Smith
Check'd	J. L. Smith
App'd	J. L. Smith

8.05034A	8.05034B	8.05034C	8.05034D	8.05034E	8.05034F	8.05034G	8.05034H	8.05034I	8.05034J	8.05034K	8.05034L	8.05034M	8.05034N	8.05034O
8.05038A	8.05038B	8.05038C	8.05038D	8.05038E	8.05038F	8.05038G	8.05038H	8.05038I	8.05038J	8.05038K	8.05038L	8.05038M	8.05038N	8.05038O
8.0503CA	8.0503CB	8.0503CC	8.0503CD	8.0503CE	8.0503CF	8.0503CG	8.0503CH	8.0503CI	8.0503CJ	8.0503CK	8.0503CL	8.0503CM	8.0503CN	8.0503CO
8.0503DA	8.0503DB	8.0503DC	8.0503DD	8.0503DE	8.0503DF	8.0503DG	8.0503DH	8.0503DI	8.0503DJ	8.0503DK	8.0503DL	8.0503DM	8.0503DN	8.0503DO
8.0503EA	8.0503EB	8.0503EC	8.0503ED	8.0503EE	8.0503EF	8.0503EG	8.0503EH	8.0503EI	8.0503EJ	8.0503EK	8.0503EL	8.0503EM	8.0503EN	
8.0503FA	8.0503FB	8.0503FC	8.0503FD	8.0503FE	8.0503FF	8.0503FG	8.0503FH	8.0503FI	8.0503FJ	8.0503FK	8.0503FL	8.0503FM	8.0503FN	

00502602 021400



MICRON  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Fig. 8.0603

8.0504AA	8.0504AB	8.0504AC	8.0504AD
8.0504BA	8.0504BB	8.0504BC	8.0504BD
8.0506CA	8.0504CB	8.0504CC	8.0504CD
8.0504DA	8.0504DB	8.0504DC	8.0504DD
8.0504EA	8.0504EB	8.0504EC	8.0504ED
			8.0504EE

JEFFREY B. MASON



004400 00000000

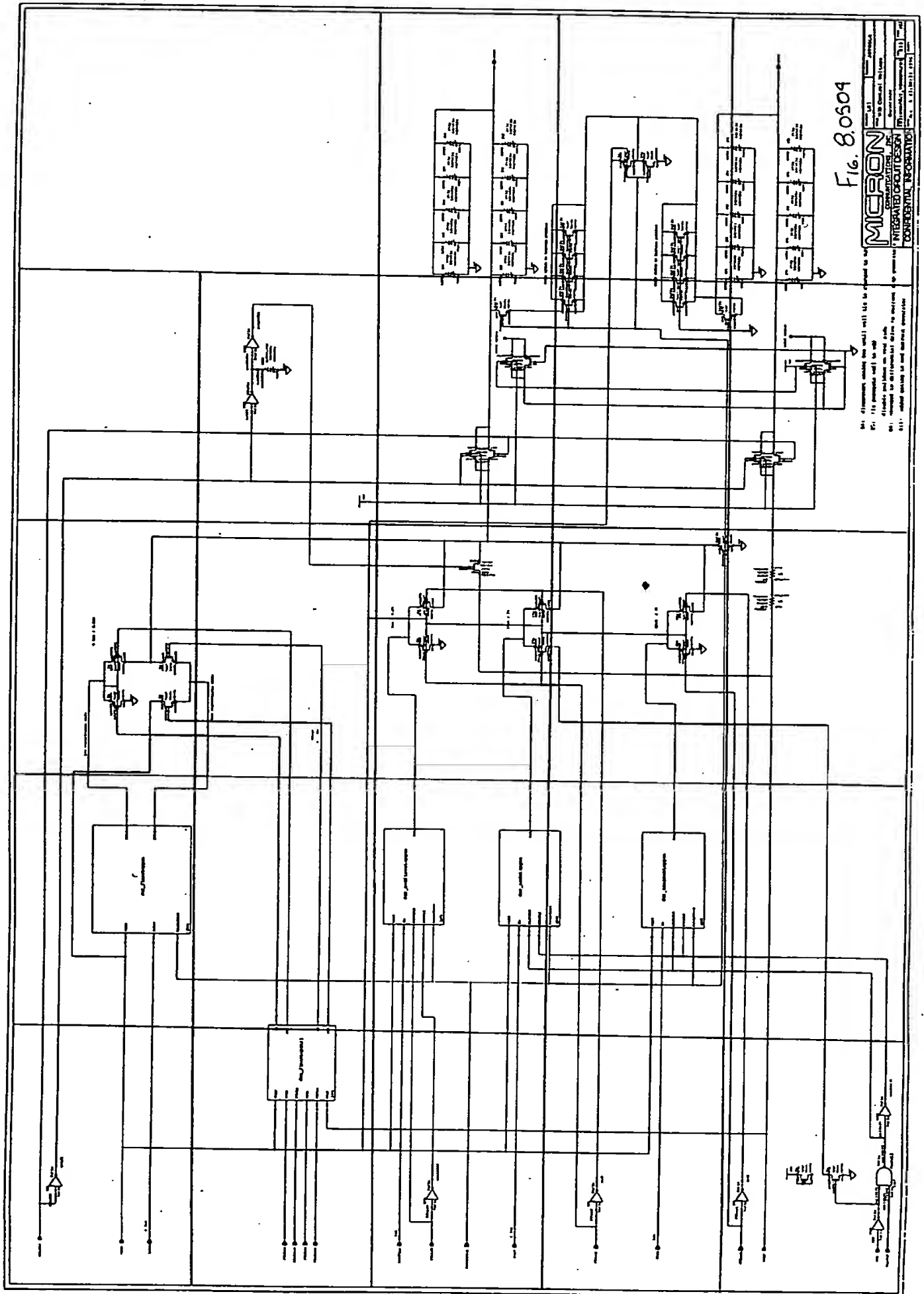


Fig. 80504

**MICRON**  
MICROPROCESSOR  
80504  
CONFIDENTIAL INFORMATION  
Rev. 1.11/01/11/1980

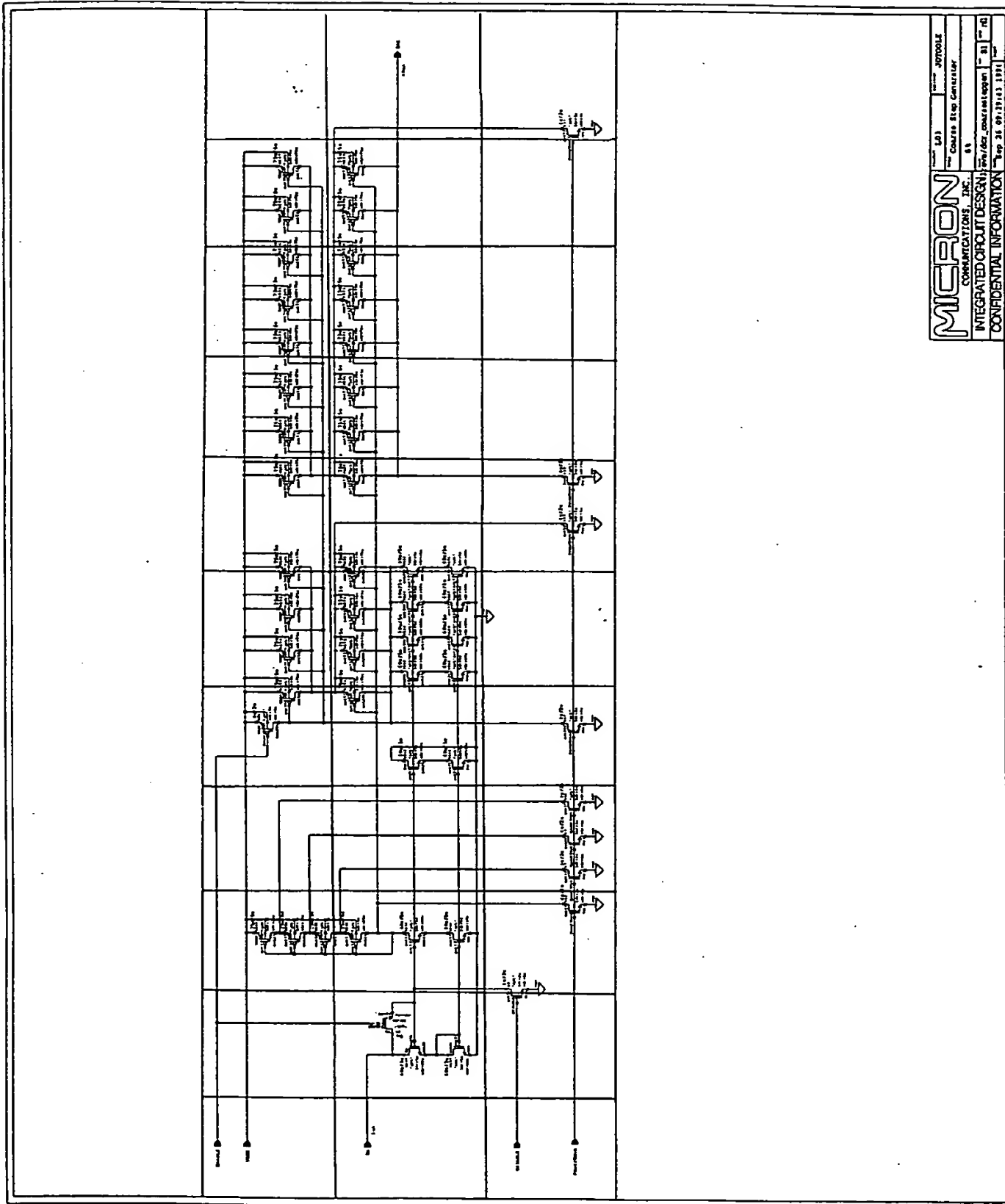
007120-20920500

MI40-030

8.050401AA	8.050401AB	8.050401AC	8.050401AD	8.050401AE	8.050401AF	8.050401AG	8.050401AH	8.050401AJ	
8.050401BA	8.050401BB	8.050401BC	8.050401BD	8.050401BE	8.050401BF	8.050401BG	8.050401BH	8.050401BJ	8.050401BK
8.050401CA	8.050401CB	8.050401CC	8.050401CD	8.050401CE	8.050401CF	8.050401CG	8.050401CH	8.050401CJ	8.050401CK

II II BB.050401 00 II

Fig 8.050401



001120-20900500

MI40-030

8.050402AA	8.050402AB	8.050402AC	8.050402AD	8.050402AE	8.050402AF	8.050402AG	8.050402AH	8.050402AI	8.050402AJ
8.050402BA	8.050402BB	8.050402BC	8.050402BD	8.050402BE	8.050402BF	8.050402BG	8.050402BH	8.050402BI	8.050402BJ
8.050402CA	8.050402CB	8.050402CC	8.050402CD	8.050402CE	8.050402CF	8.050402CG	8.050402CH	8.050402CI	8.050402CJ

IL 11 07 88.05.04.02

004420 00000000

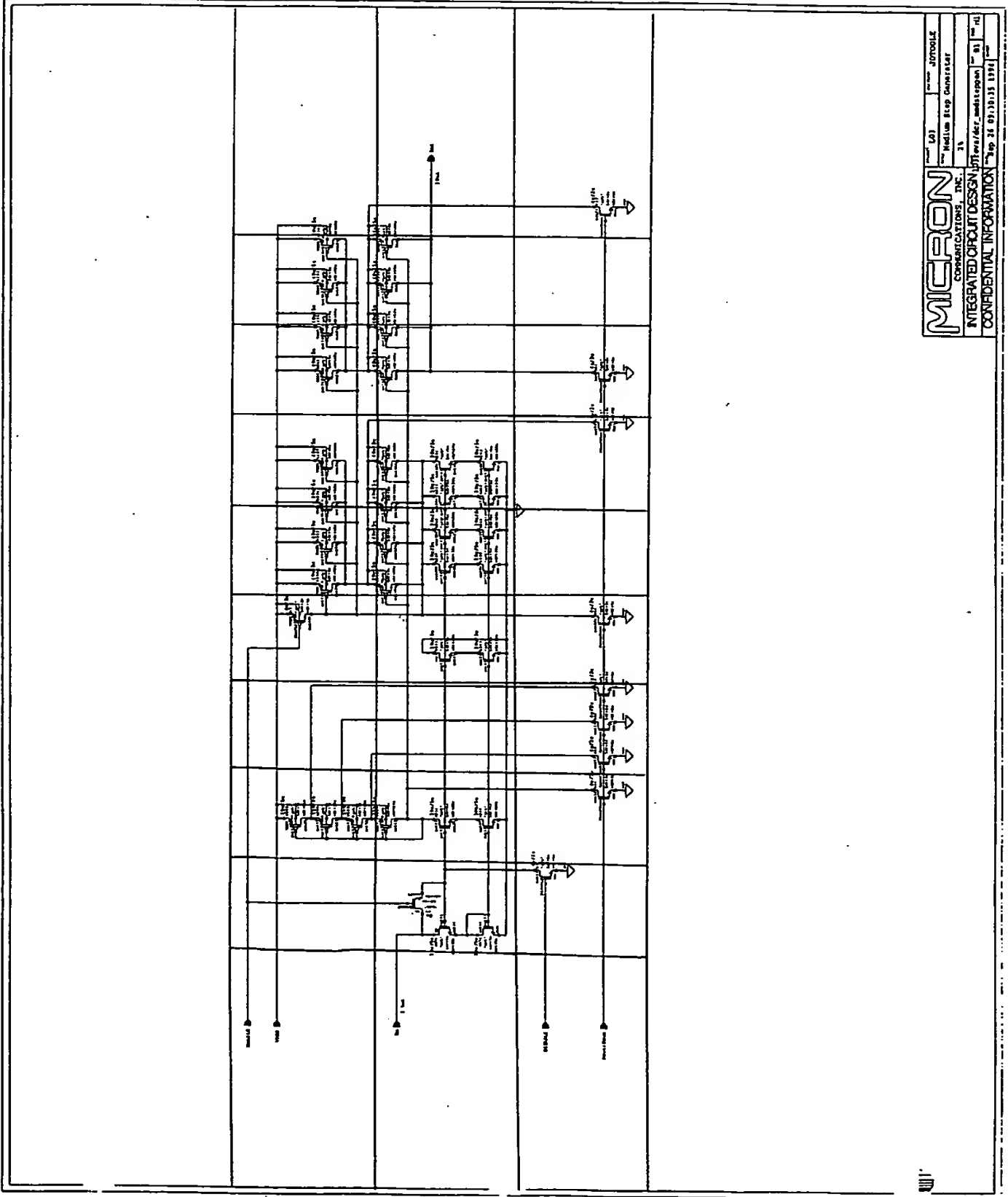
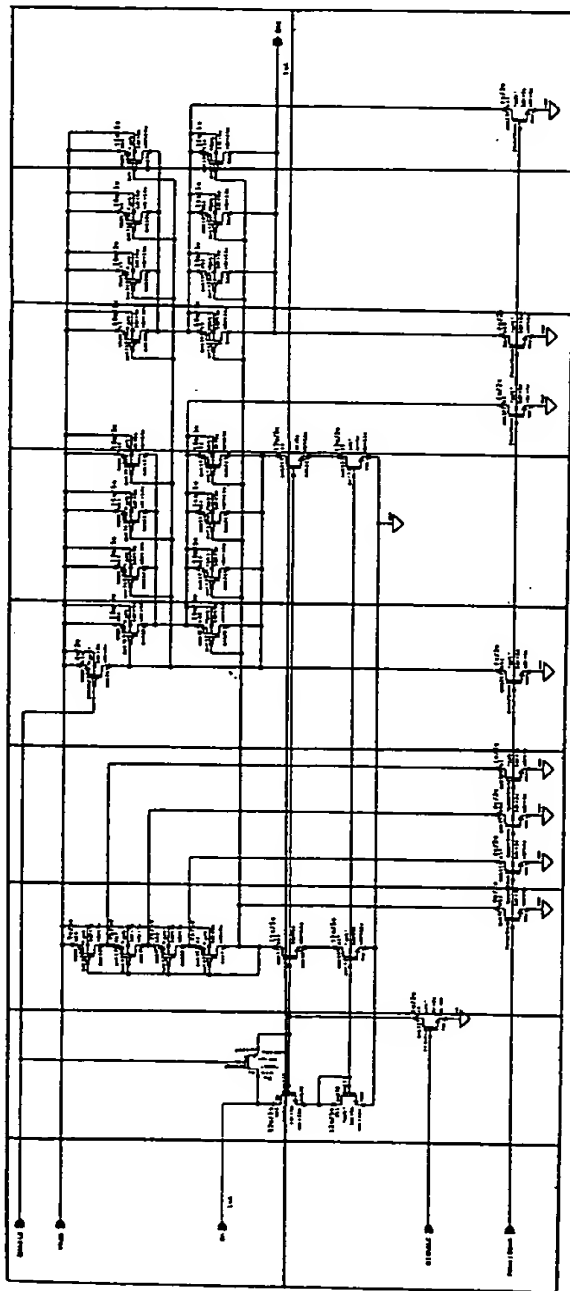


Fig. 8.050402

8.050403AA	8.050403AB	8.050403AC	8.050403AD	8.050403AE	8.050403AF	8.050403AG	8.050403AH	8.050403AI
8.050403BA	8.050403BB	8.050403BC	8.050403BD	8.050403BE	8.050403BF	8.050403BG	8.050403BH	8.050403BI

11.11.11 11.11.11 11.11.11

001420 20900000



<b>MICRON</b>		LA1	JY00015
CORPORATIONS, INC.		Medium Flow Step Converter	
INTEGRATED CIRCUIT DESIGN		0.25	
CONFIDENTIAL INFORMATION		027/027	01
		Rep 24 09/11/18 1981	01

Fig. 8.050403

001120 00000000

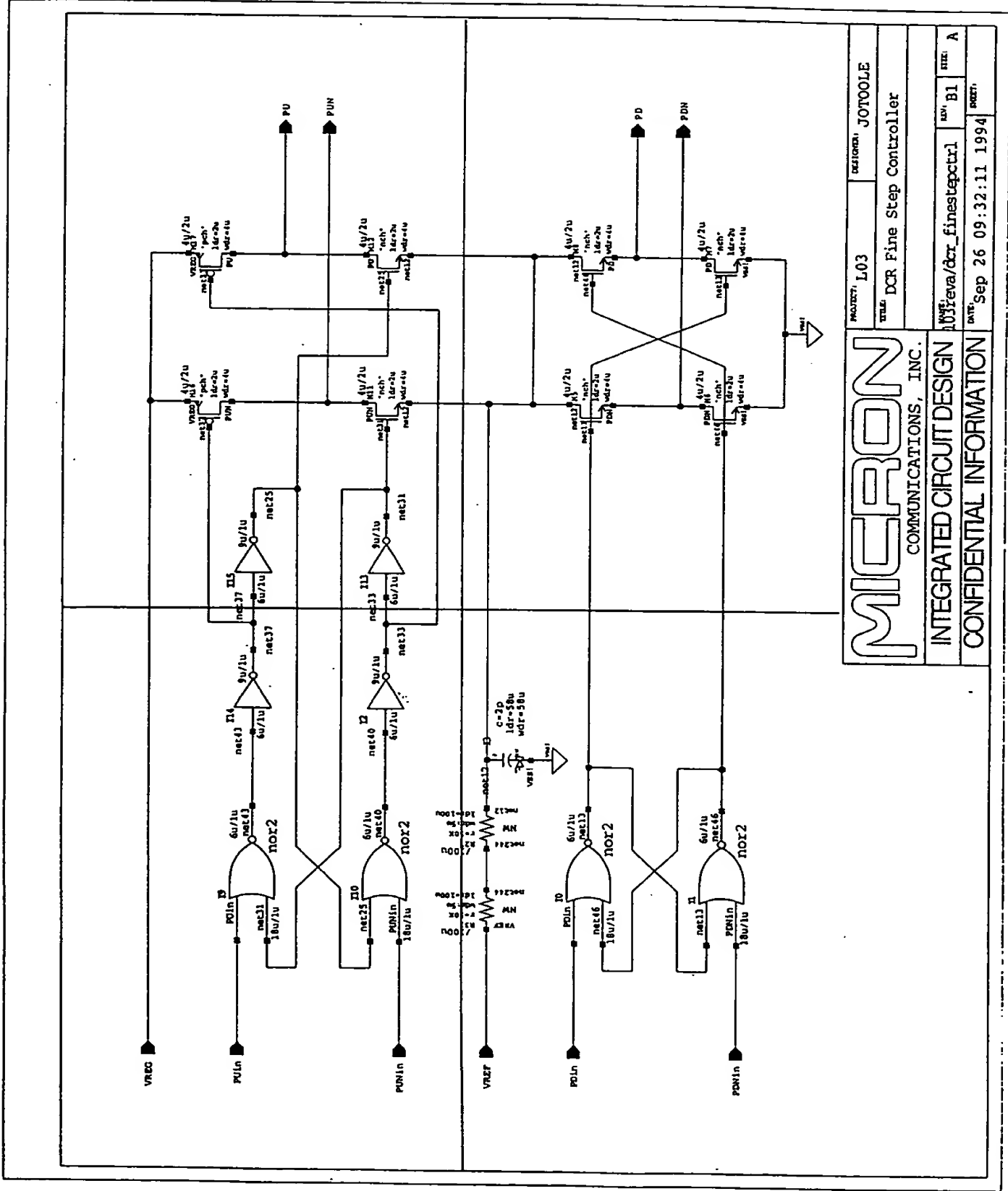
MI40-030

8.050404AA	8.050404AB
8.050404BA	8.050404BB

II II 8.050404



004400-00000000



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: DCR Fine Step Controller	
INTEGRATED CIRCUIT DESIGN		REV: 103revA/dcr_finestepctrl	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:32:11 1994	HEET: A

Fig. 8.050109

8.0504054A	8.0504054B	8.0504054C	8.0504054D	8.0504054E	8.0504054F	8.0504054G	8.0504054H	8.0504054I	8.0504054J	8.0504054K	8.0504054L	8.0504054M
8.0504056A	8.0504056B	8.0504056C	8.0504056D	8.0504056E	8.0504056F	8.0504056G	8.0504056H	8.0504056I	8.0504056J	8.0504056K	8.0504056L	8.0504056M
8.0504058A	8.0504058B	8.0504058C	8.0504058D	8.0504058E	8.0504058F	8.0504058G	8.0504058H	8.0504058I	8.0504058J	8.0504058K	8.0504058L	8.0504058M
8.0504050A	8.0504050B	8.0504050C	8.0504050D	8.0504050E	8.0504050F	8.0504050G	8.0504050H	8.0504050I	8.0504050J	8.0504050K	8.0504050L	8.0504050M
8.0504052A	8.0504052B	8.0504052C	8.0504052D	8.0504052E	8.0504052F	8.0504052G	8.0504052H	8.0504052I	8.0504052J	8.0504052K	8.0504052L	8.0504052M

И. П. 007420" 20920500

00141200 00000000

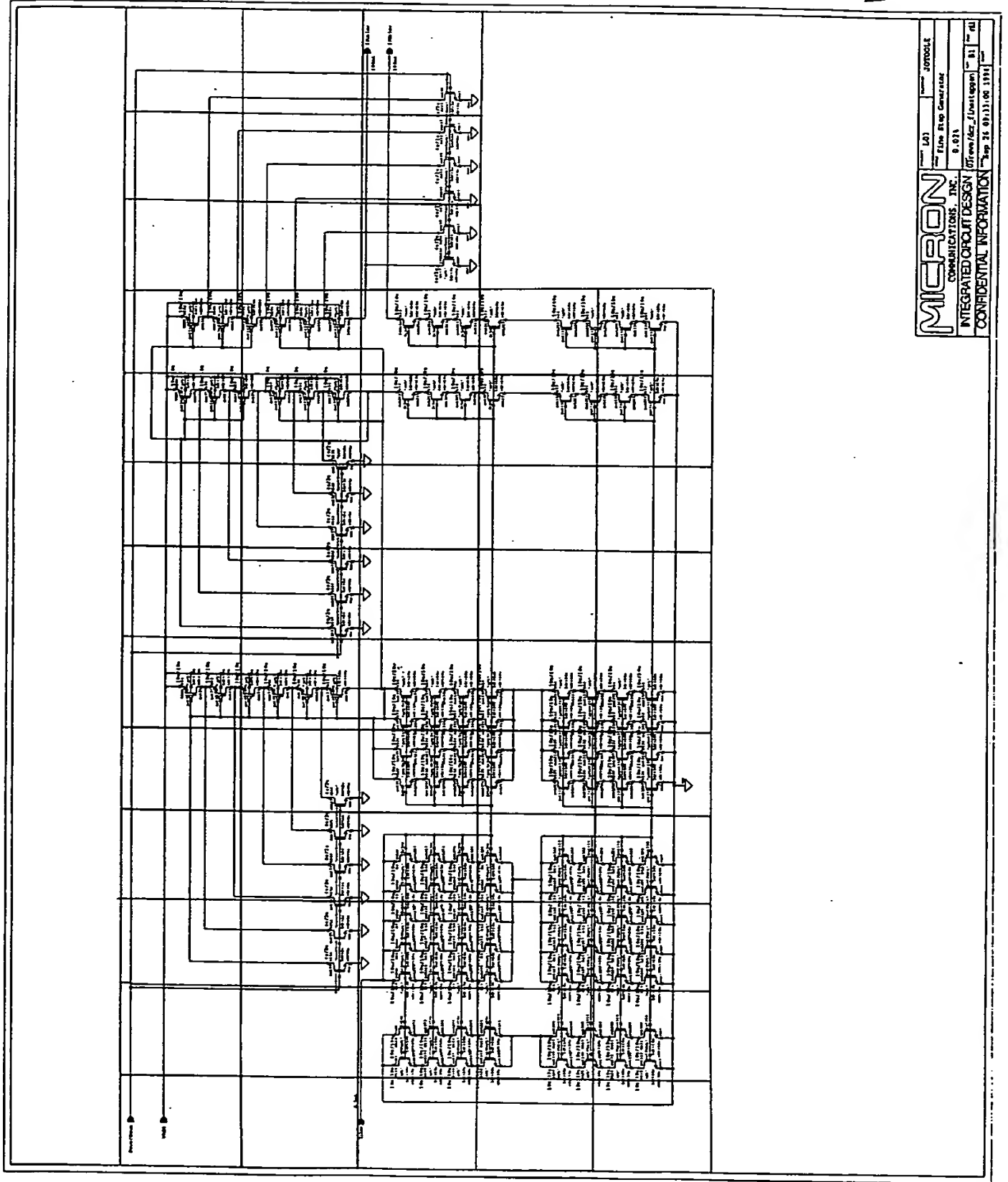


FIG 8.050405

8.05054A	8.05054B	8.05054C	8.05054D	8.05054E	8.05054F	8.05054G	8.05054H	8.05054I	8.05054J	8.05054K	8.05054L	8.05054M	8.05054N
8.05058A	8.05058B	8.05058C	8.05058D	8.05058E	8.05058F	8.05058G	8.05058H	8.05058I	8.05058J	8.05058K	8.05058L	8.05058M	8.05058N
8.0505CA	8.0505CB	8.0505CC	8.0505CD	8.0505CE	8.0505CF	8.0505CG	8.0505CH	8.0505CI	8.0505CJ	8.0505CK	8.0505CL	8.0505CM	8.0505CN
8.0505DA	8.0505DB	8.0505DC	8.0505DD	8.0505DE	8.0505DF	8.0505DG	8.0505DH	8.0505DI	8.0505DJ	8.0505DK	8.0505DL	8.0505DM	8.0505DN
8.0505EA	8.0505EB	8.0505EC	8.0505ED	8.0505EE	8.0505EF								

001420 26000000

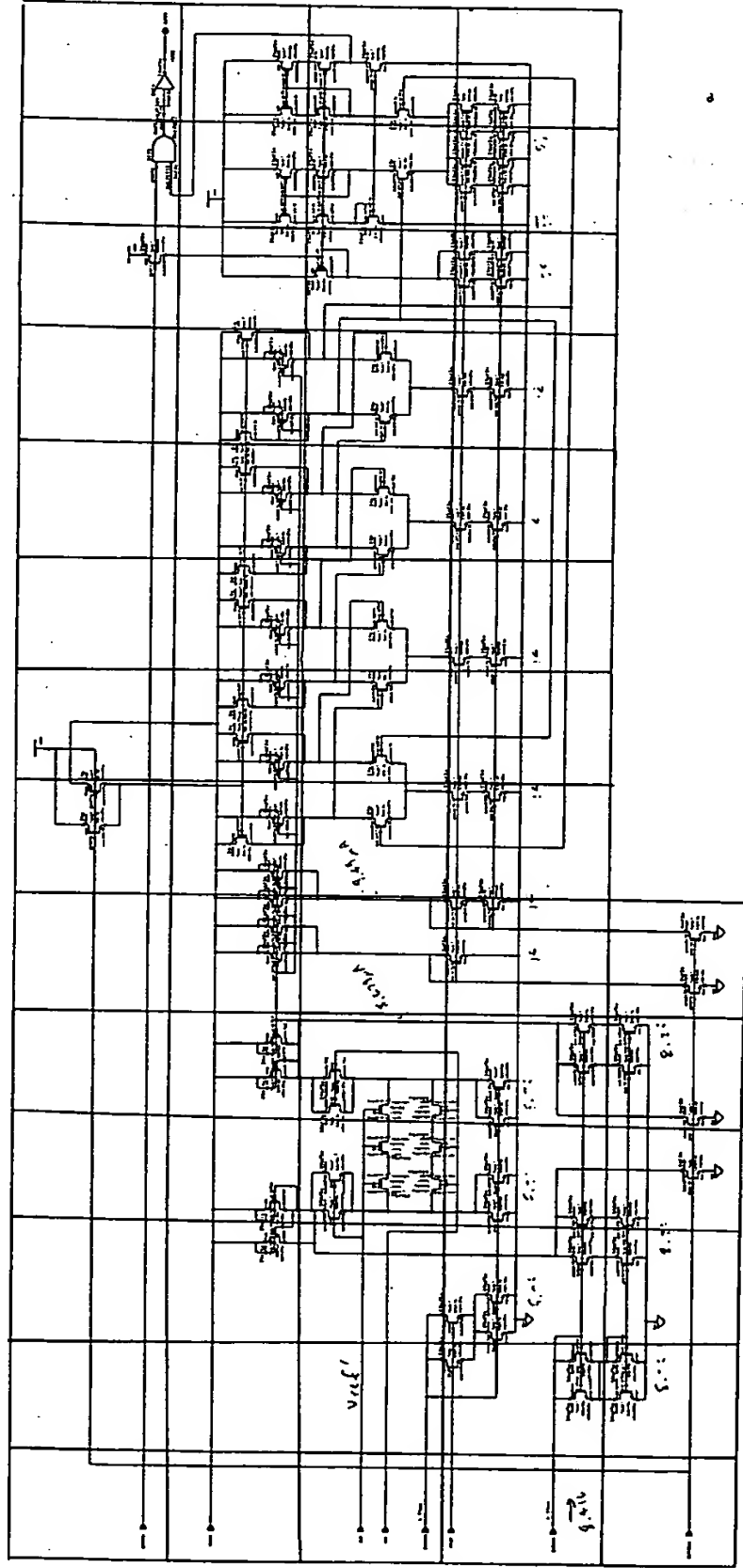


FIG. 8.0505

231A

007420 20920900

MI40-030

8.0506AA	8.0506AB
8.0506BA	8.0506BB

IL II 88.0506

The diagram illustrates the internal architecture of the 68000 microprocessor. It is organized into several main functional blocks:

- Registers:** Located at the top, including the Program Counter (PC), Stack Pointer (SP), Address Register (AR), and Data Register (DR). Each register is represented by a box with input/output lines and a 'metal option' label.
- ALU (Arithmetic Logic Unit):** A central block containing multiple ALU units (ALU1, ALU2, ALU3, ALU4) and a control logic block (ALU5). It performs various arithmetic and logical operations on data from the registers.
- Control Logic:** A complex network of logic gates (AND, OR, NOT) and multiplexers that manage the flow of data and control signals throughout the processor.
- Input/Output:** The bottom section shows the processor's external interfaces, including the Data Bus (DBUS), Address Bus (ABUS), and various control signals like RESET, WAIT, and DONE.

The diagram uses standard logic symbols: rectangles for registers and ALU, triangles for multiplexers, and various gate symbols for logic operations. Signal lines connect these components to show the internal data and control paths.

<b>MICRON</b>		Model No. <input type="text"/>		Serial No. <input type="text"/>	
COMMUNICATIONS, INC.		Fax <input type="text"/>		E-Mail <input type="text"/>	
INTEGRATED CIRCUIT DESIGN		Tel. (913) 874-7600		Fax (913) 874-7601	
CONFIDENTIAL INFORMATION		E-Mail <input type="text"/>		Fax (913) 874-7601	

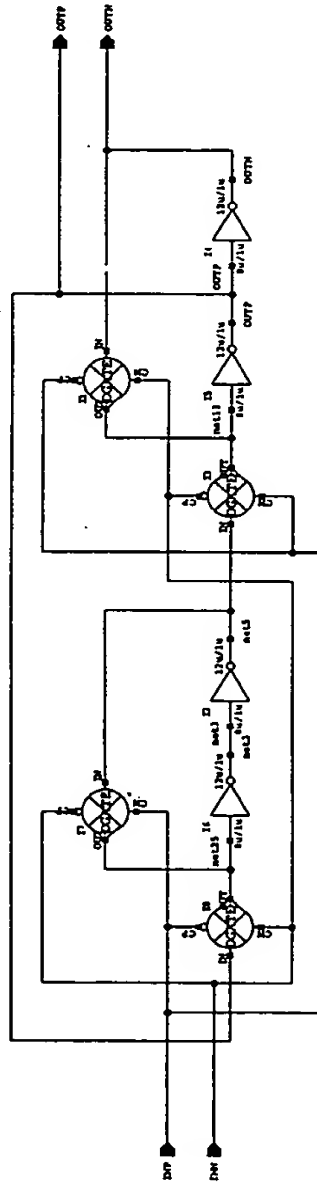


FIG. 8.050601

MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		NAME: Rx Clock Generator	
INTEGRATED CIRCUIT DESIGN		FLIP-FLOP	
CONFIDENTIAL INFORMATION		T03rev0/dcr_rxclockgeniff	REV: B1
		DATE: Sep 26 09:36:05 1994	DESIGN: rll



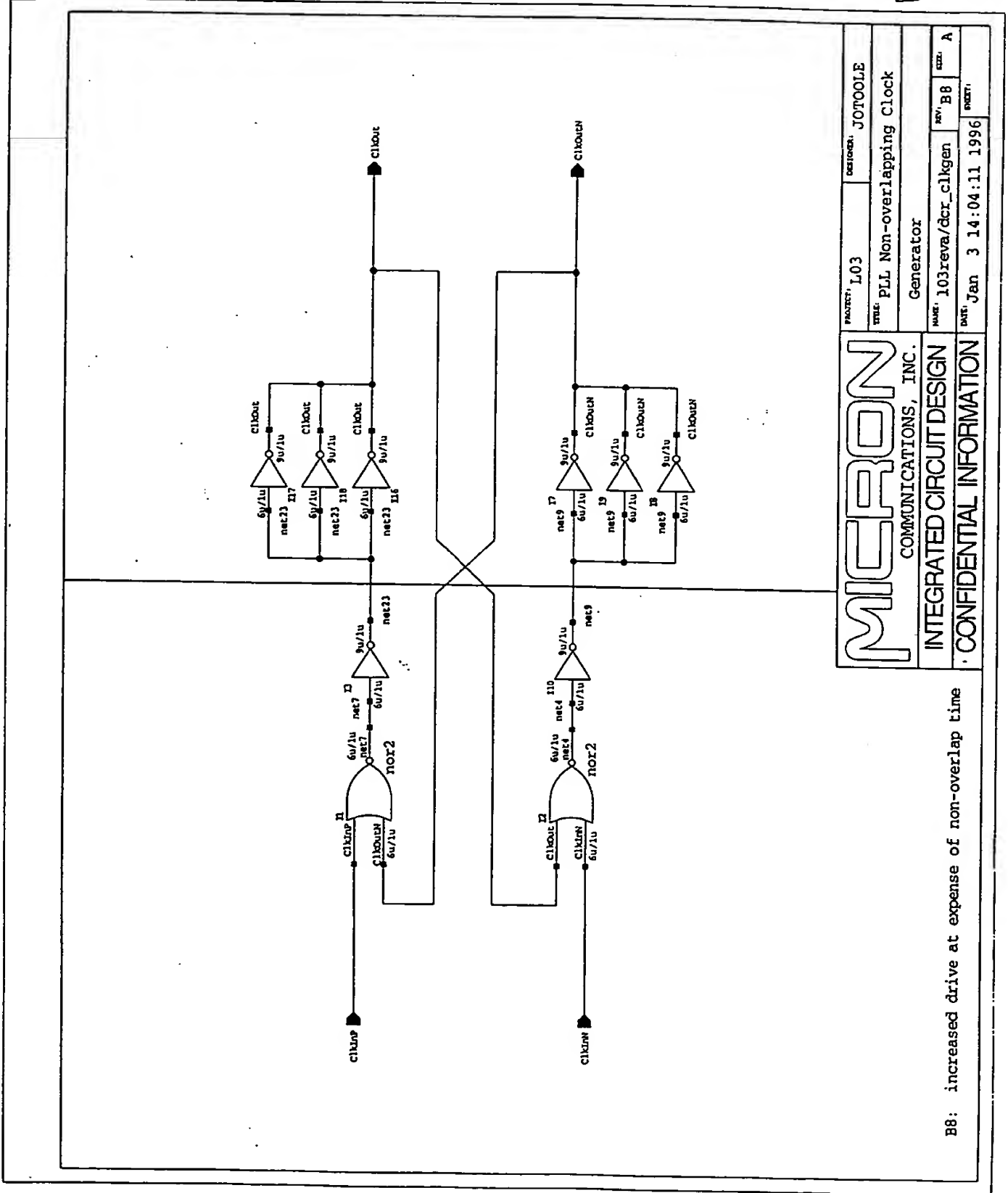
[illegible]

2025 RELEASE UNDER E.O. 14176

8.0507AB

8.0507AA

EX-88 88.0507



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TIME: PLL Non-overlapping Clock	
INTEGRATED CIRCUIT DESIGN		GENERATOR	
CONFIDENTIAL INFORMATION		NAME: 103revm/dcr_clkgen	REV: B8
		DATE: Jan 3 14:04:11 1996	SIZE: A

8.06AA	8.06AB	8.06AC	8.06AD
8.06BA	8.06BB	8.06BC	8.06BD
8.06CA	8.06CB	8.06CC	8.06CD
8.06DA	8.06DB	8.06DC	8.06DD
8.06EA	8.06EB	8.06EC	8.06ED

[illegible]

```

38: added test mode to backscatter
    modified current source
    redesigned backscatter transmitter

```

**THEAS (Backscatter) = 1.172.464**

MICROCON		LAD		J0700L6	
COMMUNICATIONS, INC.		BPSK/M/Buckmaster Transmitted			
INTEGRATED CIRCUIT DESIGN		80LX-28 - 6AA(H7/17 - 6AA(LP)			
CONFIDENTIAL INFORMATION		103 words		- 81 - 61	
		- Feb 6 13:16:11 1994			

001120 20920900

MI40-030

8.0601AA	8.0601AB
8.0601BA	8.0601BB

IL BB.01500 IL

[illegible]

B6: make unlocked driver smaller

MICRON COMMUNICATIONS, INC.		INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION		CONFIDENTIAL INFORMATION	
PART NO. L03		TRANSMITTER P/L	
IBAS-5, 4.3Mhz		103revs/collopyson	
DATE: 30 10:32:47 1995		REV: 16	
REV: 16		REV: 16	

8.060101AA	8.060101AB	8.060101AC
8.060101BA	8.060101BB	8.060101BC
8.060101CA	8.060101CB	8.060101CC



The schematic diagram illustrates a TX Phase/Frequency Detector circuit. It features a central delay element labeled "metal option 10 to bypass delay and connect inverter input to vss" and a "caps adjustable to lpr" block. The circuit is composed of several NAND gates (nand2, nand3, nand4) and inverters (in1, in2, in3, in4). The inputs are labeled with pin numbers and names: VDD, VSS, and DISABLE. The outputs are labeled with pin numbers and names: VDD, VSS, and DISABLE. The circuit is designed to detect phase and frequency errors in a TX signal.

FIG. 8.060101

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

DESIGN	103	REV. 1	DATE	10/1/81
FILE	TX Phase/Frequency Detector	FILE	TX Phase/Frequency Detector	FILE
DATE	10/1/81	DATE	10/1/81	DATE
TIME	10:10:53	TIME	10:10:53	TIME

Fig. 8.060101

<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
ITEM NO.	103
REV.	REV. 1
TX Phase/Frequency Detector	
DATE: 10/28/80	
BY: B1	
CHK: ml	
DATE: 10/28/80	
BY: B1	
CHK: ml	



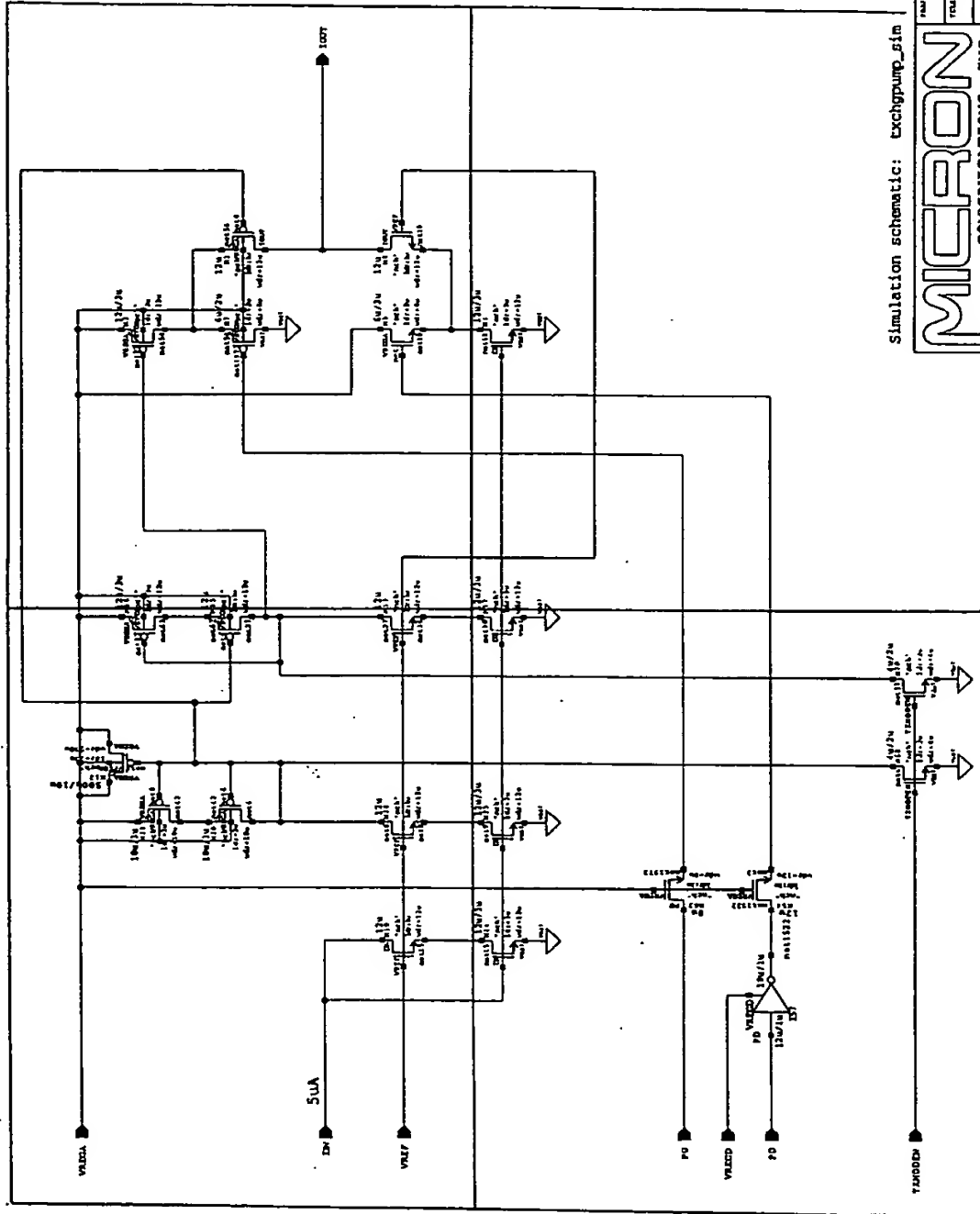


FIG. 8.060102

MICRON		COMMUNICATIONS, INC.	
PRODUCT	TX PLL Charge Pump	REVISION	JOTOOLE
DATE	1031000/txchgump	REV. B1	FILE: rll
CONFIDENTIAL INFORMATION		Feb 28 09:55:50 1995	

00423 2050550

MI40-030

8.060103AA	8.060103AB
8.060103BA	8.060103BB
8.060103CA	8.060103CB

И. П. С. 88.060103

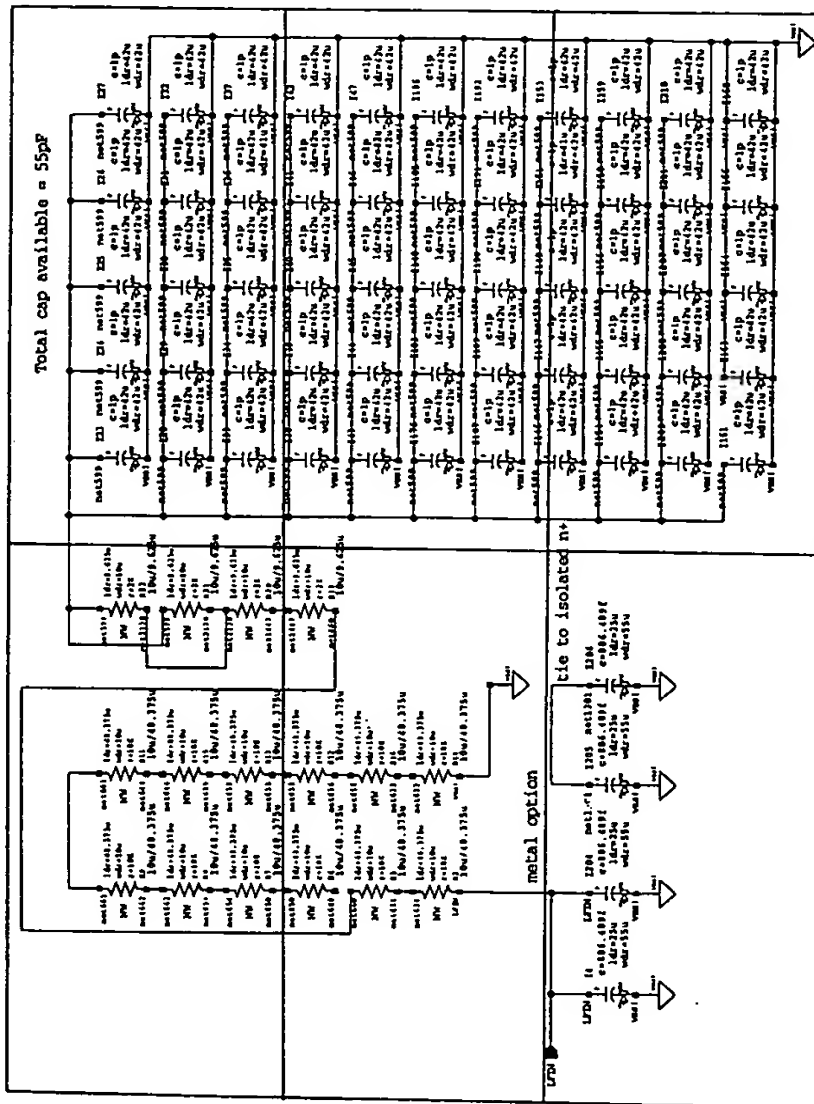


Fig. 8.060103

MICRON		PROJECT: L03	REVISION: J07001E
COMMUNICATIONS, INC.		TITLE: TX PLL Loop Filter	
INTEGRATED CIRCUIT DESIGN		BW=700KHz PM=60deg	
CONFIDENTIAL INFORMATION		DATE: 10/19/86	BY: B8
		DATE: Feb 5 14:40:11 1996	BY: B8

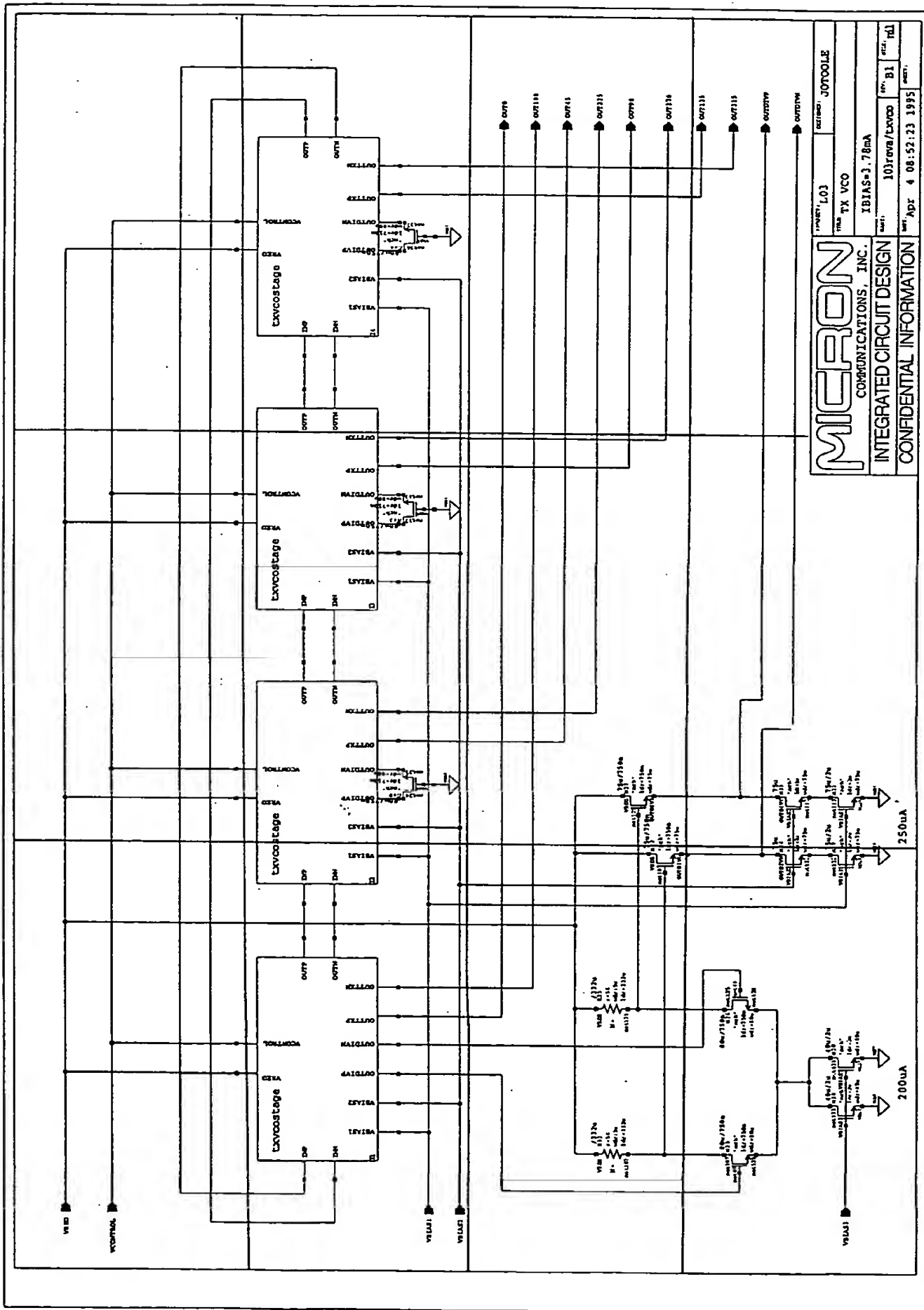
00000000000000000000

MI40-030

8.060104AA	8.060104AB	8.060104AC
8.060104BA	8.060104BB	8.060104BC
8.060104CA	8.060104CB	8.060104CC
8.060104DA	8.060104DB	8.060104DC

IL 11 11 8.060104

001120 2090300



MICRON		TX VCO	001120	2090300
COMMUNICATIONS, INC.		IBIAS=3.78mA		
INTEGRATED CIRCUIT DESIGN		10.1000/LXVCO	REV. B1	REV. D1
CONFIDENTIAL INFORMATION		Apr 4 08:52:23 1995		

Fig. 8.060104

00000000000000000000

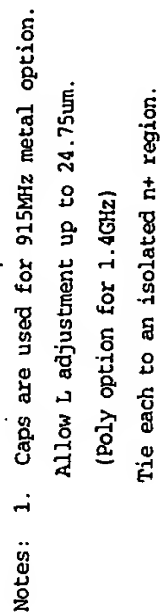
MI40-030

8.06010401AA	8.06010401AB	8.06010401AC	8.06010401AD
8.06010401BA	8.06010401BB	8.06010401BC	8.06010401BD

IL IL 88.00600110040011



150601090010901  
F16.8.06010901



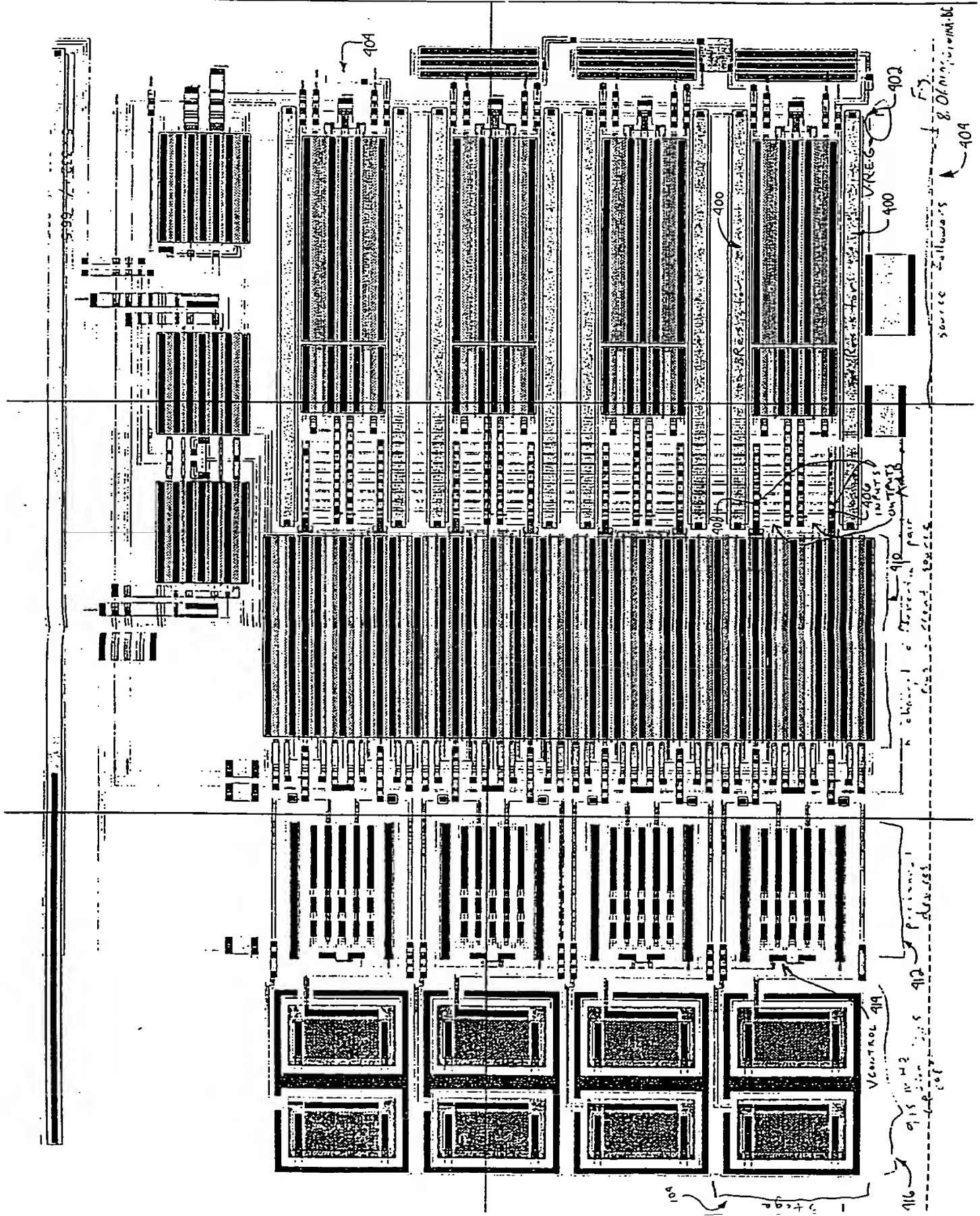
Notes: 1. Caps are used for 915MHz metal option.  
Allow L adjustment up to 24.75um.  
(Poly option for 1.4GHz)  
Tie each to an isolated n+ region.

[illegible]

MI40-030

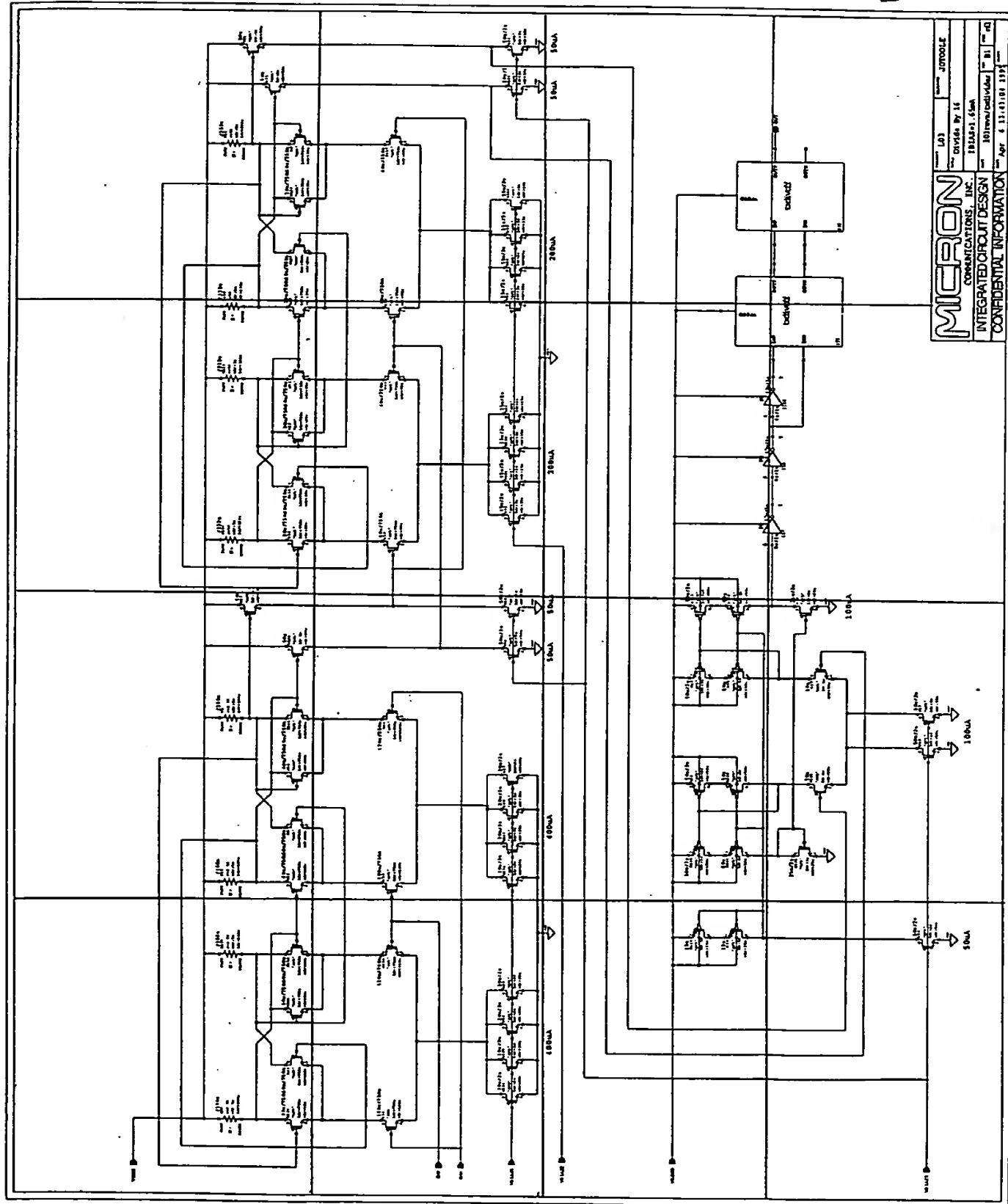
8.0601040101AA	8.0601040101AB	8.0601040101AC
8.0601040101BA	8.0601040101BB	8.0601040101BC

II II III    III. IIII IIII IIII



8.060105AA	8.060105AB	8.060105AC	8.060105AD
8.060105BA	8.060105BB	8.060105BC	8.060105BD
8.060105CA	8.060105CB	8.060105CC	8.060105CD
8.060105DA	8.060105DB	8.060105DC	8.060105DD

Fig. 8.060105

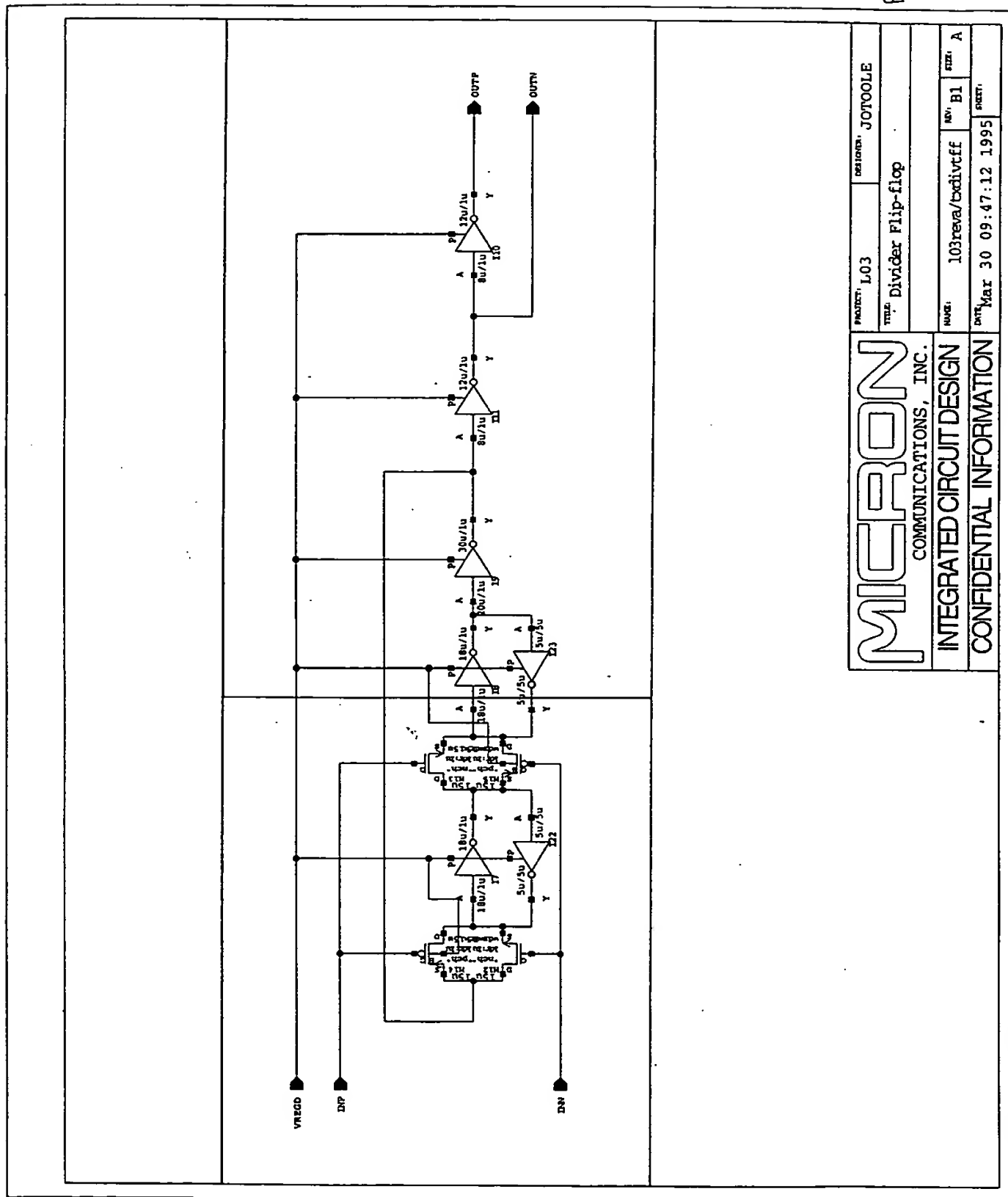


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8.06010501AA	8.06010501AB
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11.06.01.05.01

00000000000000000000



PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Divider Flip-flop			
REV: 103revA/bdvtc		REV: B1	REV: A
DATE: Mar 30 09:47:12 1995		SHEET: 1	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FIG. 8.06010501





004400 000000

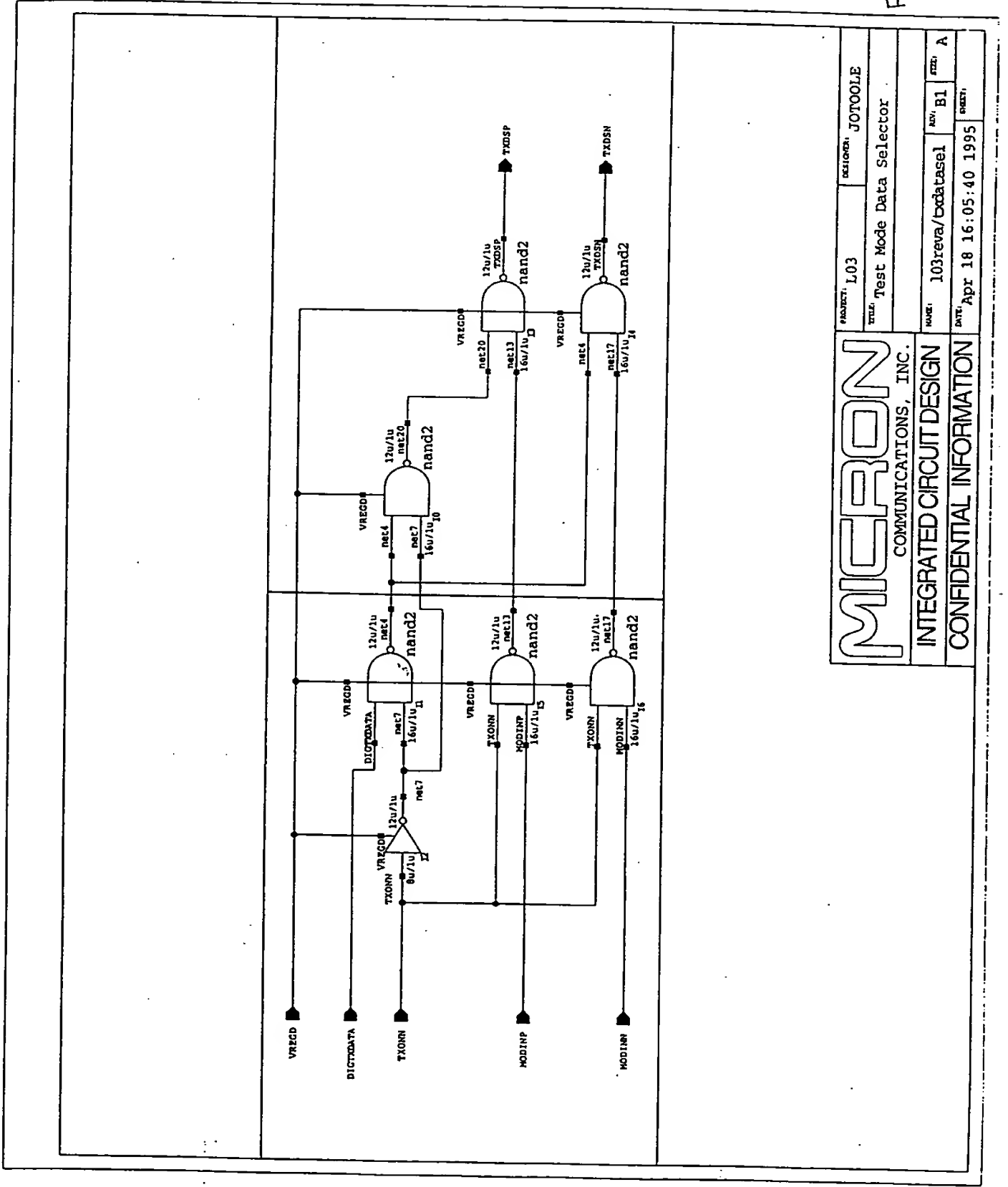


FIG. 8.0602

MICRON				PROJECT: L03		DESIGNER: JOTOOLE			
COMMUNICATIONS, INC.				TITLE: Test Mode Data Selector					
INTEGRATED CIRCUIT DESIGN				NAME: 103reva/bcdatal		REV: B1		REV: A	
CONFIDENTIAL INFORMATION				DATE: Apr 18 16:05:40 1995		SHEET:			

00000000000000000000

MI40-030

8.0603AA	8.0603AB
----------	----------

EX 8.0603

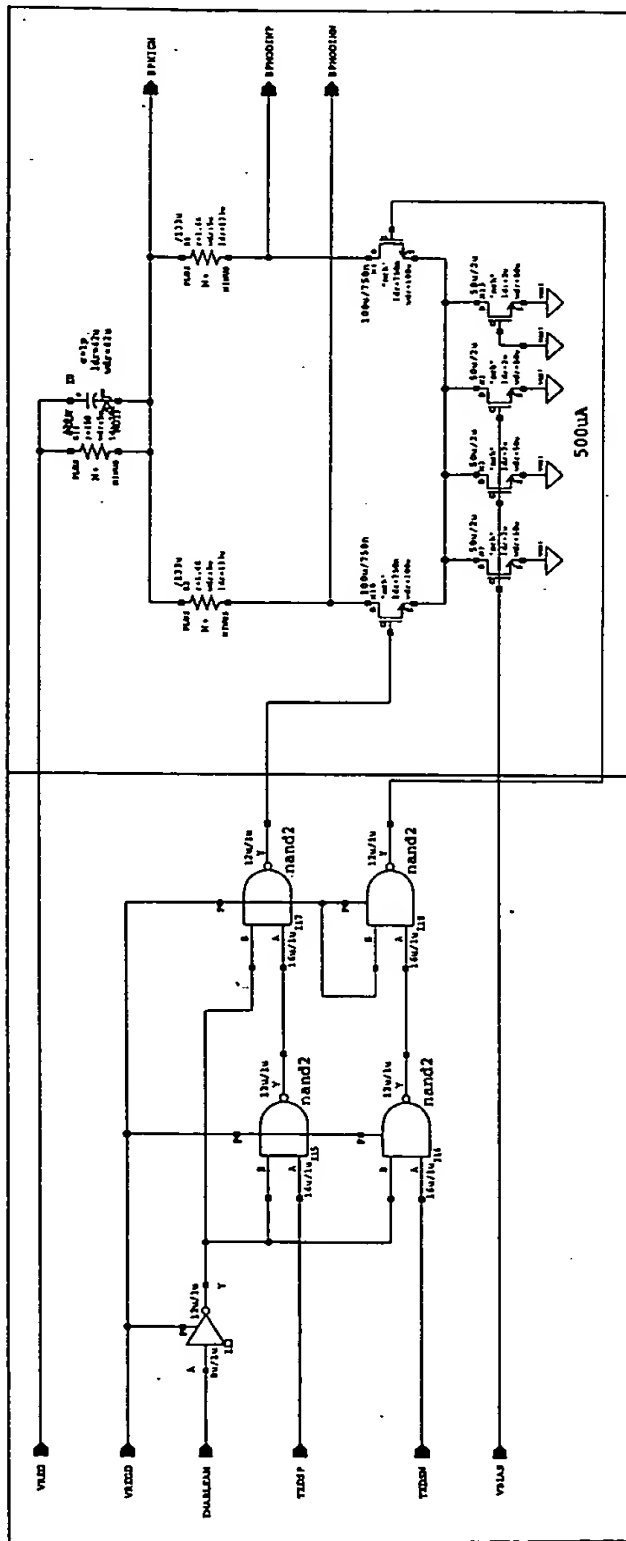


Fig. 8.0603

<b>MICRON</b> COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	PART: L03 REV: 000001	J07000LE BPSK Modulation Driver
	IDIAS=500uA	B8
	103rev/cbtpsk	B8
	Jan 18 10:28:46 1996	B8

B8: modified current source

001120 20520500

8.0604AB

8.0604AA

II II 8.0604

SECRET

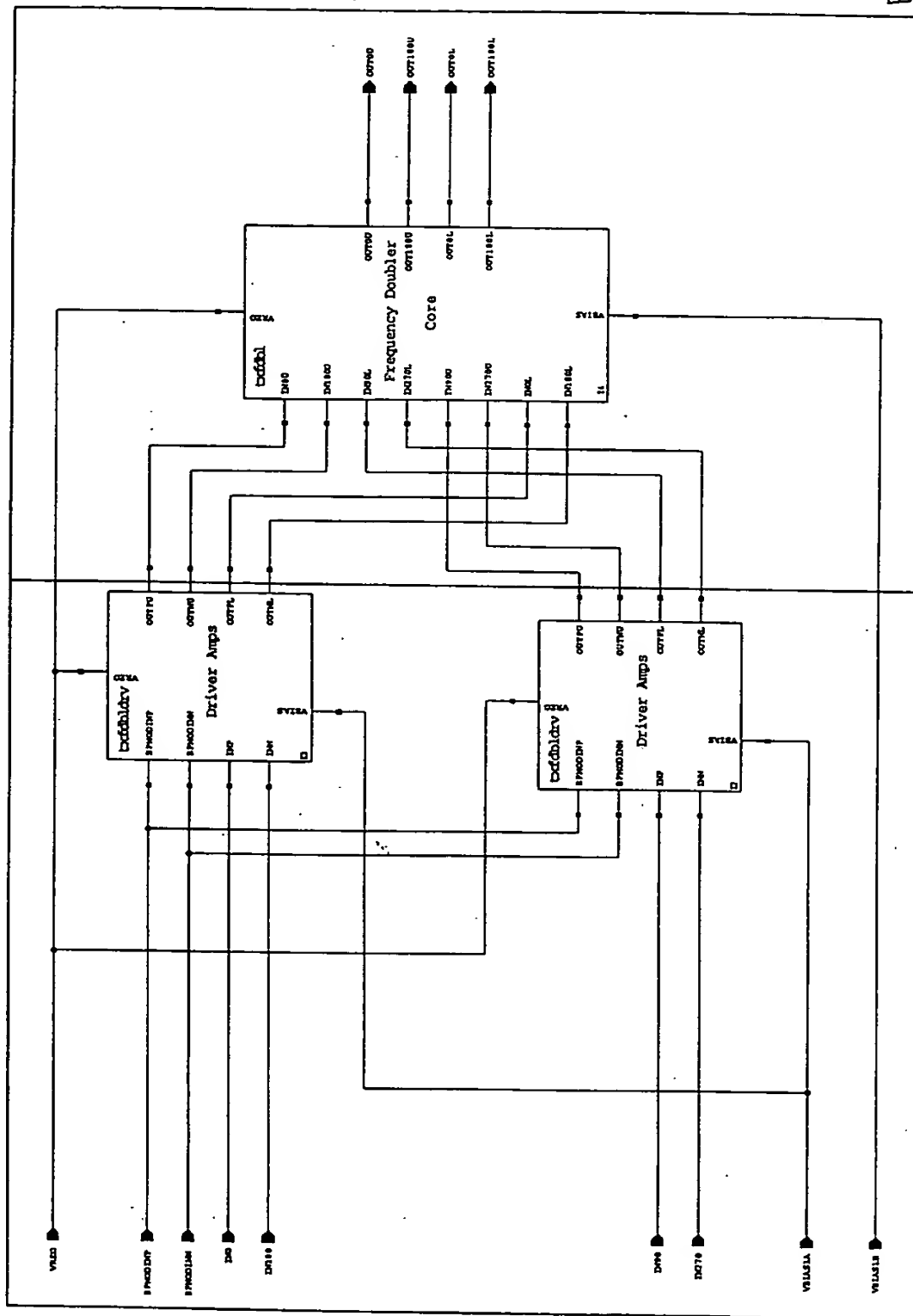
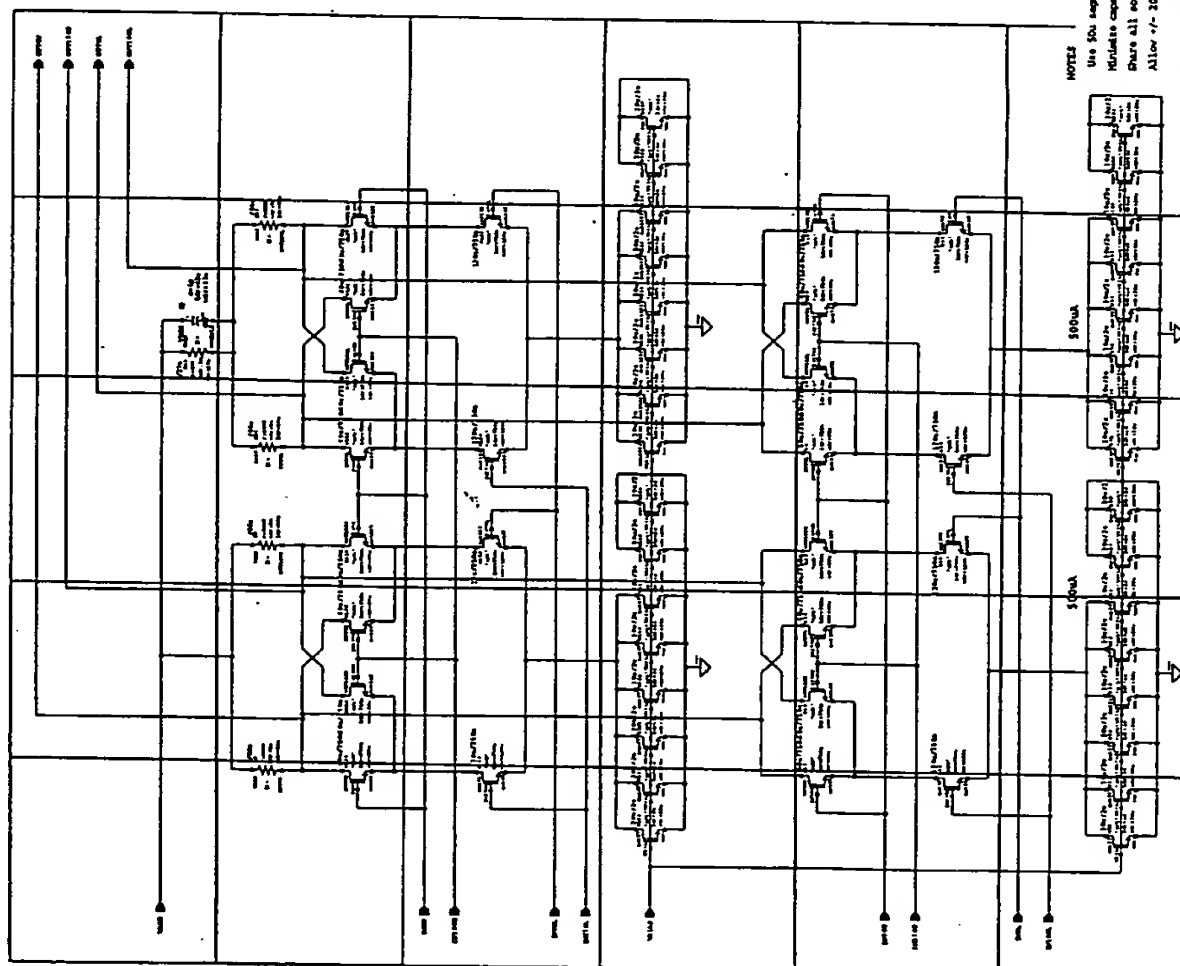


Fig. 8.0604

MICRON		PRODUCT: L03	REVISION: J07000LE
COMMUNICATIONS, INC.		TITLE: Frequency Doubler	
INTEGRATED CIRCUIT DESIGN		IBIAS=4mA	
CONFIDENTIAL INFORMATION		DATE: 103revs/bdoubler	REV: B1
		DATE: Apr 5 10:17:13 1995	REV: r11

Итого 8.060401

FIG. 8.06.0901



NOTES

- Use 500 segments for all devices.
- Minimize capacitance on all nodes (drainage have priority).
- Share all source and drain nodes.
- Allow +/- 20% adjustment on resistors at power supply

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1980: 1980-1981

001720 203235Z

8.06054A

8.06054B

IL 07 88.0605



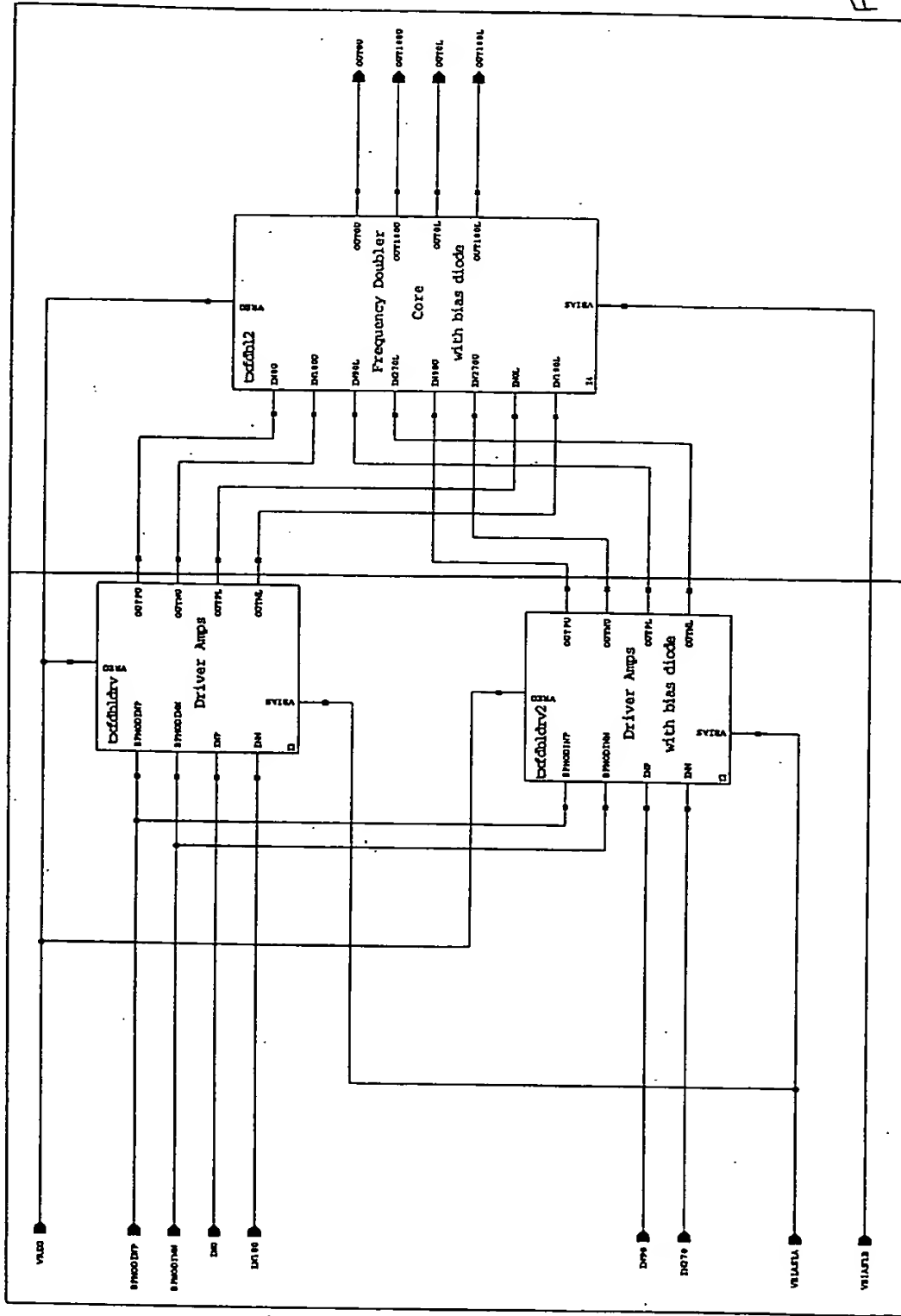


Fig. 8.0605

PART: L03		W110001, J070001E	
Title: Frequency Doubler		IBIAS=4mA	
NAME: 103rva/bcdoubler2		REV: B8	REV: rd
DATE: Jan 12 17:22:51 1996		REV: 1	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

B8: current sources modified

И. И. Б. МОСОВИ

Minimize capacitance on output nodes.  
Share all source/drain nodes.  
Allow +/- 20% adjustment on resistors  
at supply end.

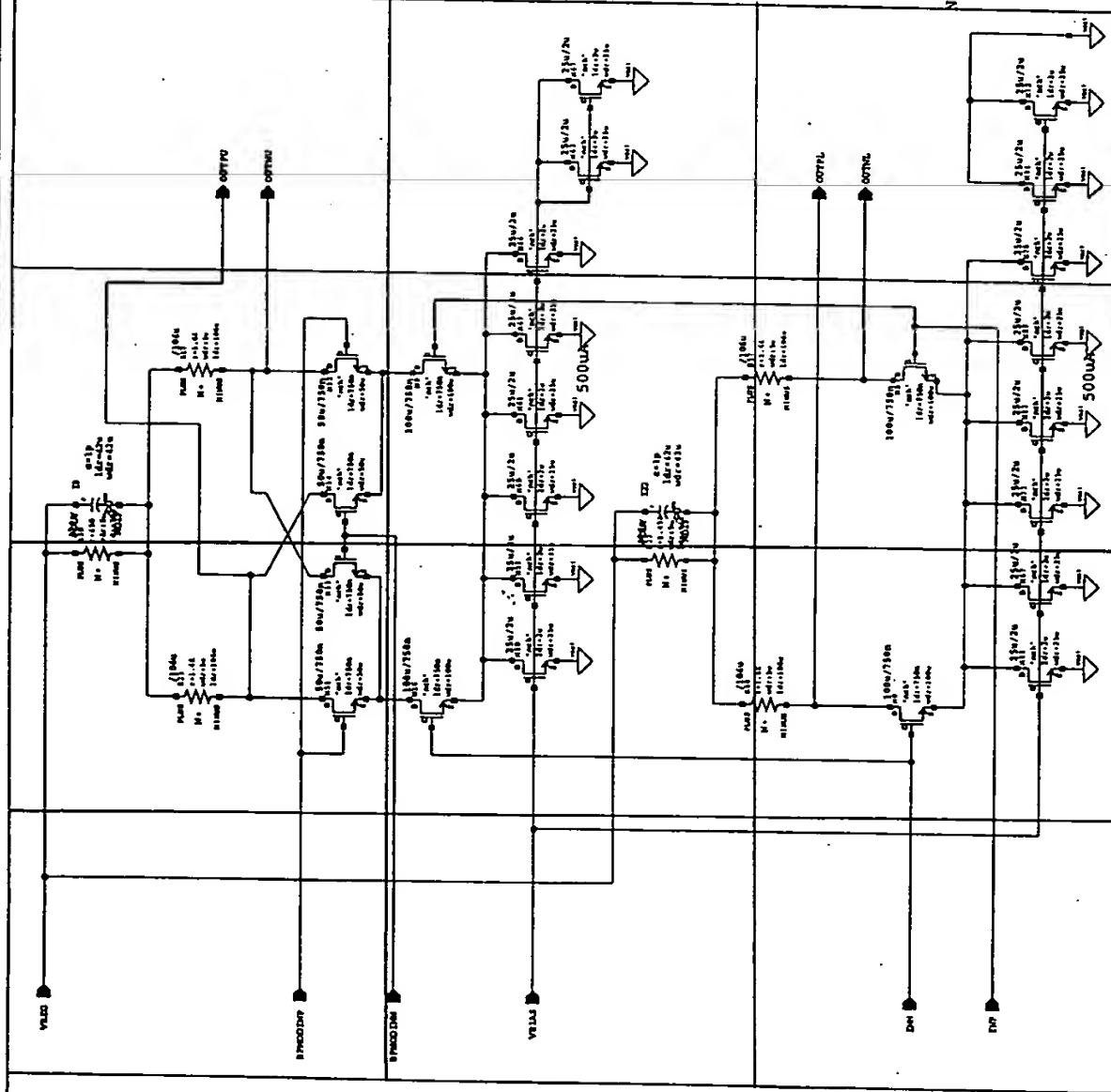
Fig. 8.060501

# NOVA

38: modified current sources

NAME:	L03		RELIGION:	JOTOOLE	
Doubler Driver Amps					
IBIAS=1mA					
NAME:	103reva/pd/coldrv		REV:	B8	
DATE:	Jan 12 15:13:26 1996		PAGE: 1		

И.И. Б.060502



NOTES  
 Minimize capacitance on output nodes.  
 Share all source/drain nodes.  
 Allow +/- 20% adjustment on resistors  
 at supply end.

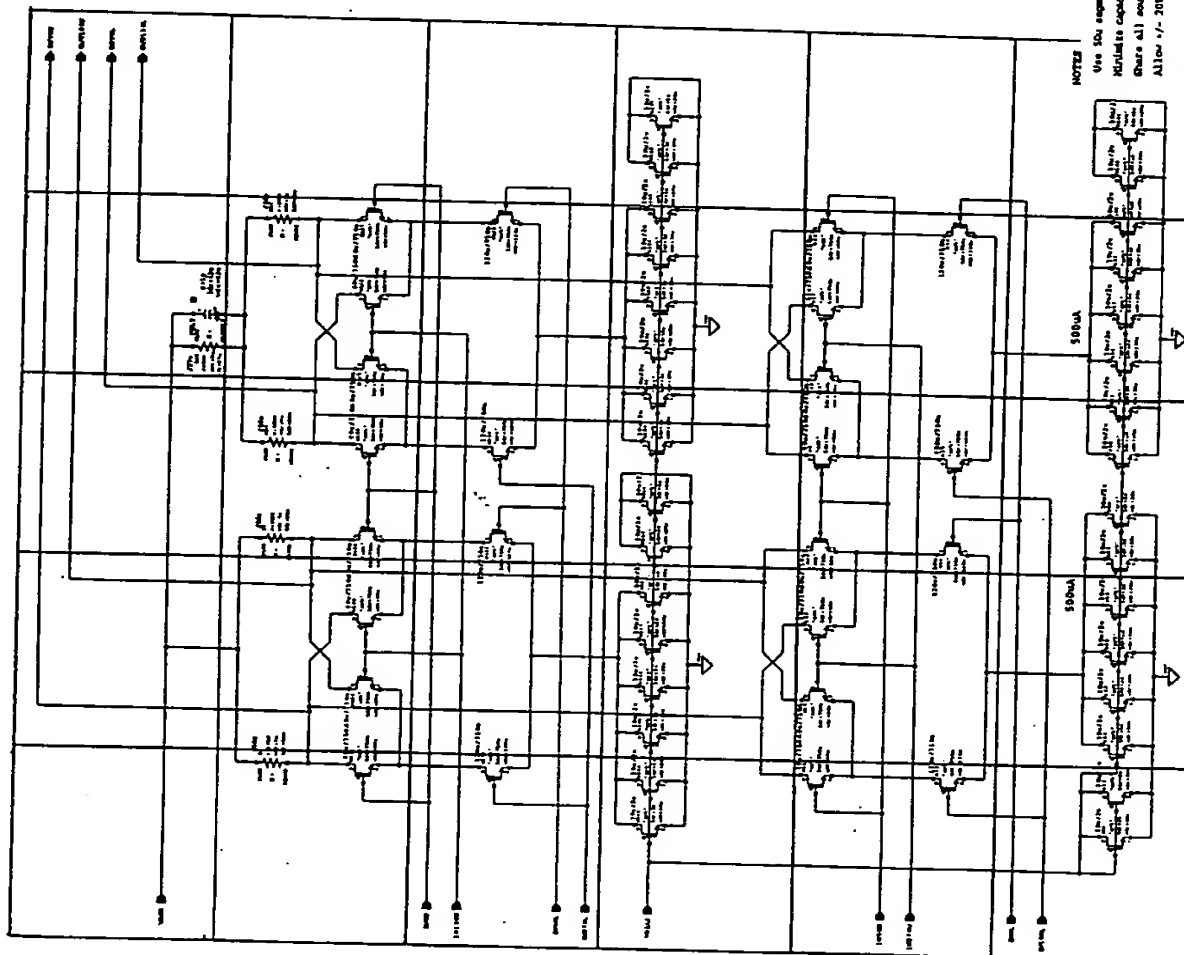
FIG. 8.06 050a

MICRON		PROPERTY L03	REVISION J0700LE
COMMUNICATIONS, INC.		Title Double Driver Amps	
INTEGRATED CIRCUIT DESIGN		IBIAS=1mA	
CONFIDENTIAL INFORMATION		part 103rev0/bx6b01drv2	part B8
		date Jan 18 08:22:12 1996	part

B8: modified current sources

Итого 8.060503

FIG. 8.060503



NOTES

- Use 50 $\mu$  squares (or all) devices.
- Minimize capacitance on all nodes (drains have priority).
- Share all source and drain nodes.
- Allow +/- 20% adjustment on resistors at power supply end.

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FORM NO. 10-72  
REV. 10-72

DATE: 10-72  
PAGE: 10-72

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ADDRESS: [REDACTED]  
CITY: [REDACTED] STATE: [REDACTED] ZIP: [REDACTED]

TELEPHONE: [REDACTED]  
FAX: [REDACTED]

EMAIL: [REDACTED]

COMPANY: [REDACTED]  
DEPARTMENT: [REDACTED]  
TITLE: [REDACTED]

PROJECT: [REDACTED]  
SUBJECT: [REDACTED]  
REFERENCE: [REDACTED]

APPROVAL: [REDACTED]  
DATE: [REDACTED]

REVISIONS:

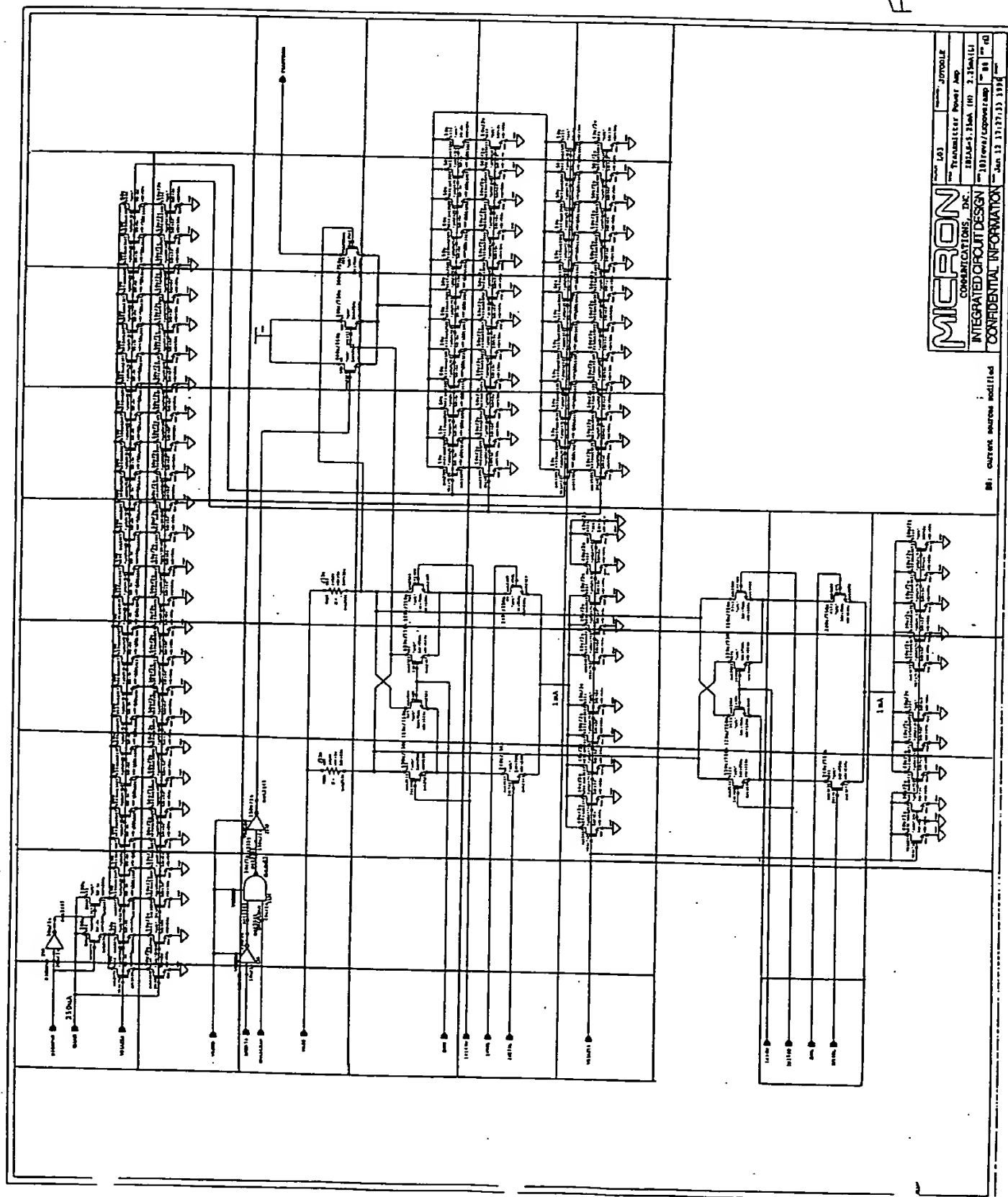
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**medische Fakultät**

8.0606AA	8.0606AB	8.0606AC	8.0606AD	8.0606AE	8.0606AF	8.0606AG	8.0606AH
8.0606BA	8.0606BB	8.0606BC	8.0606BD	8.0606BE	8.0606BF	8.0606BG	8.0606BH
8.0606CA	8.0606CB	8.0606CC	8.0606CD	8.0606CE	8.0606CF	8.0606CG	8.0606CH
8.0606DA	8.0606DB	8.0606DC	8.0606DD	8.0606DE	8.0606DF	8.0606DG	8.0606DH
8.0606EA	8.0606EB	8.0606EC	8.0606ED	8.0606EE	8.0606EF	8.0606EG	8.0606EH
8.0606FA	8.0606FB	8.0606FC	8.0606FD	8.0606FE	8.0606FF	8.0606FG	8.0606FH
		8.0606GC	8.0606GD	8.0606GE			
8.0606HA	8.0606HB	8.0606HC	8.0606HD	8.0606HE			
	8.0606IB	8.0606IC	8.0606ID	8.0606IE			



FIG. 8.0606



8.0607AA	8.0607AB	8.0607AC	8.0607AD	8.0607AE	8.0607AF	8.0607AG	8.0607AH	8.0607AI	8.0607AJ
8.0607BA	8.0607BB	8.0607BC	8.0607BD	8.0607BE	8.0607BF	8.0607BG	8.0607BH	8.0607BI	8.0607BJ
8.0607CA	8.0607CB	8.0607CC	8.0607CD	8.0607CE	8.0607CF	8.0607CG	8.0607CH	8.0607CI	8.0607CJ
8.0607DA	8.0607DB	8.0607DC	8.0607DD	8.0607DE	8.0607DF	8.0607DG	8.0607DH	8.0607DI	8.0607DJ
8.0607EA	8.0607EB	8.0607EC	8.0607ED	8.0607EE	8.0607EF	8.0607EG	8.0607EH	8.0607EI	8.0607EJ
8.0607FA	8.0607FB	8.0607FC	8.0607FD	8.0607FE	8.0607FF	8.0607FG	8.0607FH	8.0607FI	8.0607FJ
8.0607GA	8.0607GB	8.0607GC	8.0607GD	8.0607GE	8.0607GF	8.0607GG	8.0607GH	8.0607GI	8.0607GJ
8.0607HA	8.0607HB	8.0607HC	8.0607HD	8.0607HE	8.0607HF	8.0607HG	8.0607HH	8.0607HI	8.0607HJ
8.0607IA	8.0607IB	8.0607IC	8.0607ID	8.0607IE	8.0607IF	8.0607IG	8.0607IH	8.0607II	8.0607IJ
8.0607JA	8.0607JB	8.0607JC	8.0607JD	8.0607JE	8.0607JF	8.0607JG	8.0607JH	8.0607JI	8.0607JJ

The diagram is a complex integrated circuit (IC) layout, likely for a microprocessor or memory controller. It features a dense grid of logic gates, flip-flops, and other digital components, interconnected by a network of lines. The components are labeled with various identifiers, including gate names (e.g., 11.1, 11.2, 11.3, 11.4, 11.5, 11.6, 11.7, 11.8, 11.9, 11.10, 11.11, 11.12, 11.13, 11.14, 11.15, 11.16, 11.17, 11.18, 11.19, 11.20, 11.21, 11.22, 11.23, 11.24, 11.25, 11.26, 11.27, 11.28, 11.29, 11.30, 11.31, 11.32, 11.33, 11.34, 11.35, 11.36, 11.37, 11.38, 11.39, 11.40, 11.41, 11.42, 11.43, 11.44, 11.45, 11.46, 11.47, 11.48, 11.49, 11.50, 11.51, 11.52, 11.53, 11.54, 11.55, 11.56, 11.57, 11.58, 11.59, 11.60, 11.61, 11.62, 11.63, 11.64, 11.65, 11.66, 11.67, 11.68, 11.69, 11.70, 11.71, 11.72, 11.73, 11.74, 11.75, 11.76, 11.77, 11.78, 11.79, 11.80, 11.81, 11.82, 11.83, 11.84, 11.85, 11.86, 11.87, 11.88, 11.89, 11.90, 11.91, 11.92, 11.93, 11.94, 11.95, 11.96, 11.97, 11.98, 11.99, 12.0, 12.1, 12.2, 12.3, 12.4, 12.5, 12.6, 12.7, 12.8, 12.9, 12.10, 12.11, 12.12, 12.13, 12.14, 12.15, 12.16, 12.17, 12.18, 12.19, 12.20, 12.21, 12.22, 12.23, 12.24, 12.25, 12.26, 12.27, 12.28, 12.29, 12.30, 12.31, 12.32, 12.33, 12.34, 12.35, 12.36, 12.37, 12.38, 12.39, 12.40, 12.41, 12.42, 12.43, 12.44, 12.45, 12.46, 12.47, 12.48, 12.49, 12.50, 12.51, 12.52, 12.53, 12.54, 12.55, 12.56, 12.57, 12.58, 12.59, 12.60, 12.61, 12.62, 12.63, 12.64, 12.65, 12.66, 12.67, 12.68, 12.69, 12.70, 12.71, 12.72, 12.73, 12.74, 12.75, 12.76, 12.77, 12.78, 12.79, 12.80, 12.81, 12.82, 12.83, 12.84, 12.85, 12.86, 12.87, 12.88, 12.89, 12.90, 12.91, 12.92, 12.93, 12.94, 12.95, 12.96, 12.97, 12.98, 12.99, 13.0, 13.1, 13.2, 13.3, 13.4, 13.5, 13.6, 13.7, 13.8, 13.9, 13.10, 13.11, 13.12, 13.13, 13.14, 13.15, 13.16, 13.17, 13.18, 13.19, 13.20, 13.21, 13.22, 13.23, 13.24, 13.25, 13.26, 13.27, 13.28, 13.29, 13.30, 13.31, 13.32, 13.33, 13.34, 13.35, 13.36, 13.37, 13.38, 13.39, 13.40, 13.41, 13.42, 13.43, 13.44, 13.45, 13.46, 13.47, 13.48, 13.49, 13.50, 13.51, 13.52, 13.53, 13.54, 13.55, 13.56, 13.57, 13.58, 13.59, 13.60, 13.61, 13.62, 13.63, 13.64, 13.65, 13.66, 13.67, 13.68, 13.69, 13.70, 13.71, 13.72, 13.73, 13.74, 13.75, 13.76, 13.77, 13.78, 13.79, 13.80, 13.81, 13.82, 13.83, 13.84, 13.85, 13.86, 13.87, 13.88, 13.89, 13.90, 13.91, 13.92, 13.93, 13.94, 13.95, 13.96, 13.97, 13.98, 13.99, 14.0, 14.1, 14.2, 14.3, 14.4, 14.5, 14.6, 14.7, 14.8, 14.9, 14.10, 14.11, 14.12, 14.13, 14.14, 14.15, 14.16, 14.17, 14.18, 14.19, 14.20, 14.21, 14.22, 14.23, 14.24, 14.25, 14.26, 14.27, 14.28, 14.29, 14.30, 14.31, 14.32, 14.33, 14.34, 14.35, 14.36, 14.37, 14.38, 14.39, 14.40, 14.41, 14.42, 14.43, 14.44, 14.45, 14.46, 14.47, 14.48, 14.49, 14.50, 14.51, 14.52, 14.53, 14.54, 14.55, 14.56, 14.57, 14.58, 14.59, 14.60, 14.61, 14.62, 14.63, 14.64, 14.65, 14.66, 14.67, 14.68, 14.69, 14.70, 14.71, 14.72, 14.73, 14.74, 14.75, 14.76, 14.77, 14.78, 14.79, 14.80, 14.81, 14.82, 14.83, 14.84, 14.85, 14.86, 14.87, 14.88, 14.89, 14.90, 14.91, 14.92, 14.93, 14.94, 14.95, 14.96, 14.97, 14.98, 14.99, 15.0, 15.1, 15.2, 15.3, 15.4, 15.5, 15.6, 15.7, 15.8, 15.9, 15.10, 15.11, 15.12, 15.13, 15.14, 15.15, 15.16, 15.17, 15.18, 15.19, 15.20, 15.21, 15.22, 15.23, 15.24, 15.25, 15.26, 15.27, 15.28, 15.29, 15.30, 15.31, 15.32, 15.33, 15.34, 15.35, 15.36, 15.37, 15.38, 15.39, 15.40, 15.41, 15.42, 15.43, 15.44, 15.45, 15.46, 15.47, 15.48, 15.49, 15.50, 15.51, 15.52, 15.53, 15.54, 15.55, 15.56, 15.57, 15.58, 15.59, 15.60, 15.61, 15.62, 15.63, 15.64, 15.65, 15.66, 15.67, 15.68, 15.69, 15.70, 15.71, 15.72, 15.73, 15.74, 15.75, 15.76, 15.77, 15.78, 15.79, 15.80, 15.81, 15.82, 15.83, 15.84, 15.85, 15.86, 15.87, 15.88, 15.89, 15.90, 15.91, 15.92, 15.93, 15.94, 15.95, 15.96, 15.97, 15.98, 15.99, 16.0, 16.1, 16.2, 16.3, 16.4, 16.5, 16.6, 16.7, 16.8, 16.9, 16.10, 16.11, 16.12, 16.13, 16.14, 16.15, 16.16, 16.17, 16.18, 16.19, 16.20, 16.21, 16.22, 16.23, 16.24, 16.25, 16.26, 16.27, 16.28, 16.29, 16.30, 16.31, 16.32, 16.33, 16.34, 16.35, 16.36, 16.37, 16.38, 16.39, 16.40, 16.41, 16.42, 16.43, 16.44, 16.45, 16.46, 16.47, 16.48, 16.49, 16.50, 16.51, 16.52, 16.53, 16.54, 16.55, 16.56, 16.57, 16.58, 16.59, 16.60, 16.61, 16.62, 16.63, 16.64, 16.65, 16.66, 16.67, 16.68, 16.69, 16.70, 16.71, 16.72, 16.73, 16.74, 16.75, 16.76, 16.77

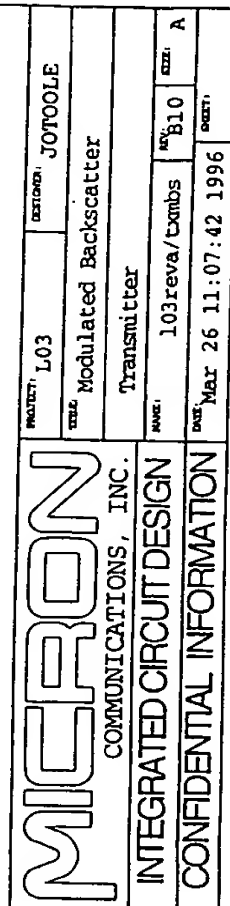
Fig. 8.0607

00110 2020300

8.0608AA	8.0608AB
8.0608BA	8.0608BB

IL 10 88.0160088

Fig. 8.0608



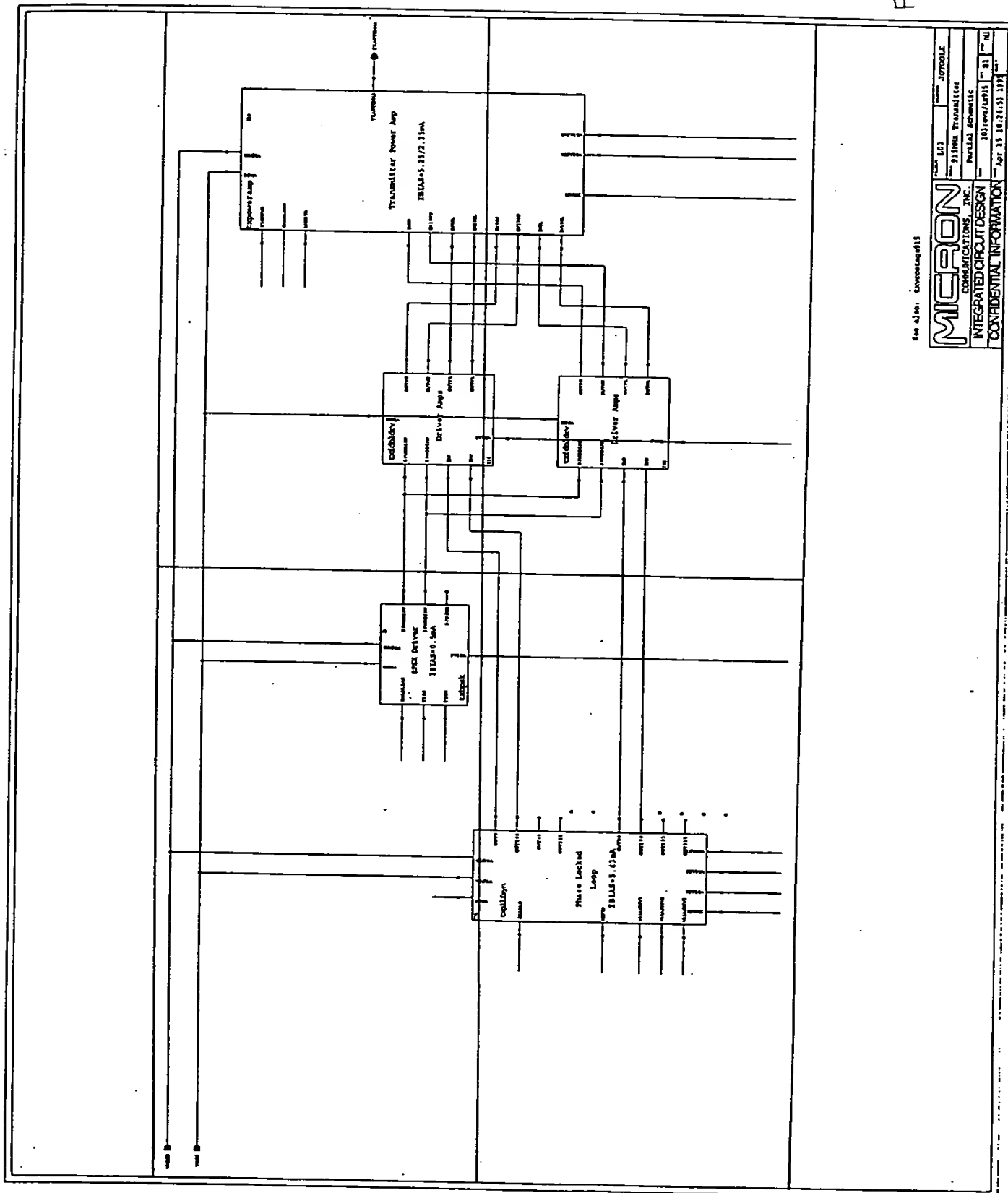
**B10: disconnected inverter**

00000000000000000000

8.07AA	8.07AB
8.07BA	8.07BB

BB.007

FIG. 8.07



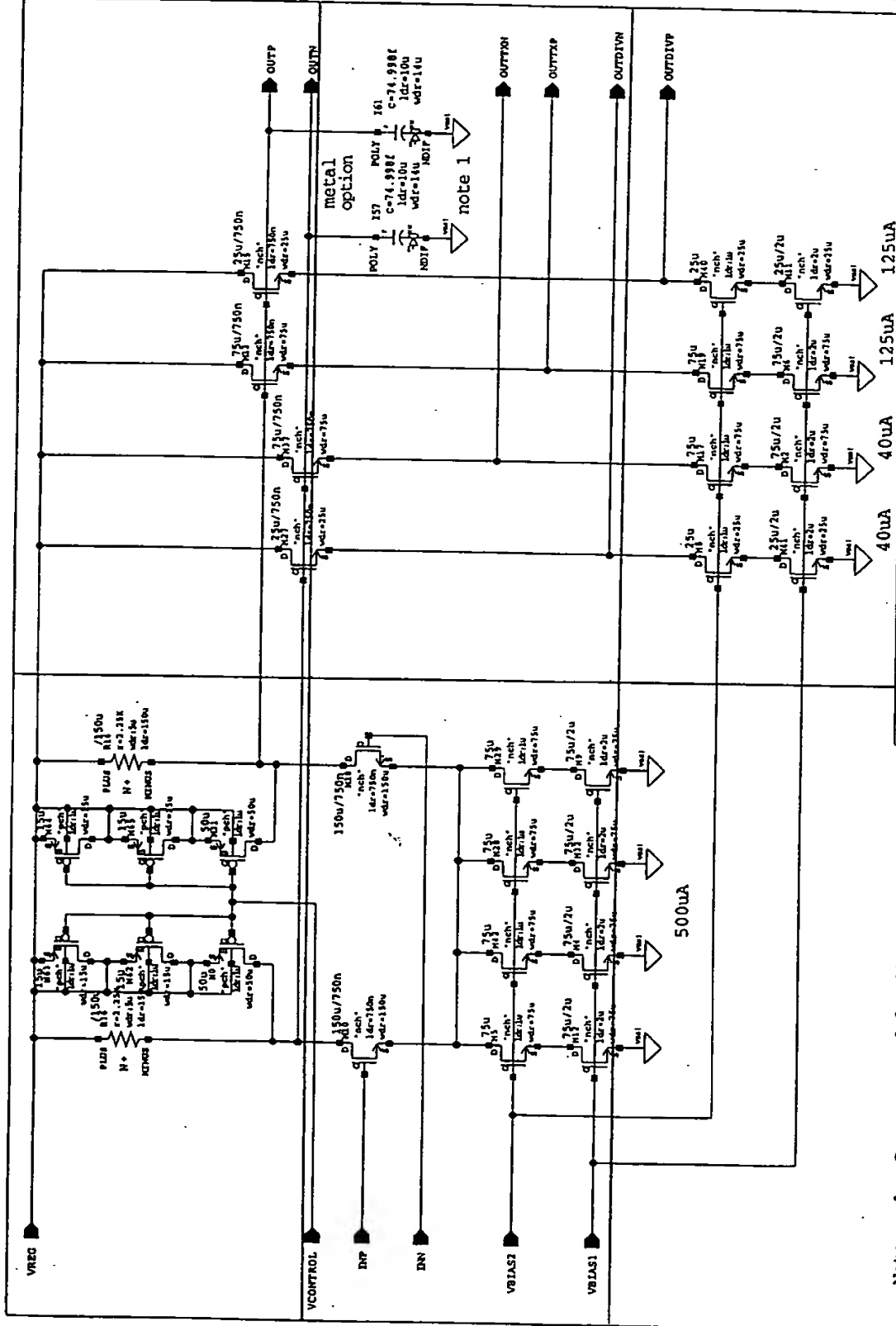
001120-20920000

MI40-030

8.0701AA	8.0701AB
8.0701BA	8.0701BB
8.0701CA	8.0701CB

IF 11 8.070100





Notes: 1. Caps are used for 915MHz metal option.  
 Allow L adjustment up to 14um.  
 Tie each to an isolated n+ region.

**MICRON**  
 COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

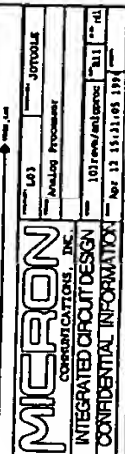
PROJECT: L03	DESIGNED: JOTOOLE
TITLE: TX VCO Stage for 915MHz	
IBIAS=833uA	
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DATE: Apr 15 10:23:19 1995	SHEET: A

001120 00000000

9AA	9AB
9BA	9BB
9CA	9CB

11 11 11 11

Fig. 9



9.01AA	9.01AB	9.01AC	9.01AD	9.01AE	9.01AF	9.01AG	9.01AH
9.01BA	9.01BB	9.01BC	9.01BD	9.01BE	9.01BF	9.01BG	9.01BH
9.01CA	9.01CB	9.01CC	9.01CD	9.01CE	9.01CF	9.01CG	9.01CH
9.01DA	9.01DB	9.01DC	9.01DD	9.01DE	9.01DF	9.01DG	9.01DH

The diagram is a complex schematic of the MC146010 integrated circuit. It features several key functional blocks:
 

- COMPARATOR:** Located in the upper left, it includes a differential input stage and a decision logic block.
- 12/0 XI VOLTAGE AMPLIFIER:** Positioned in the middle left, it contains a differential input stage and a decision logic block.
- SAMPLE & HOLD:** Situated in the lower left, it includes a differential input stage and a decision logic block.
- Internal Logic:** The central and right portions of the chip are filled with a dense network of logic gates, multiplexers, and control logic, all interconnected with a central bus system.
- Pinout:** A detailed list of pins is provided on the right side, including power supply pins (VCC, GND), control pins (CS, S, H, L, M, P, Q, R, S, T, U, V, W, X, Y, Z), and data pins (D0-D15).
- Title Block:** Located at the bottom right, it contains the Micron logo, the part number MC146010, and other identifying information.

106.913

9.0101AA	9.0101AB	9.0101AC	9.0101AD	9.0101AE	9.0101AF	9.0101AG	9.0101AH	9.0101AI	9.0101AJ	9.0101AK
9.0101BA	9.0101BB	9.0101BC	9.0101BD	9.0101BE	9.0101BF	9.0101BG	9.0101BH	9.0101BI	9.0101BJ	9.0101BK
9.0101CA	9.0101CB	9.0101CC	9.0101CD	9.0101CE	9.0101CF	9.0101CG	9.0101CH	9.0101CI	9.0101CJ	9.0101CK

FIG. 9.0101

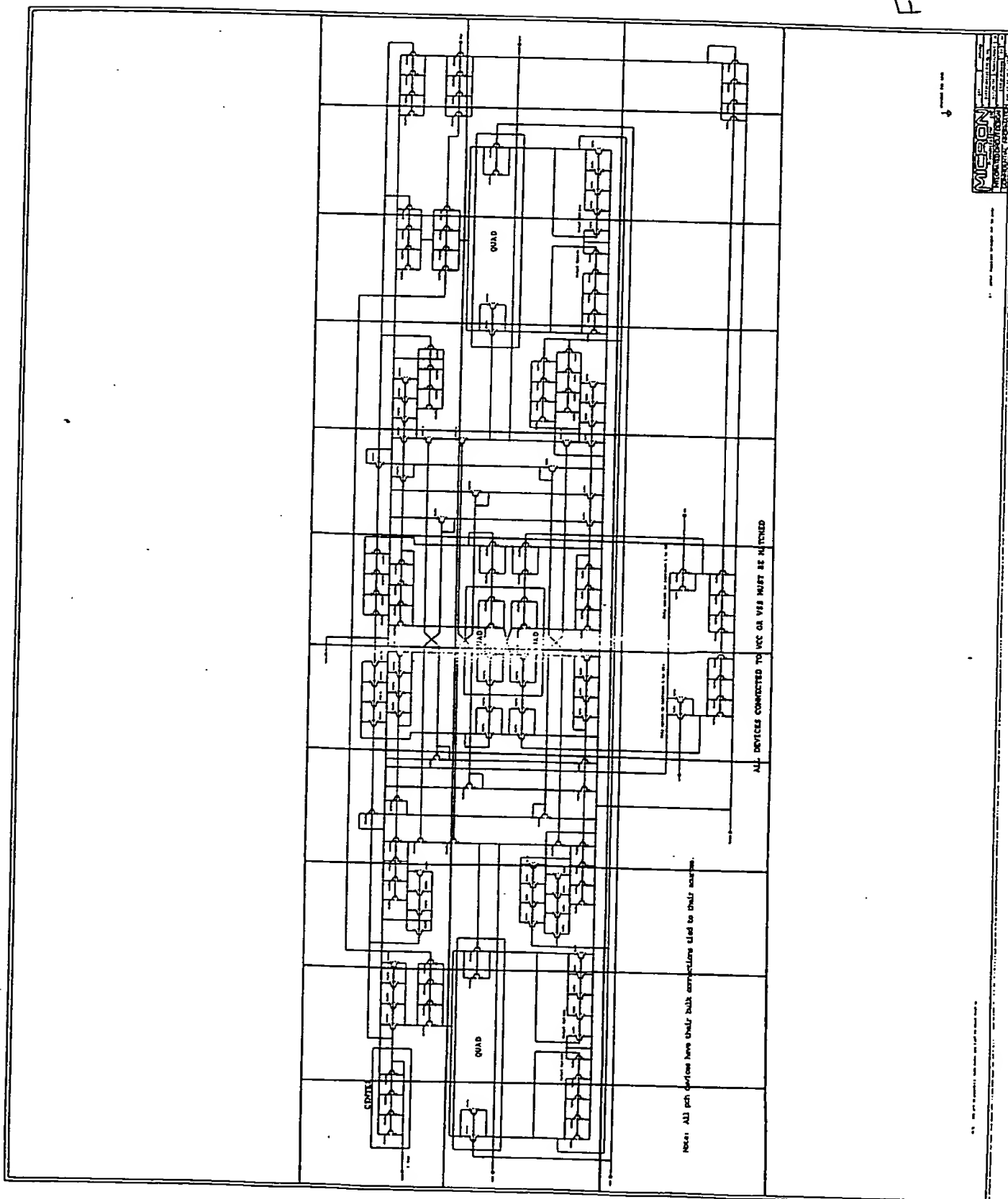
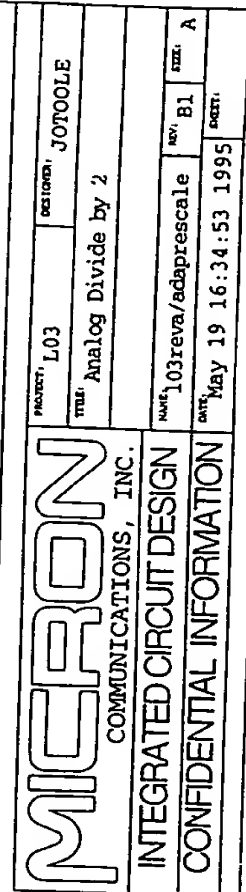


Fig. 9.0102





9.0103BA	9.0103BB	9.0103BC	9.0103BD	9.0103BE	9.0103BF	9.0103BG	9.0103BH
9.0103CA	9.0103CB	9.0103CC	9.0103CD	9.0103CE	9.0103CF	9.0103CG	9.0103CH
9.0103DA	9.0103DB	9.0103DC	9.0103DD	9.0103DE	9.0103DF	9.0103DG	9.0103DH
9.0103EA	9.0103EB	9.0103EC	9.0103ED	9.0103EE	9.0103EF	9.0103EG	9.0103EH
9.0103FA	9.0103FB	9.0103FC	9.0103FD	9.0103FE	9.0103FF	9.0103FG	9.0103FH
9.0103JA	9.0103JB	9.0103JC	9.0103JD	9.0103JE	9.0103JF	9.0103JG	9.0103JH
9.0103KA	9.0103KB	9.0103KC	9.0103KD	9.0103KE	9.0103KF	9.0103KG	9.0103KH
9.0103LA	9.0103LB	9.0103LC	9.0103LD	9.0103LE	9.0103LF	9.0103LG	9.0103LH
9.0103MA	9.0103MB	9.0103MC	9.0103MD	9.0103ME	9.0103MF	9.0103MG	9.0103MH
9.0103NA	9.0103NB	9.0103NC	9.0103ND	9.0103NE	9.0103NF	9.0103NG	9.0103NH
9.0103OA	9.0103OB	9.0103OC	9.0103OD	9.0103OE	9.0103OF	9.0103OG	9.0103OH
9.0103PA	9.0103PB	9.0103PC	9.0103PD	9.0103PE	9.0103PF	9.0103PG	9.0103PH
9.0103QA	9.0103QB	9.0103QC	9.0103QD	9.0103QE	9.0103QF	9.0103QG	9.0103QH
9.0103RA	9.0103RB	9.0103RC	9.0103RD	9.0103RE	9.0103RF	9.0103RG	9.0103RH
9.0103SA	9.0103SB	9.0103SC	9.0103SD	9.0103SE	9.0103SF	9.0103SG	9.0103SH
9.0103TA	9.0103TB	9.0103TC	9.0103TD	9.0103TE	9.0103TF	9.0103TG	9.0103TH
9.0103UA	9.0103UB	9.0103UC	9.0103UD	9.0103UE	9.0103UF	9.0103UG	9.0103UH
9.0103VA	9.0103VB	9.0103VC	9.0103VD	9.0103VE	9.0103VF	9.0103VG	9.0103VH
9.0103WA	9.0103WB	9.0103WC	9.0103WD	9.0103WE	9.0103WF	9.0103WG	9.0103WH
9.0103XA	9.0103XB	9.0103XC	9.0103XD	9.0103XE	9.0103XF	9.0103XG	9.0103XH
9.0103YA	9.0103YB	9.0103YC	9.0103YD	9.0103YE	9.0103YF	9.0103YG	9.0103YH
9.0103ZA	9.0103ZB	9.0103ZC	9.0103ZD	9.0103ZE	9.0103ZF	9.0103ZG	9.0103ZH

007420 20520500

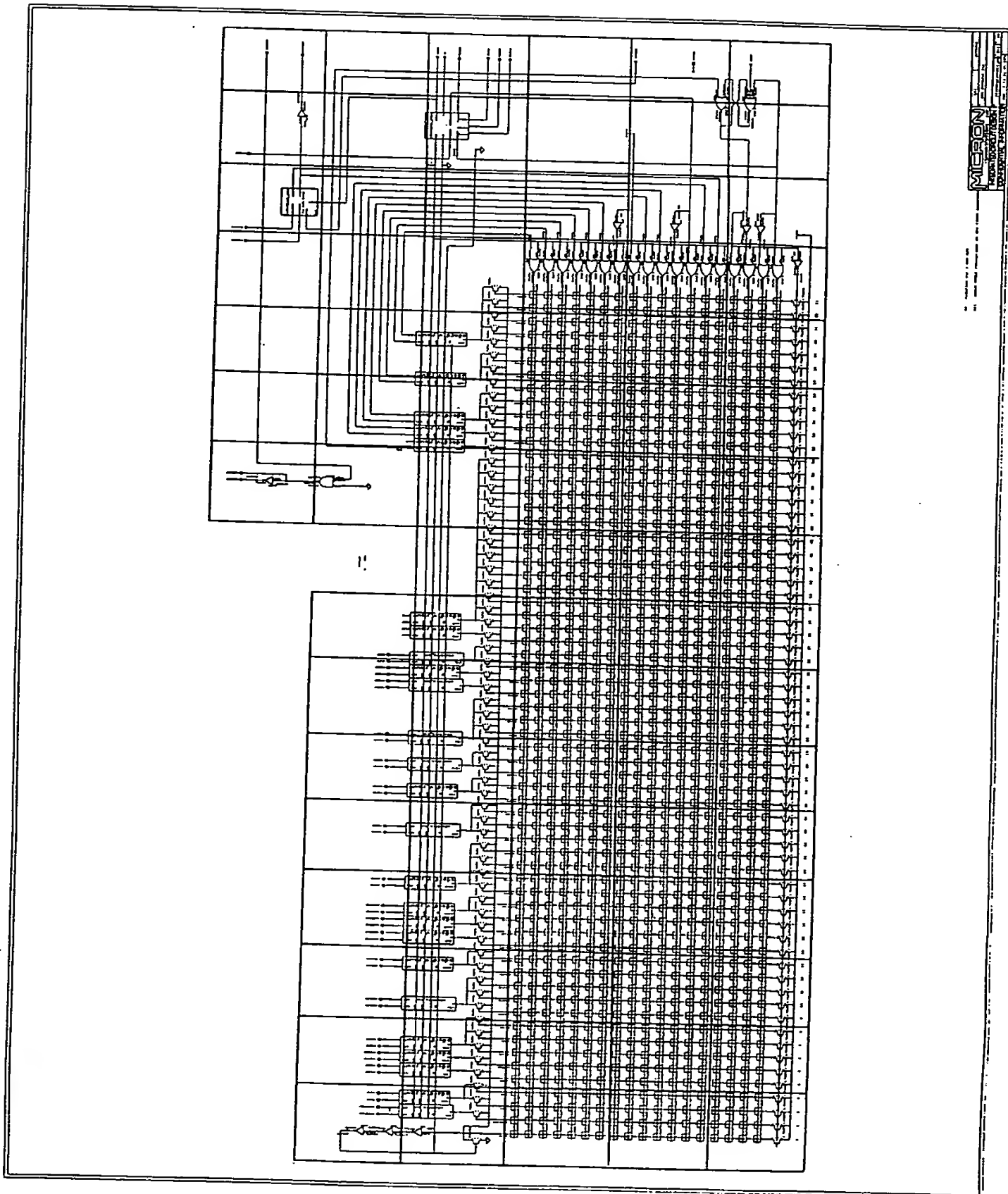
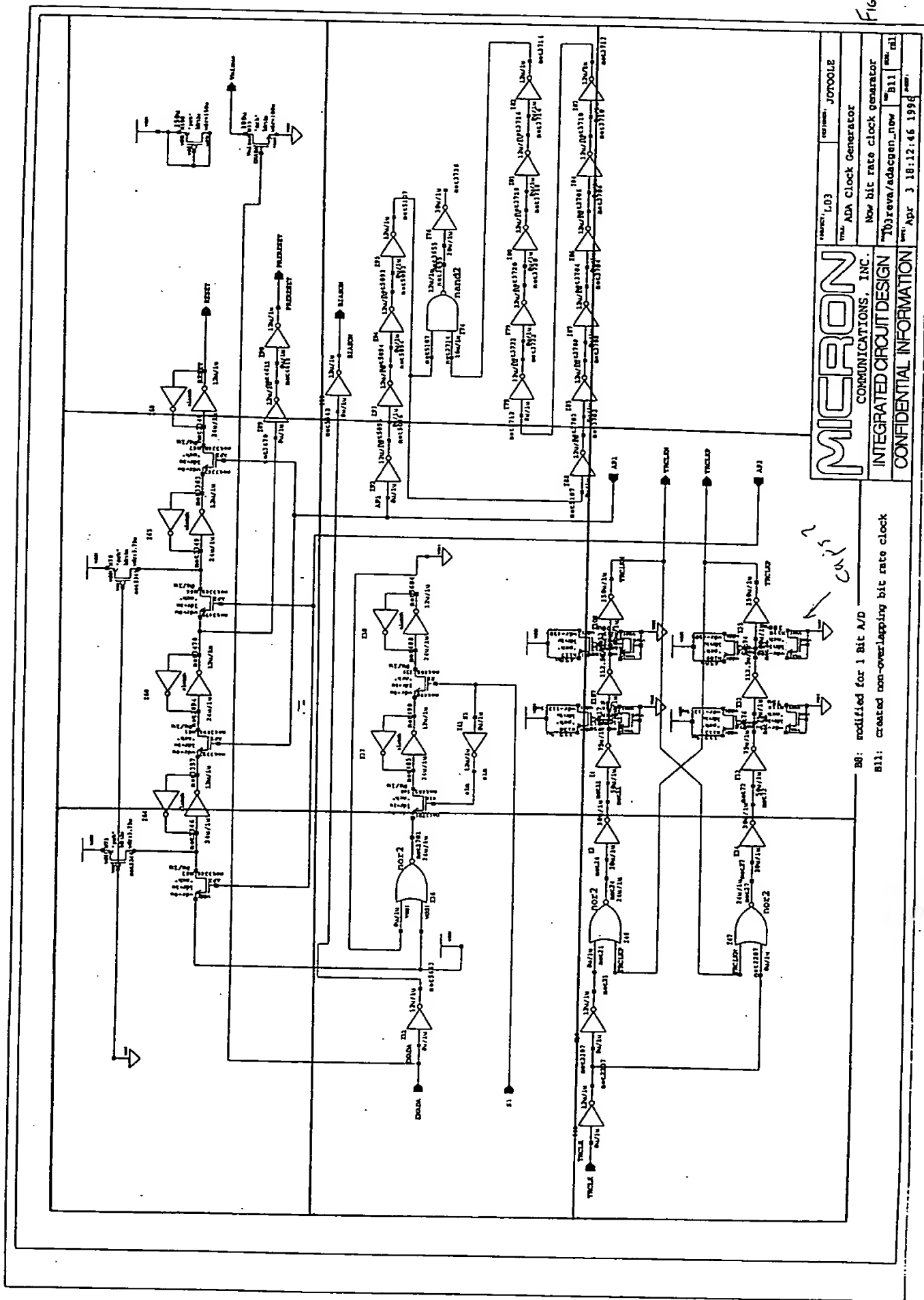


Fig. 9.0103

9.010301AA	9.010301AB	9.010301AC
9.010301BA	9.010301BB	9.010301BC
9.010301CA	9.010301CB	9.010301CC

001420 2030300



B8: modified for 1 Bit A/D  
B11: created non-overlapping bit rate clock

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

PROJECT: L03  
TITLE: ADA Clock Generator

NEW BIT RATE CLOCK GENERATOR

TO: reva/adagen\_new

DATE: Apr 3 18:12:46 1998

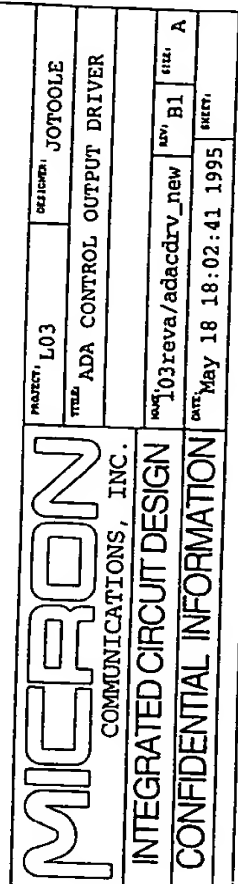
FILE 9.010301

001420-60920500

9.010302AA	9.010302AB
------------	------------

11 9.010302

FIG. 9.010302



# 2001

COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

**DESIGNER: JOTOOLE**

TRADE	ADA CONTROL OUTPUT DRIVER	JOTOUCLE

NOVA	B1	A
I03reva/adacdrv_new		

DATE: May 18 18:02:41 1995	SHEET:
----------------------------	--------

0014100 0000000000

MI40-030

9.010303AA	9.010303AB
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MEMORANDUM

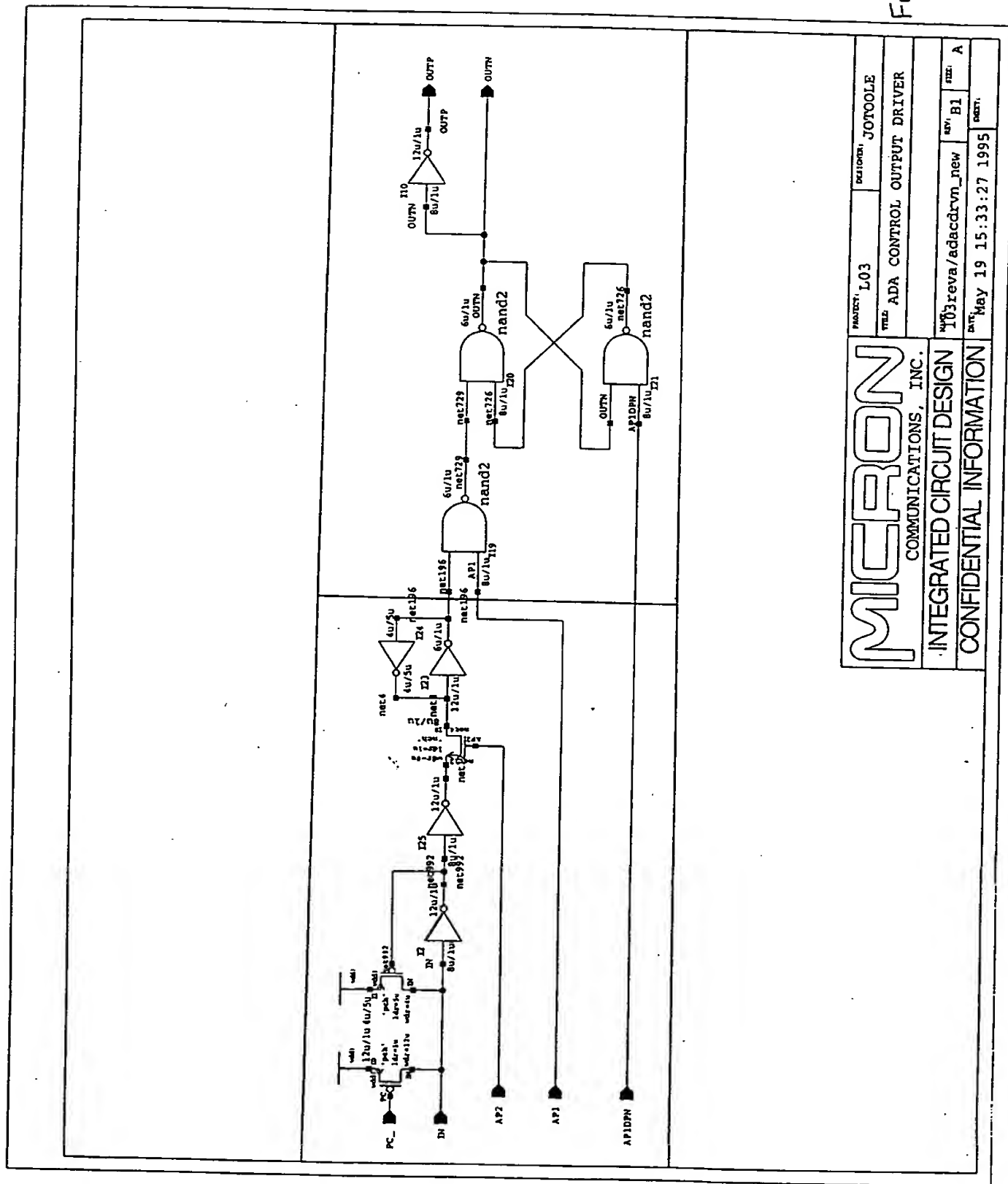


FIG. 9.010303

PROJECT: L03		REVISION: J0000LE	
TITLE: ADA CONTROL OUTPUT DRIVER			
DESIGNER: J003reva/adacdrn_new		REV: B1	SIZE: A
DATE: May 19 15:33:27 1995		PAGE: 1	

**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

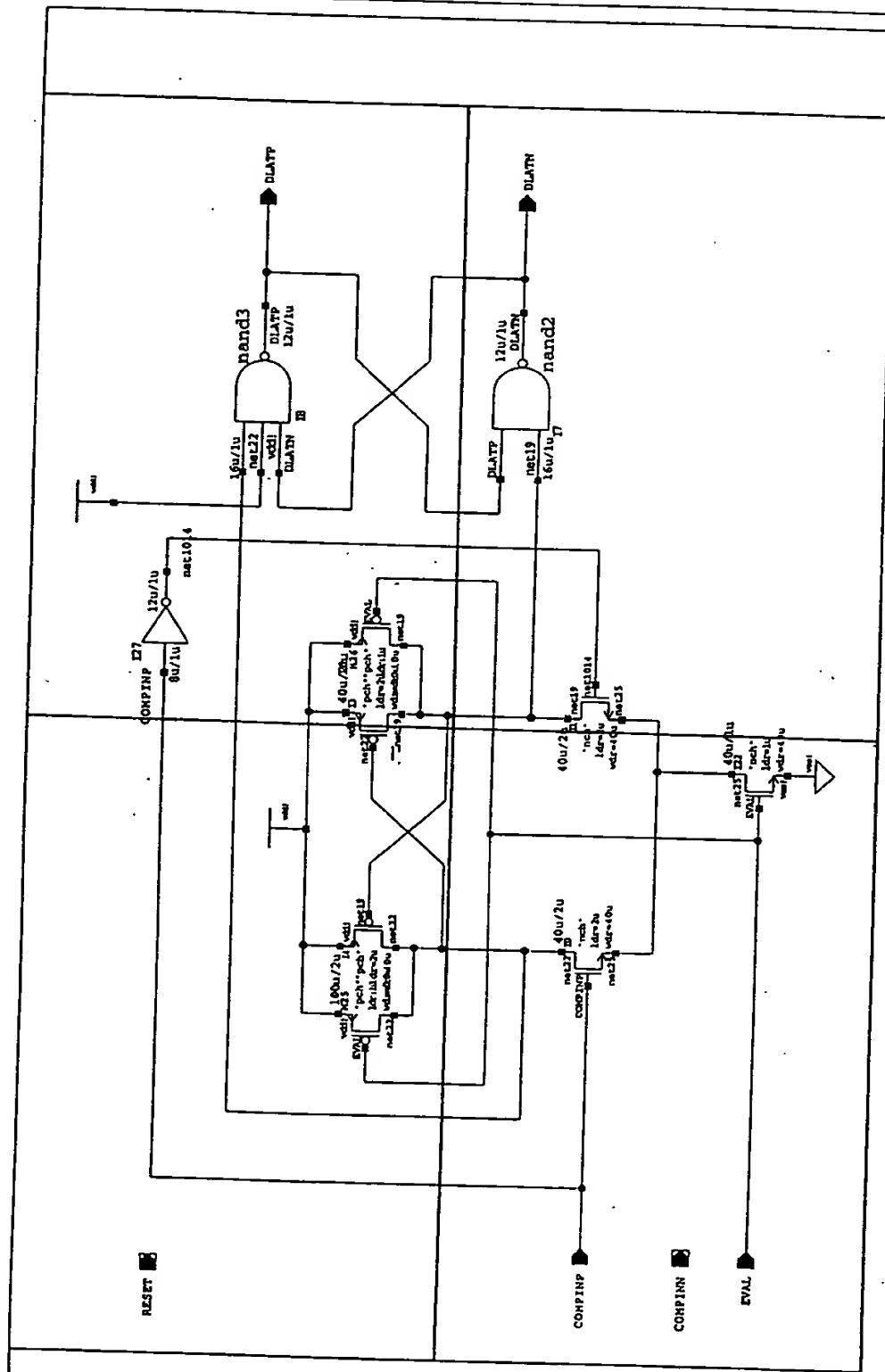


00770 2050500

MI40-030

9.010304AA	9.010304AB
9.010304BA	9.010304BB

II II 9.010304



- B5: disconnect eval input; connect to vss
- B8: reconnect eval and change reset polarity
- B11: modified for use in battery voltage sensor

<b>MICRON</b>		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: ADA Data Latch	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/adadlat_new	REV: B11
CONFIDENTIAL INFORMATION		DATE: Apr 8 10:39:12 1996	SHEET: A

9.0104AA	9.0104AB	9.0104AC	9.0104AD	9.0104AE
9.0104BA	9.0104BB	9.0104BC	9.0104BD	9.0104BE
9.0104CA	9.0104CB	9.0104CC	9.0104CD	
9.0104DA	9.0104DB	9.0104DC	9.0104DD	

IL II 037 9.0104AE

Fig. 9.0104

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

PROJECT: L03  
 TITLE: ADA Analog Bias  
 REV: B1  
 DATE: May 18 17:28:39 1995  
 SHEET: 101

Fig. 9.0109

# 21c3

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

107, 108	107, 108
----------	----------

ADA Analog Bias

NAME	103reva/adablas_new	SEC.	B1	STIM.	rd
DATE	May 16 17:28:39 1995				

9.02AA	9.02AB	9.02AC	9.02AD	9.02AE	9.02AF	9.02AG	9.02AH	9.02AI	9.02AJ	9.02AK
9.02BA	9.02BB	9.02BC	9.02BD	9.02BE	9.02BF	9.02BG	9.02BH	9.02BI	9.02BJ	9.02BK
9.02CA		9.02CC	9.02CD	9.02CE	9.02CF	9.02CG	9.02CH	9.02CI	9.02CJ	9.02CK
9.02DA	9.02DB	9.02DC	9.02DD		9.02DF	9.02DG	9.02DH	9.02DI	9.02DJ	9.02DK

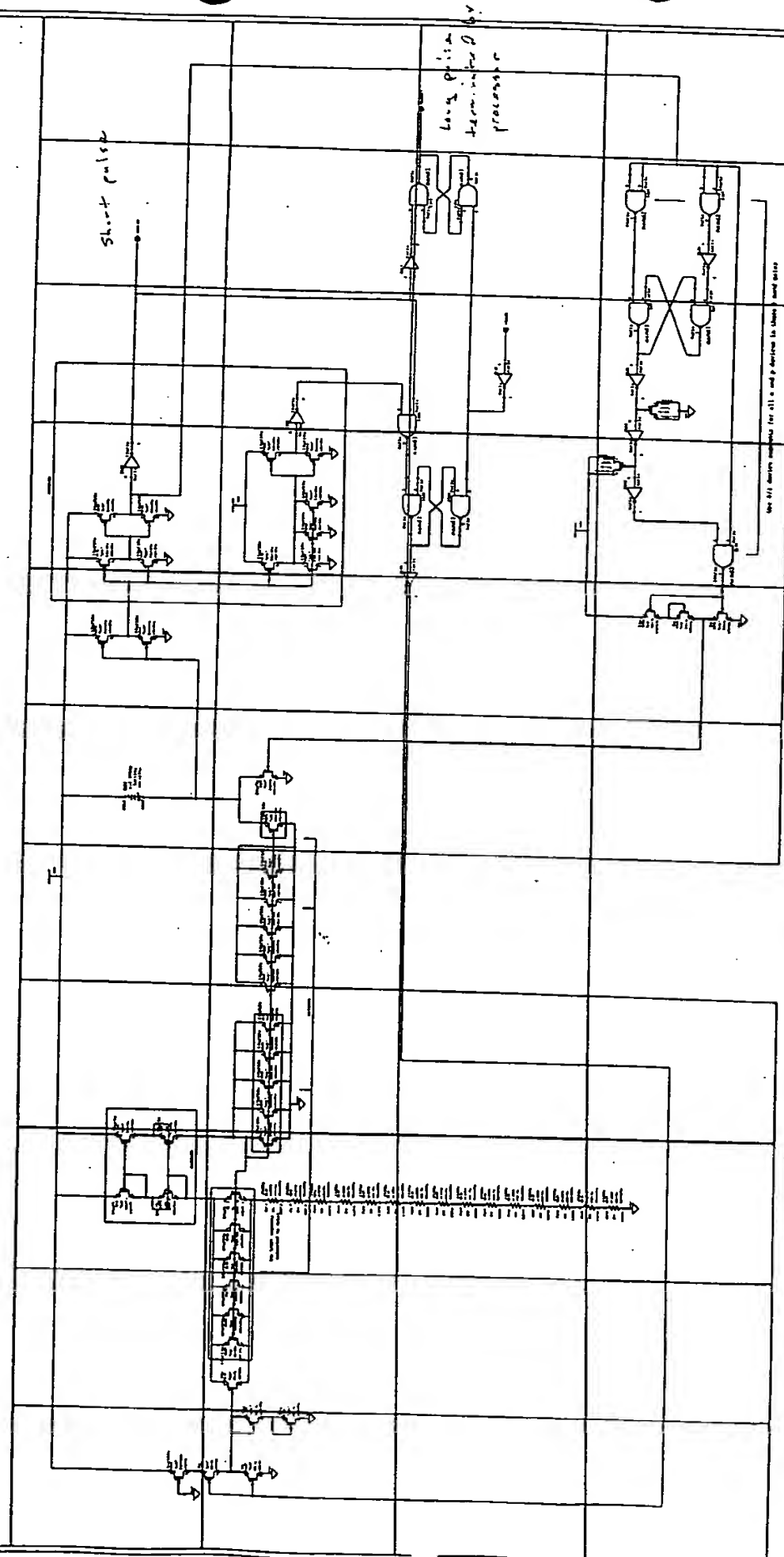
[illegible]

FIG. 9.02

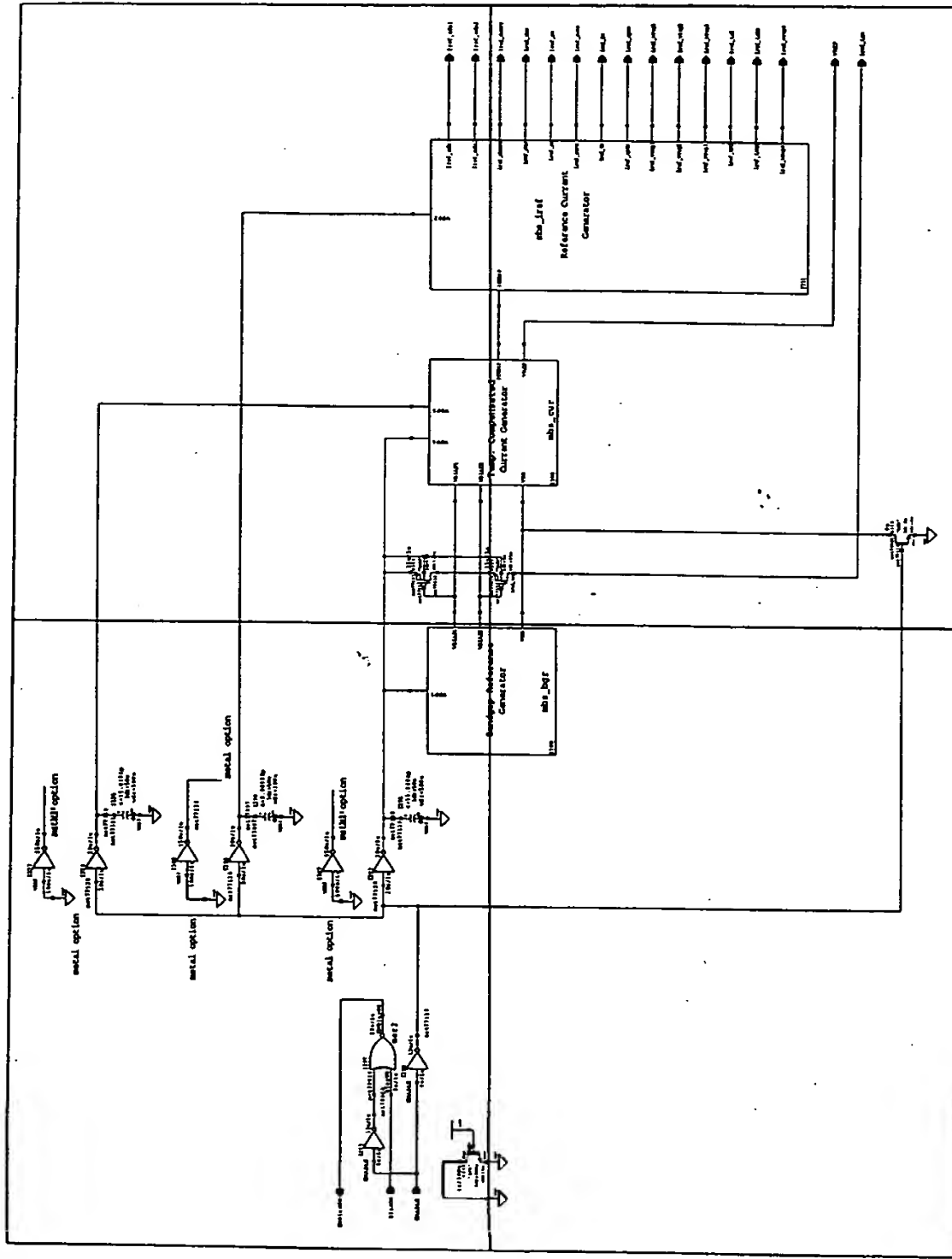
007420 00000000

MI40-030

9.03AA	9.03AB
9.03BA	9.03BB

11.11.11 09.11.11

FIG. 9.03



```

12: deleted TESTING function
    added DISKIOK logic
    created buffered VMD

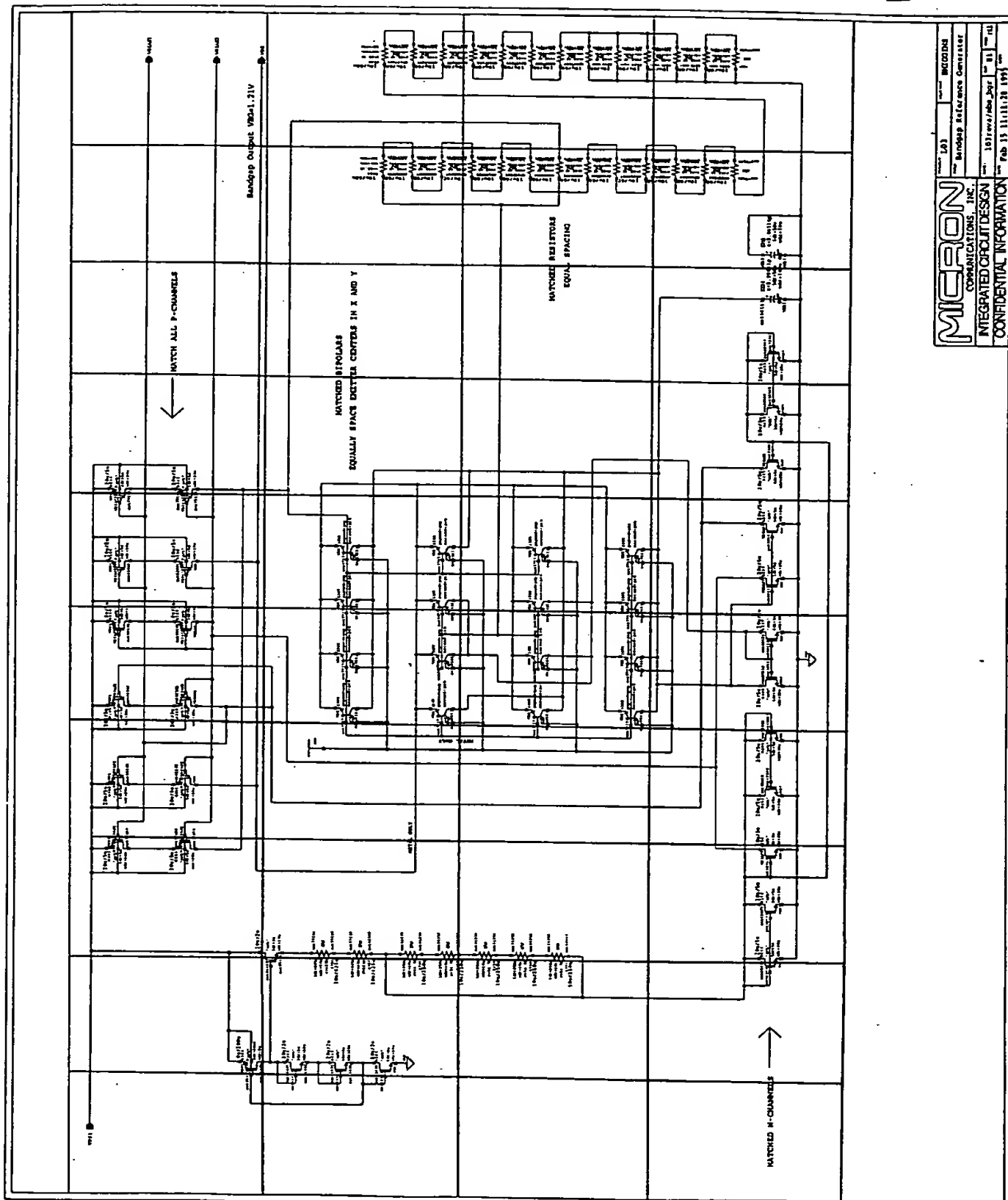
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<b>MICRON</b>		FORM NO. 10-1	
COMMUNICATIONS, INC.		REV. 10-1	
INTEGRATED CIRCUIT DESIGN		DATE: 10-1-10	
CONFIDENTIAL INFORMATION		PAGE: 1	



9.0301AA	9.0301AB	9.0301AC	9.0301AD	9.0301AE	9.0301AF	9.0301AG	9.0301AH	9.0301AI	9.0301AJ
9.0301BA	9.0301BB	9.0301BC	9.0301BD	9.0301BE	9.0301BF	9.0301BG	9.0301BH	9.0301BI	9.0301BJ
	9.0301CB	9.0301CC	9.0301CD	9.0301CE	9.0301CF	9.0301CG	9.0301CH	9.0301CI	9.0301CJ
	9.0301DB	9.0301DC	9.0301DD	9.0301DE	9.0301DF	9.0301DG	9.0301DH	9.0301DI	9.0301DJ

FIG. 9.0301



9.0302AA	9.0302AB	9.0302AC	9.0302AD	9.0302AE	9.0302AF	9.0302AG	9.0302AH	9.0302AI	9.0302AJ
9.0302BA	9.0302BB	9.0302BC	9.0302BD	9.0302BE	9.0302BF	9.0302BG	9.0302BH	9.0302BI	9.0302BJ
		9.0302CC	9.0302CD	9.0302CE	9.0302CF	9.0302CG	9.0302CH	9.0302CI	9.0302CJ
						9.0302DG	9.0302DH	9.0302DI	

2016.6.16

000000000000000000

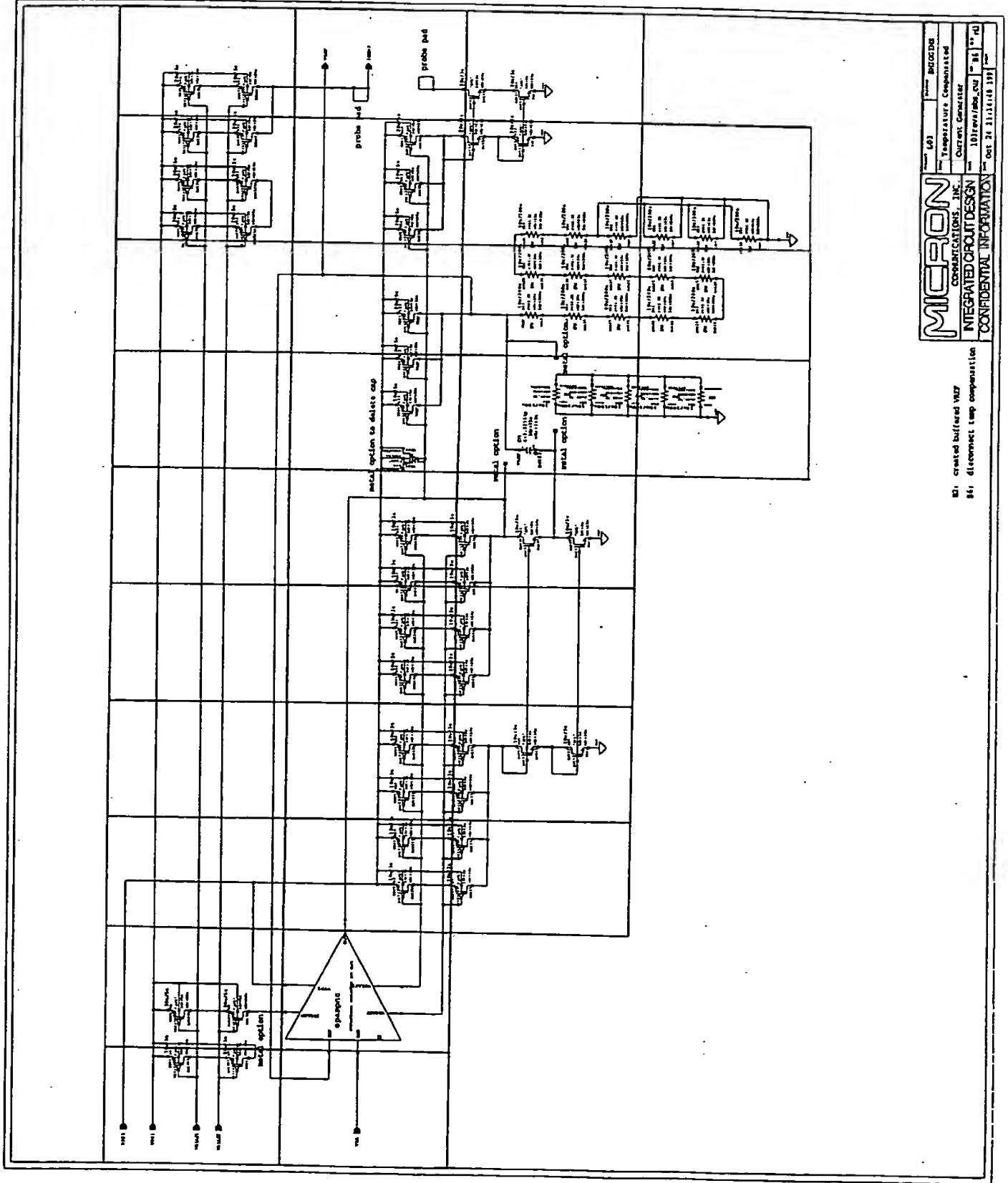


FIG. 9.0302

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

401	401	401	401
Temperature Compensated	Temperature Compensated	Temperature Compensated	Temperature Compensated
Current Compensated	Current Compensated	Current Compensated	Current Compensated
10/10/10/10/10	10/10/10/10/10	10/10/10/10/10	10/10/10/10/10
Oct 24 11:14:18 1987	Oct 24 11:14:18 1987	Oct 24 11:14:18 1987	Oct 24 11:14:18 1987

101: created buffered V<sub>DD</sub>  
101: disconnect temp compensation

007420" 24900540

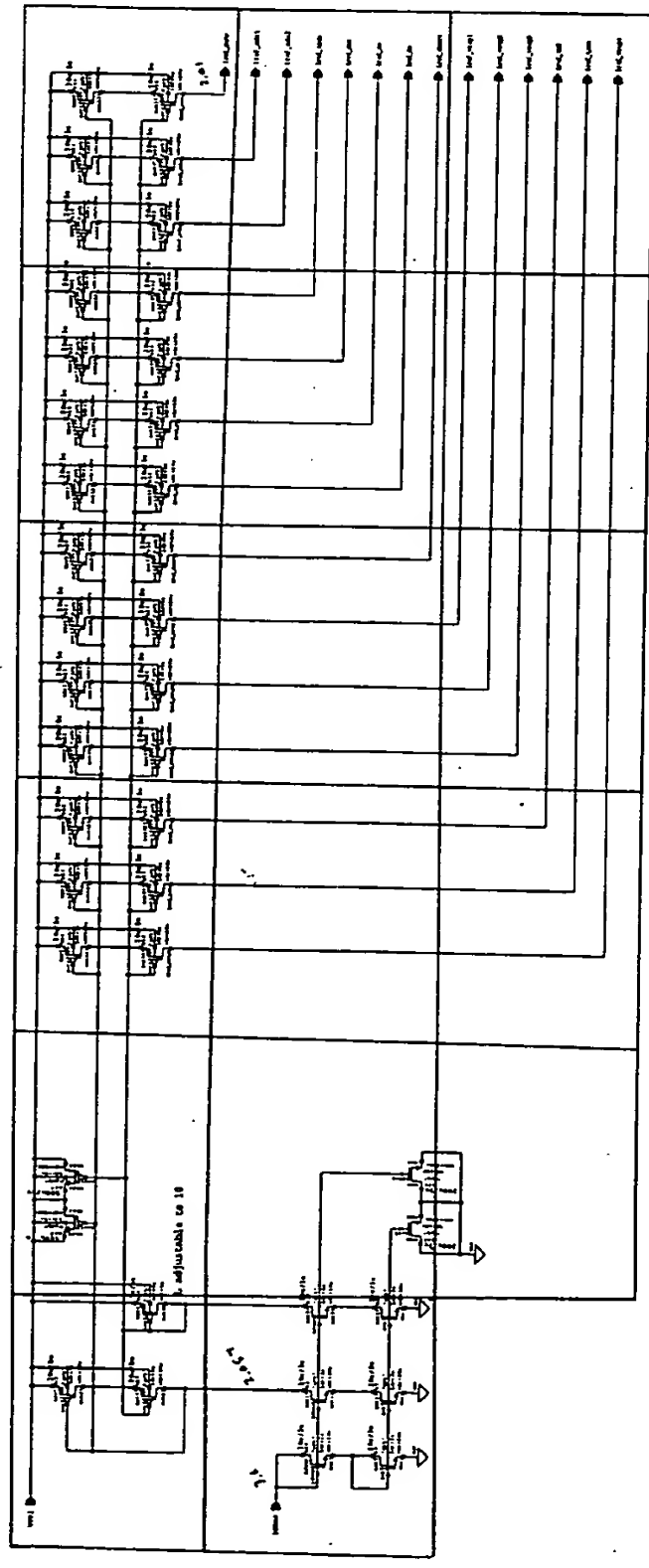
MI40-030

9.0303AA	9.0303AB	9.0303AC	9.0303AD	9.0303AE	9.0303AF
9.0303BA	9.0303BB	9.0303BC	9.0303BD	9.0303BE	9.0303BF
	9.0303CB	9.0303CC	9.0303CD	9.0303CE	9.0303CF

И. П. О. Г. 9.0303

001410 00000000

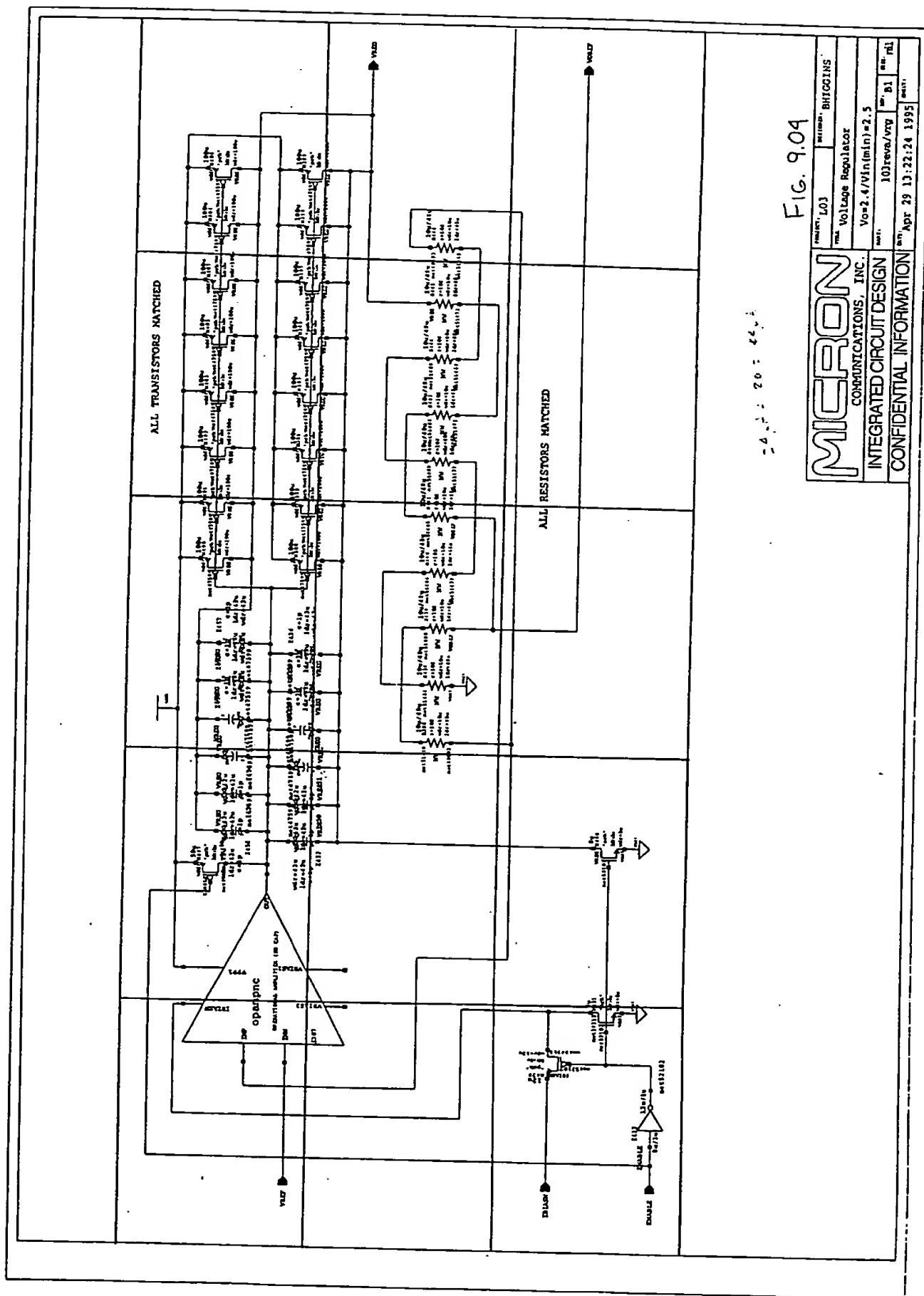
FIG. 9.0303



MICRON		103	SHOGG
COMMUNICATIONS, INC.		Reference Current	Generator
INTEGRATED CIRCUIT DESIGN		1017000000_101	81
CONFIDENTIAL INFORMATION		Apr 21 11:11:21 1991	10

9.04AA	9.04AB	9.04AC	9.04AD	9.04AE
9.04BA	9.04BB	9.04BC	9.04BD	9.04BE
9.04CA	9.04CB	9.04CC	9.04CD	9.04CE

IX. II. III. IV. V.



20:44

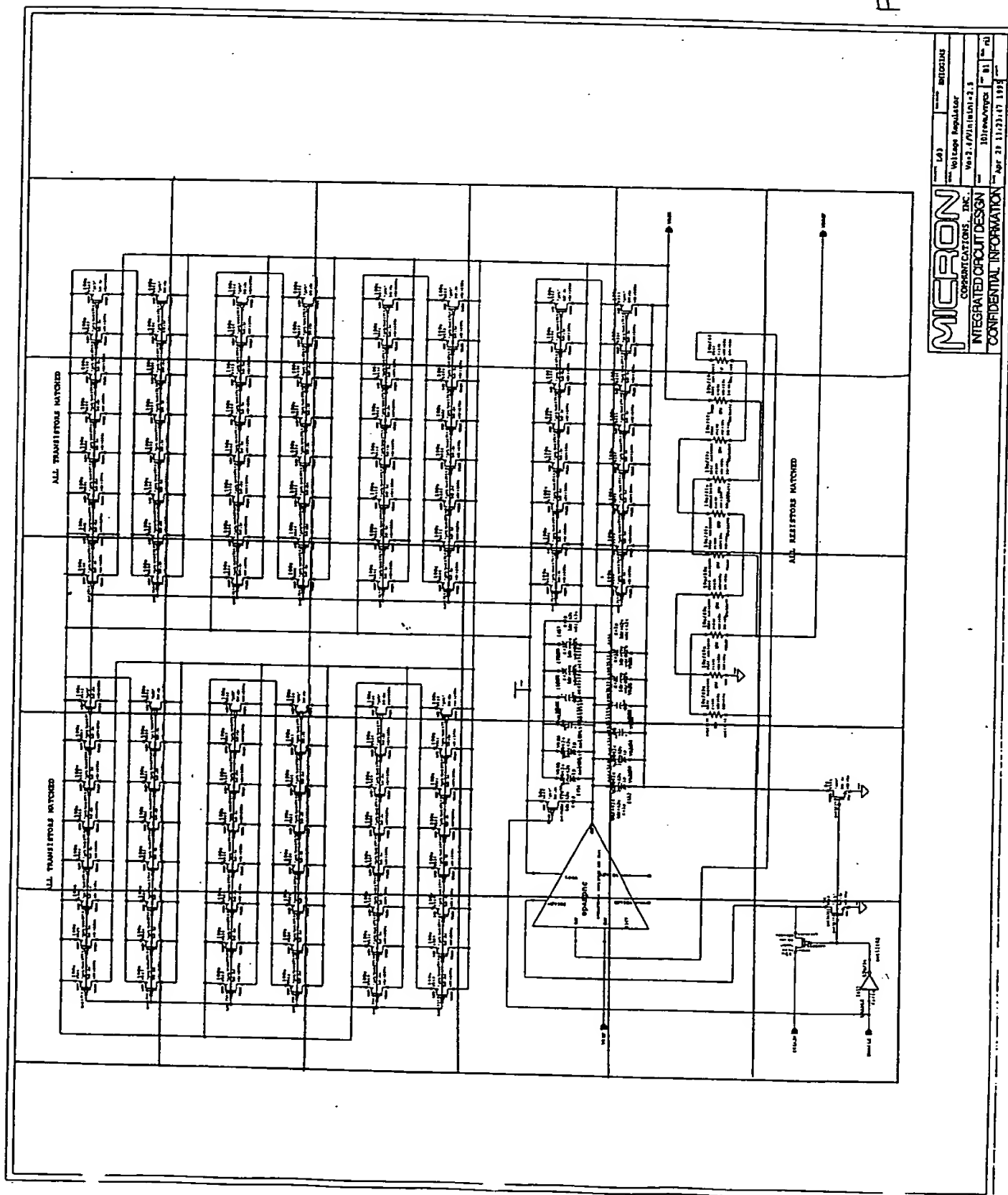
FIG. 9.04

MICRON		PROJECT: L03	REVISION: B1000135
COMMUNICATIONS, INC.		VDD: Voltage Regulator	
INTEGRATED CIRCUIT DESIGN		Vo=2.4V(min)=2.5	
CONFIDENTIAL INFORMATION		REV: 103revn/vrp	REV: B1
		DATE: Apr 29 13:22:24 1995	



9.05AA	9.05AB	9.05AC	9.05AD	9.05AE
9.05BA	9.05BB	9.05BC	9.05BD	9.05BE
9.05CA	9.05CB	9.05CC	9.05CD	9.05CE
9.05DA	9.05DB	9.05DC	9.05DD	9.05DE
9.05EA	9.05EB	9.05EC	9.05ED	9.05EE
9.05FA	9.05FB	9.05FC	9.05FD	9.05FE

Fig. 9.05



9.0501AA	9.0501AB	9.0501AC	9.0501AD
9.0501BA	9.0501BB	9.0501BC	9.0501BD
9.0501CA	9.0501CB	9.0501CC	9.0501CD

001100 00000000

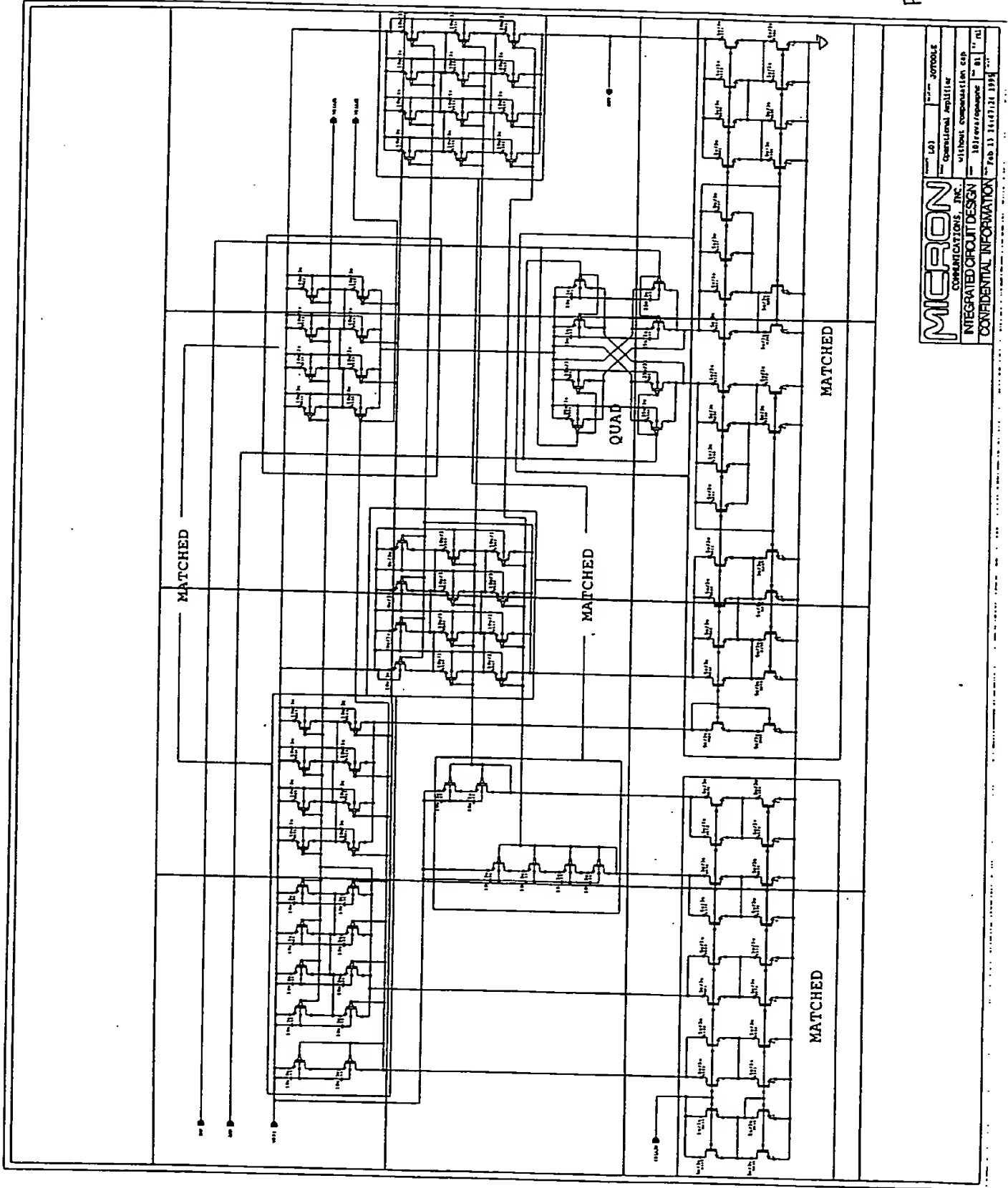
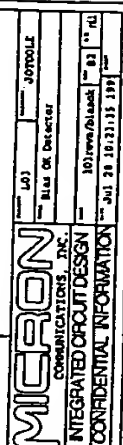


Fig. 9.0501

MICRON		L20	10000
COMMUNICATIONS, INC.		Commercial Amplifier	
INTEGRATED CIRCUIT DESIGN		without compensation cap	
CONFIDENTIAL INFORMATION		10000/counter	81
		Feb 13 11:47:31 1974	

9.06AA	9.06AB	9.06AC	9.06AD	9.06AE
9.06BA	9.06BB	9.06BC	9.06BD	9.06BE
9.06CA	9.06CB	9.06CC	9.06CD	
9.06DA	9.06DB	9.06DC	9.06DD	

Fig. 9.06



9.07AA	9.07AB	9.07AC	9.07AD	9.07AE	9.07AF	9.07AG	9.07AH	9.07AI
9.07BA	9.07BB	9.07BC	9.07BD	9.07BE	9.07BF	9.07BG	9.07BH	9.07BI
9.07CA	9.07CB	9.07CC	9.07CD	9.07CE	9.07CF	9.07CG	9.07CH	
9.07DA	9.07DB	9.07DC	9.07DD	9.07DE	9.07DF	9.07DG		
9.07EA	9.07EB	9.07EC	9.07ED	9.07EE	9.07EF	9.07EG		

004400 00000000

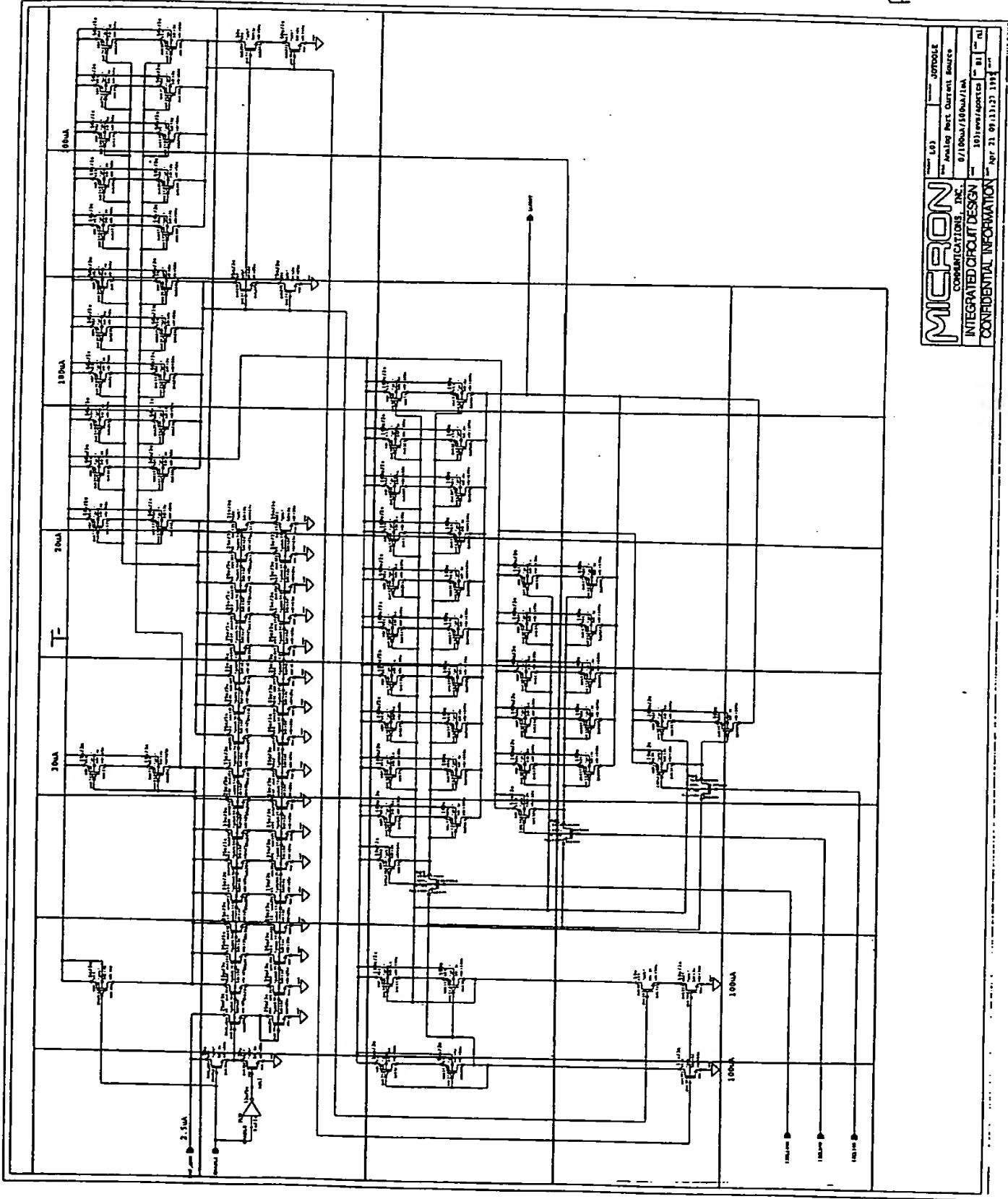


Fig. 9.07



<i>9.08AA</i>	<i>9.08AB</i>	<i>9.08AC</i>
<i>9.08BA</i>	<i>9.08BB</i>	<i>9.08BC</i>
<i>9.08CA</i>	<i>9.08CB</i>	<i>9.08CC</i>

Итого 9.088

[illegible]

F16.9.08



<b>MICRON</b>		DATE	1/91	BY	JRM/ML
<b>COMMUNICATIONS, INC.</b>		Project MULTIMASTER December			
<b>INTEGRATED CIRCUIT DESIGN</b>		and LOGIC test logic			
<b>CONFIDENTIAL INFORMATION</b>		DATE	10/29/91	BY	du
		Rev. 13 10-26-91 10:00 AM			

007420 20920566

MI40-030

9.09AA	9.09AB
9.09BA	9.09BB

EX 9.09

Fig. 9.09

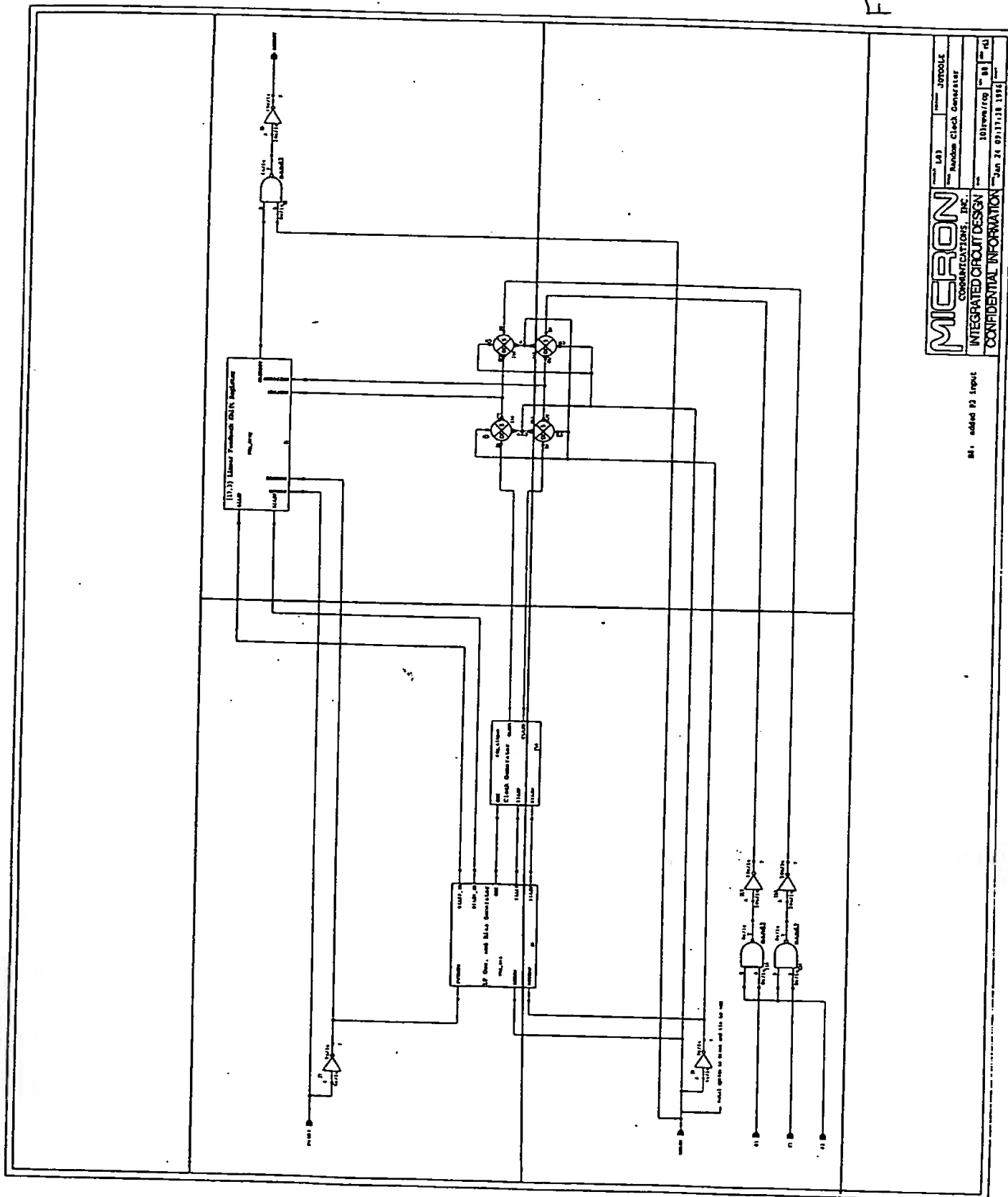
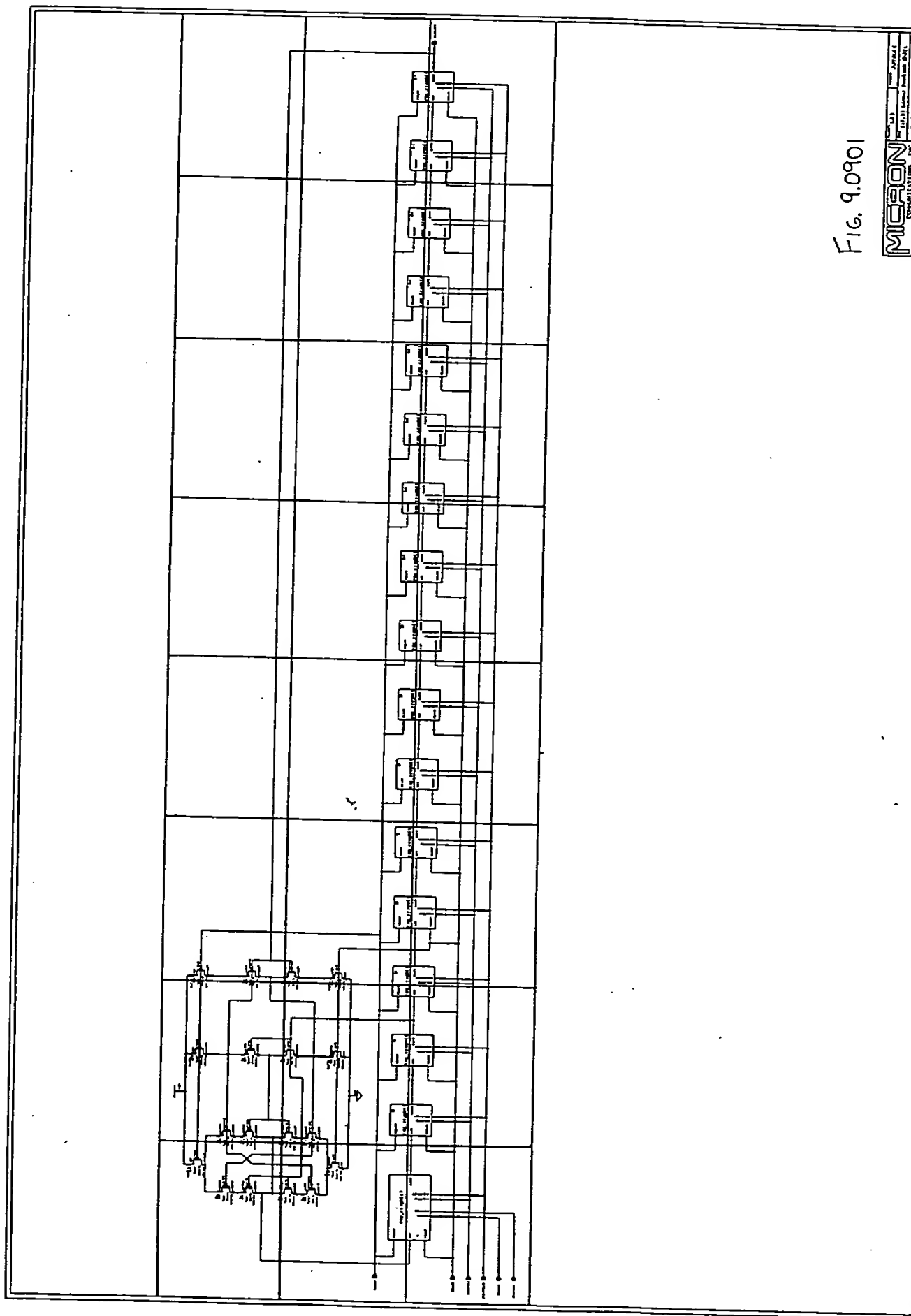


Fig. 9.09

9.0901AA	9.0901AB	9.0901AC	9.0901AD	9.0901AE	9.0901AF	9.0901AG	9.0901AH
9.0901BA	9.0901BB	9.0901BC	9.0901BD	9.0901BE	9.0901BF	9.0901BG	9.0901BH
9.0901CA	9.0901CB	9.0901CC	9.0901CD	9.0901CE	9.0901CF	9.0901CG	9.0901CH

三

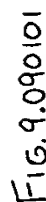


001120 26920559

MI40-030

9.090101AA	9.090101AB	9.090101AC
9.090101BA	9.090101BB	9.090101BC
9.090101CA	9.090101CB	9.090101CC

II II 9.090101





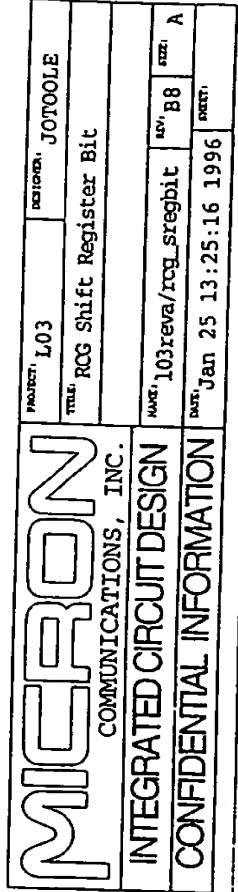
DOTED C0900000

MI40-030

9.090102AA	9.090102AB
9.090102BA	9.090102BB

II 9.090102

Fig. 9,090102.



**B8: increased L of passgates  
decreased L of pch keepers**

9.0902AA	9.0902AB	9.0902AC	9.0902AD	9.0902AE	9.0902F	9.0902AG	9.0902AH	9.0902AI	9.0902AJ	9.0902AK	9.0902AL
9.0902BA	9.0902BB	9.0902BC	9.0902BD	9.0902BE	9.0902F	9.0902BG	9.0902BH	9.0902BI	9.0902BJ	9.0902BK	9.0902BL
		9.0902CC	9.0902CD	9.0902CE	9.0902F	9.0902CG	9.0902CH	9.0902CI	9.0902CJ	9.0902CK	9.0902CL
		9.0902DC	9.0902DD	9.0902DE	9.0902F						9.0902DL
9.0902EA	9.0902EB	9.0902EC	9.0902ED	9.0902EE	9.0902F	9.0902EG	9.0902EH	9.0902EI	9.0902EJ	9.0902EK	9.0902EL
			9.0902FD	9.0902FE	9.0902F	9.0902FG	9.0902FH	9.0902FI	9.0902FJ	9.0902FK	9.0902FL

FIG. 9.0902AA-FL



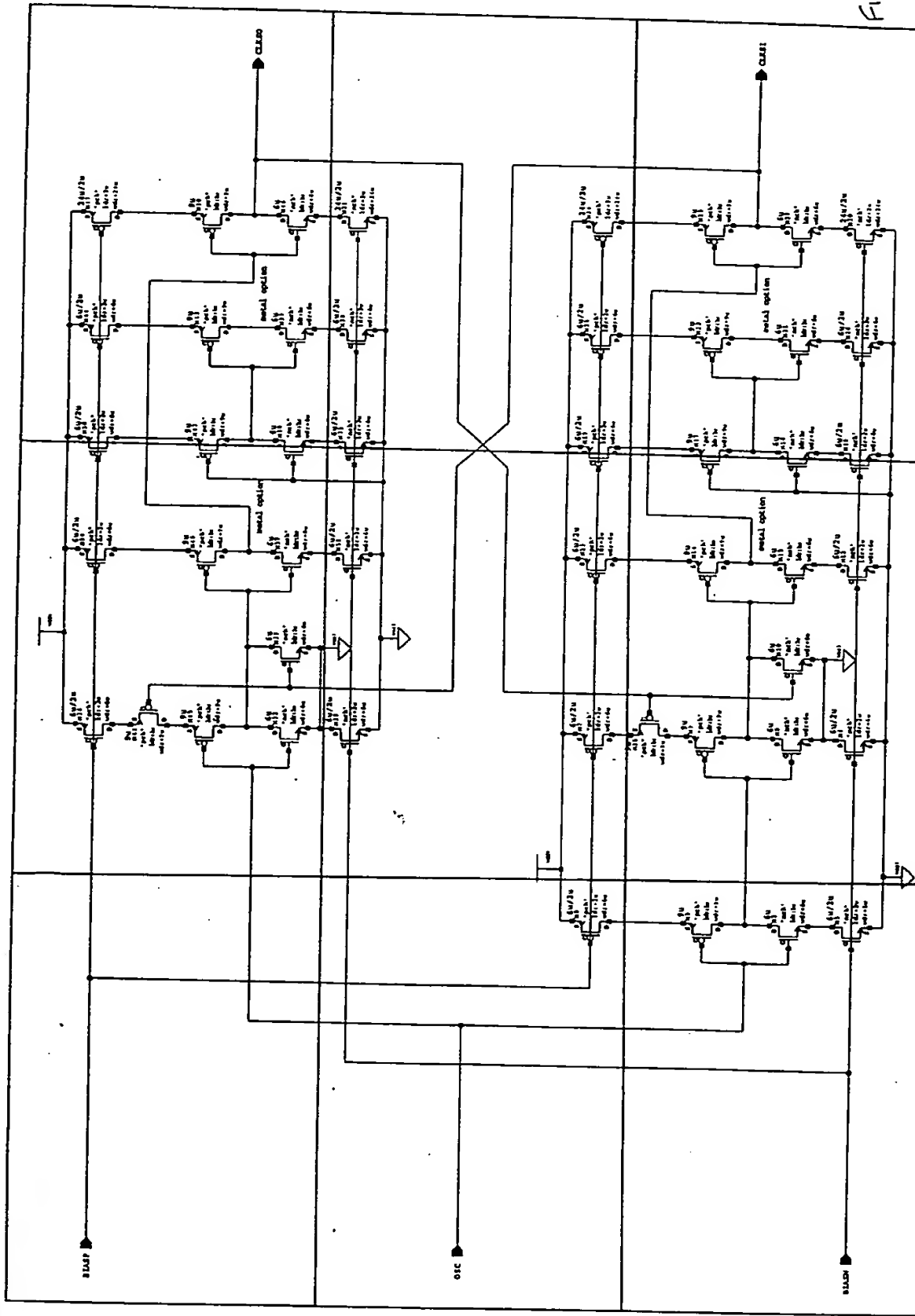
00440" 0050000

MI40-030

9.0903AA	9.0903AB	9.0903AC
9.0903BA	9.0903BB	9.0903BC
9.0903CA	9.0903CB	9.0903CC

И. П. 09.09.09

00420 2430303



MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		Title: Clock Generator	
INTEGRATED CIRCUIT DESIGN		Part: 103revA/rcg_clkgen	Rev: 98
CONFIDENTIAL INFORMATION		Date: Jan 24 09:56:43 1996	Drawn: ml

B8: wired cross-couples to ground

10AA	10AB	10AC	10AD
10BA	10BB	10BC	10BD
10CA	10CB	10CC	10CD
10DA	10DB	10DC	10DD

Fig. 10

[illegible]

111: added non-overlapping clocks TMSH and TMSH

added MODEL input to follow

[illegible]

100



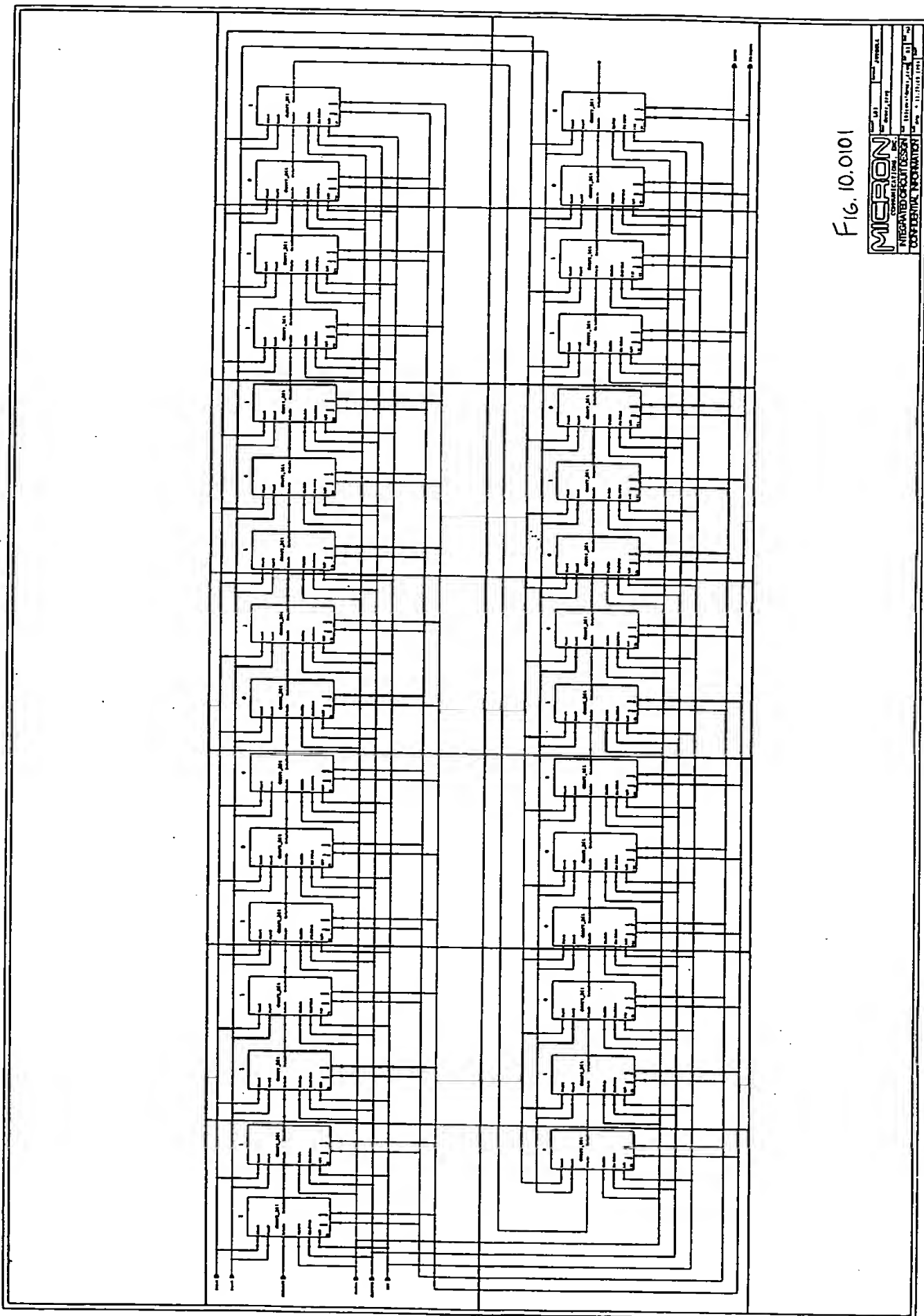
10.01AA	10.01AB	10.01AC	10.01AD	10.01AE	10.01AF	10.01AG			
10.01BA	10.01BB	10.01BC	10.01BD	10.01BE	10.01BF	10.01BG	10.01BH	10.01BI	10.01BJ
10.01CA	10.01CB	10.01CC	10.01CD	10.01CE	10.01CF	10.01CG	10.01CH	10.01CI	10.01CJ
	10.01DB	10.01DC	10.01DD	10.01DE	10.01DF	10.01DG	10.01DH	10.01DI	10.01DJ

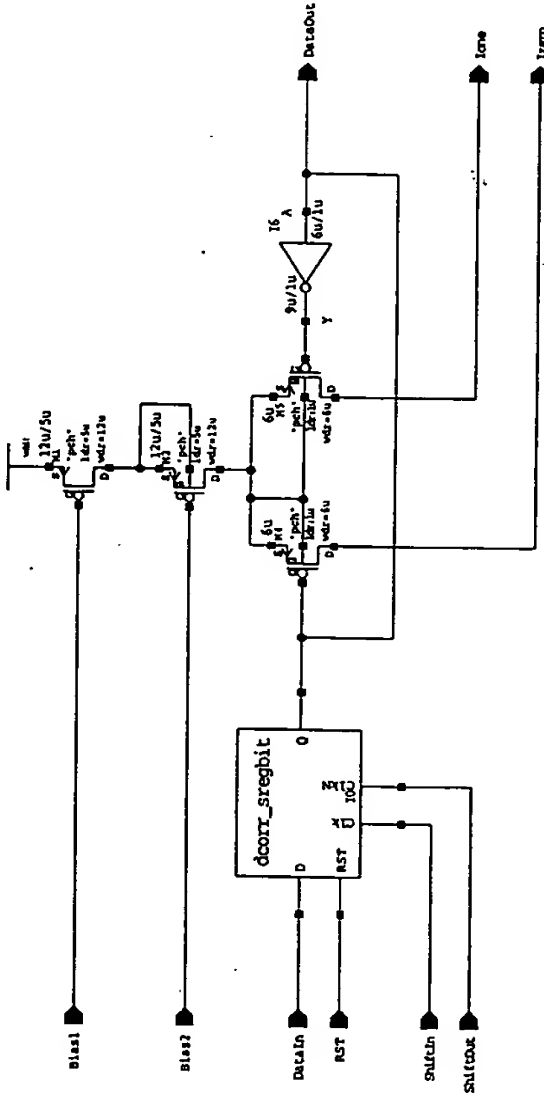
[illegible]

10.0101AA	10.0101AB	10.0101AC	10.0101AD	10.0101AE	10.0101AF	10.0101AG
10.0101BA	10.0101BB	10.0101BC	10.0101BD	10.0101BE	10.0101BF	10.0101BG

II II II II II II II II

001420 2090900





<b>MICRON</b>		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Correlator Bit	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/dcorr_bit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 9 11:37:26 1994	SIZE: A

FIG. 10.010101

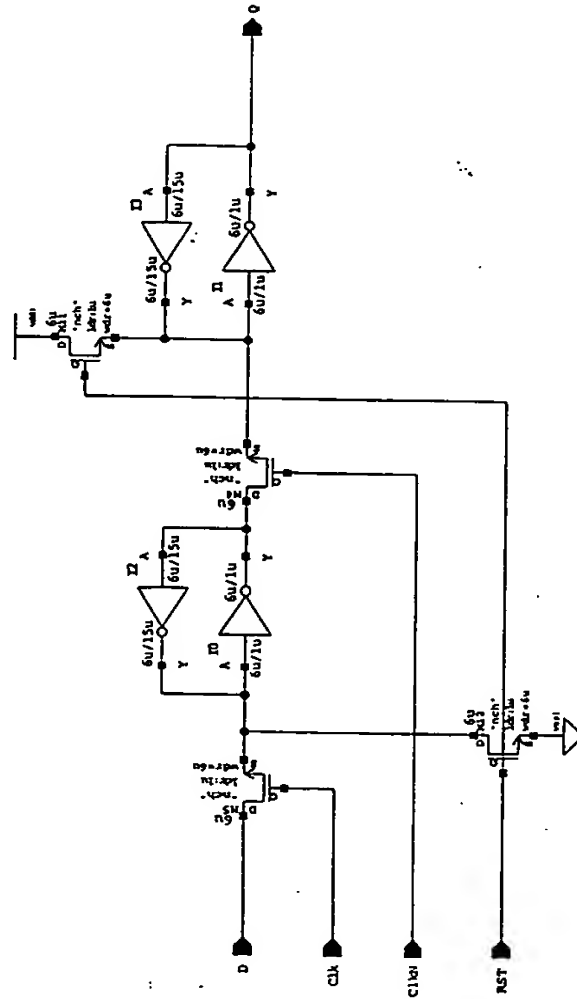


FIG. 10.01010101

MICRON		PRODUCT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		REV: B1	SIZE: A
CONFIDENTIAL INFORMATION		DATE: Sep 9 14:08:50 1994	SHEET: 1

10.01.2022

0014120 20300500

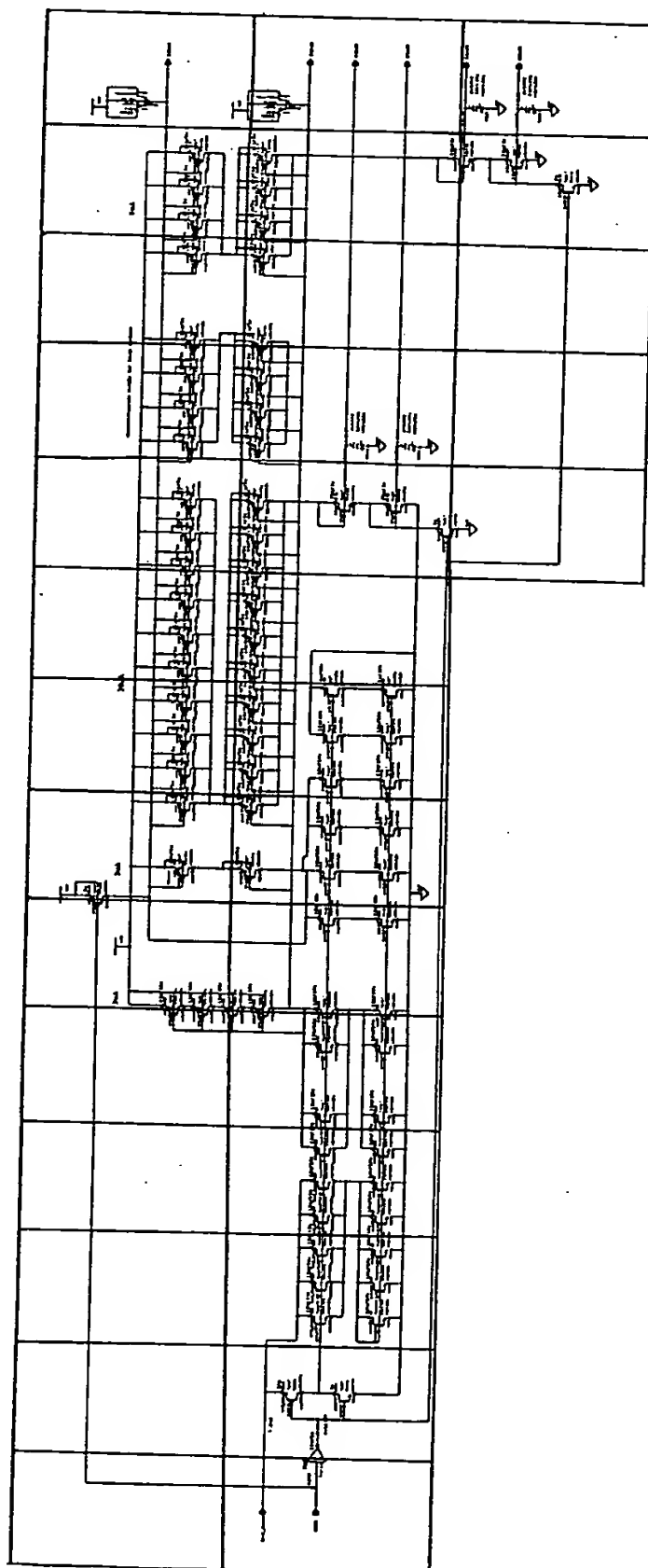


Fig. 10.0102

<b>MICRON</b>	
DESIGN	DATE
REVISION	DATE
INTEGRATED CIRCUITS	
CONFIDENTIAL INFORMATION	



10.02AA	10.02AB	10.02AC	10.02AD	10.02AE
10.02BA	10.02BB	10.02BC	10.02BD	10.02BE

11.11.11.11.11.11

001423 20320500

Detects preamble by counting 4 consecutive 0's

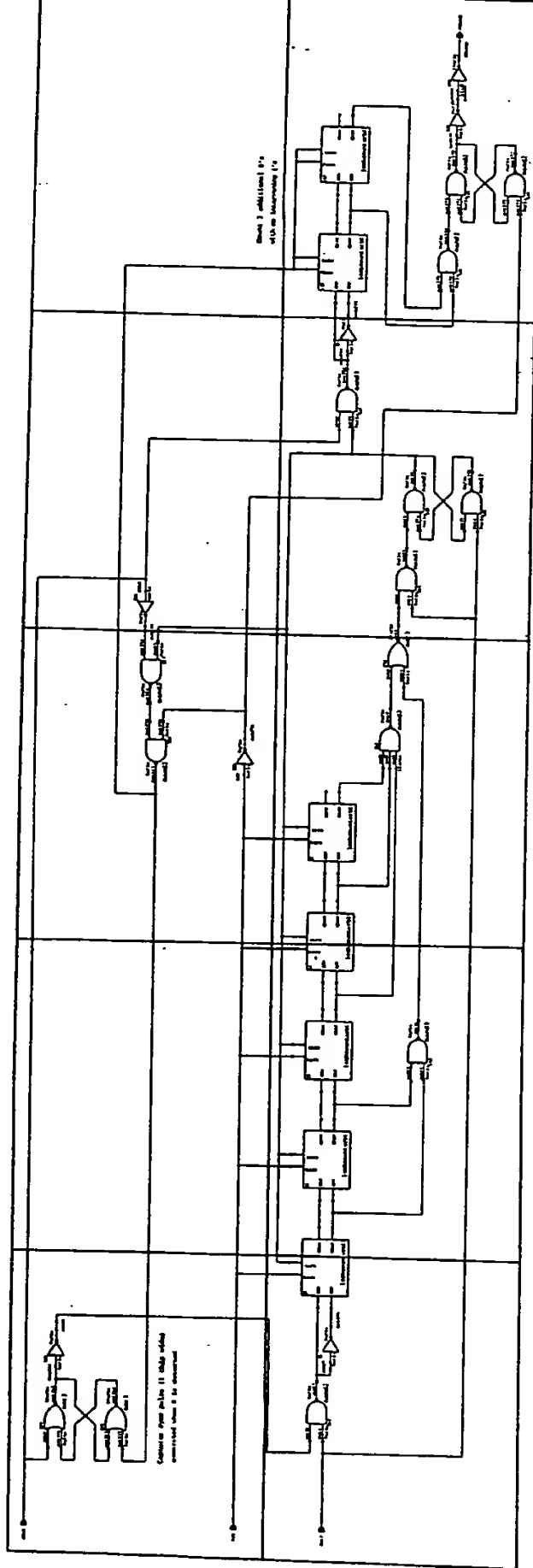


Fig. 10.02

61) Revised this drawing to show 3 bit output  
(changed P101 to P11)

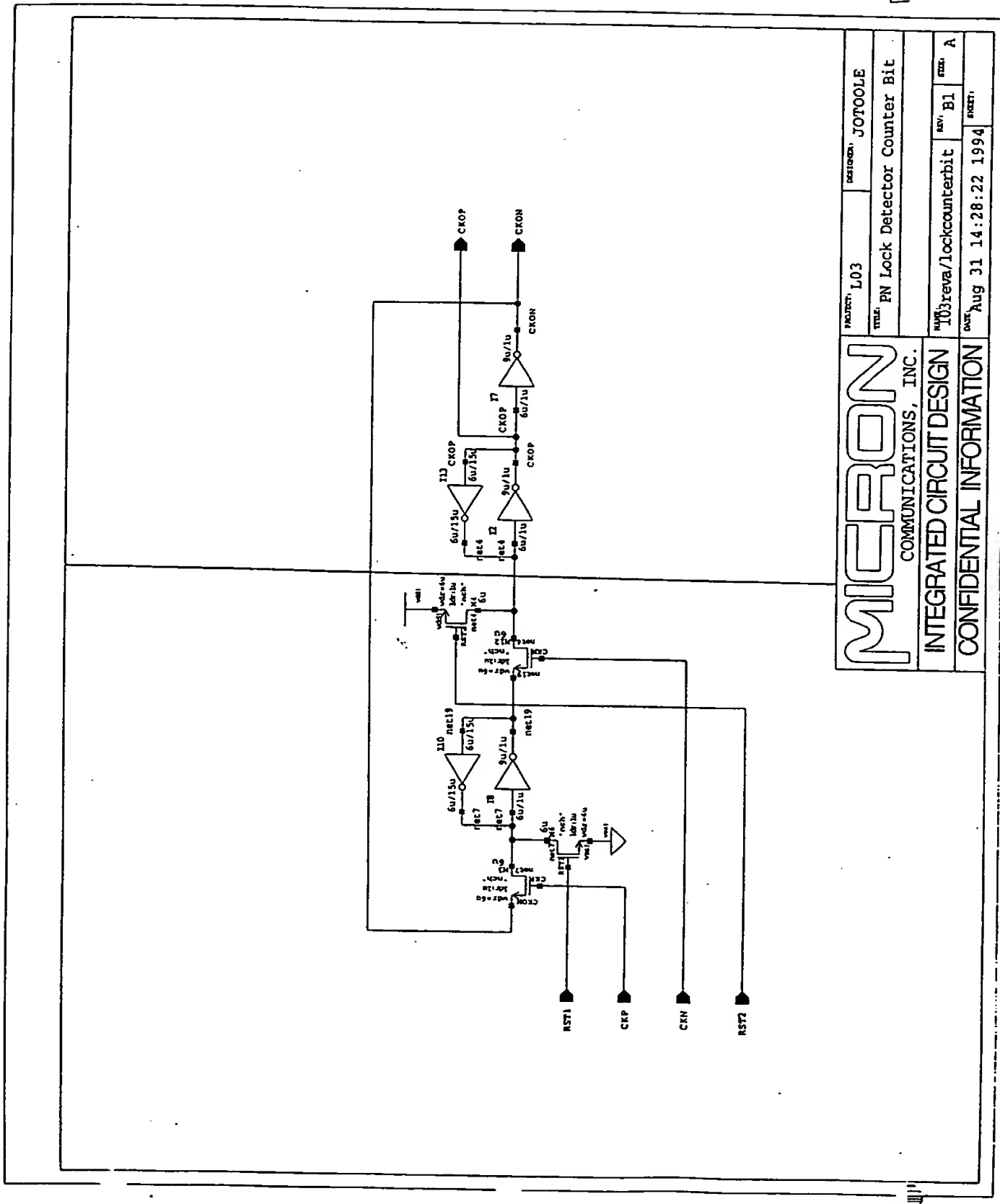
MICRON		DATE	10/1/73
COMMUNICATIONS		DESIGNED BY	J. J. JONES
INTEGRATED CIRCUIT DESIGN		CHECKED BY	J. J. JONES
CONFIDENTIAL INFORMATION		APPROVED BY	J. J. JONES

00140 24320500

10.0201AA	10.0201AB
-----------	-----------

11.11.11.11.11.11

0011201 20000000



MICRON				PROJECT: L03		DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.							
INTEGRATED CIRCUIT DESIGN							
CONFIDENTIAL INFORMATION							
DATE: Aug 31 14:28:22 1994				REV: B1		REV: A	
CONFIDENTIAL INFORMATION							

00000000000000000000

10.03AA	10.03AB
---------	---------

Ex. 10.03

—



FIG. 10.03

10.04AA	10.04AB	10.04AC	10.04AD	10.04AE
10.04BA	10.04BB	10.04BC	10.04BD	10.04BE
10.04CA	10.04CB	10.04CC	10.04CD	10.04CE

001420-20920500

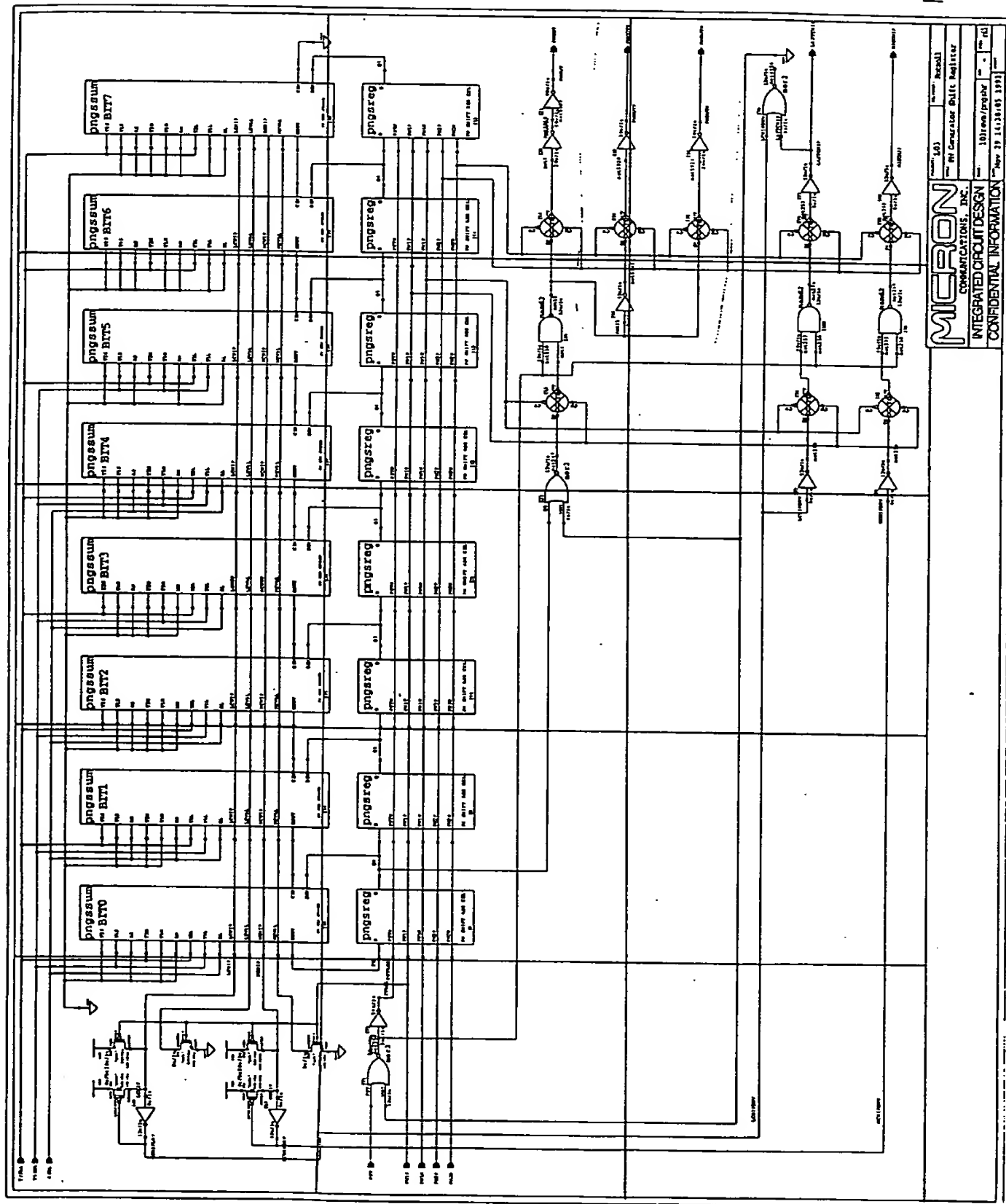
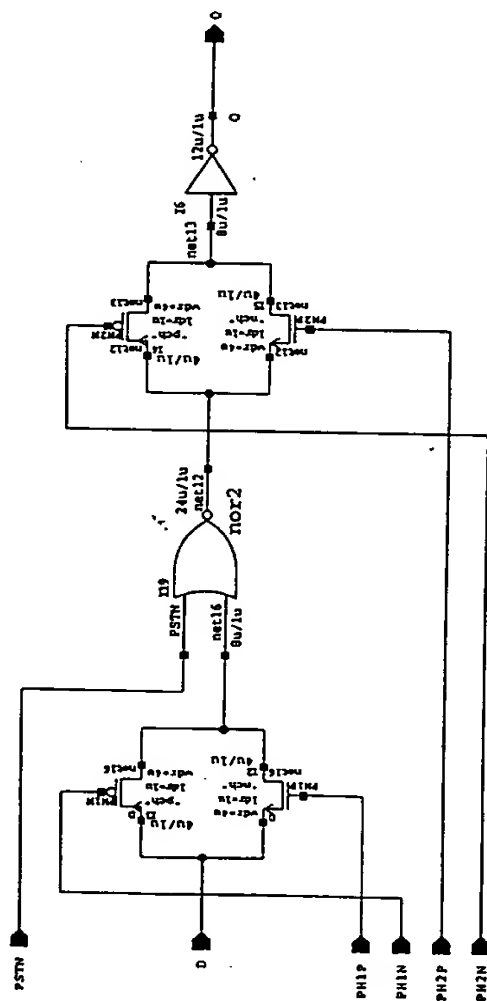


FIG. 10.04

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION  
Rev. 3.0 11/28/85 13131



001120 20920500



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: PN Generator Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/pngsreg	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 20 21:22:37 1993	REV: A

FIG. 10.0401

10.0402AA	10.0402AB
10.0402BA	10.0402BB
10.0402CA	10.0402CB

10.0402

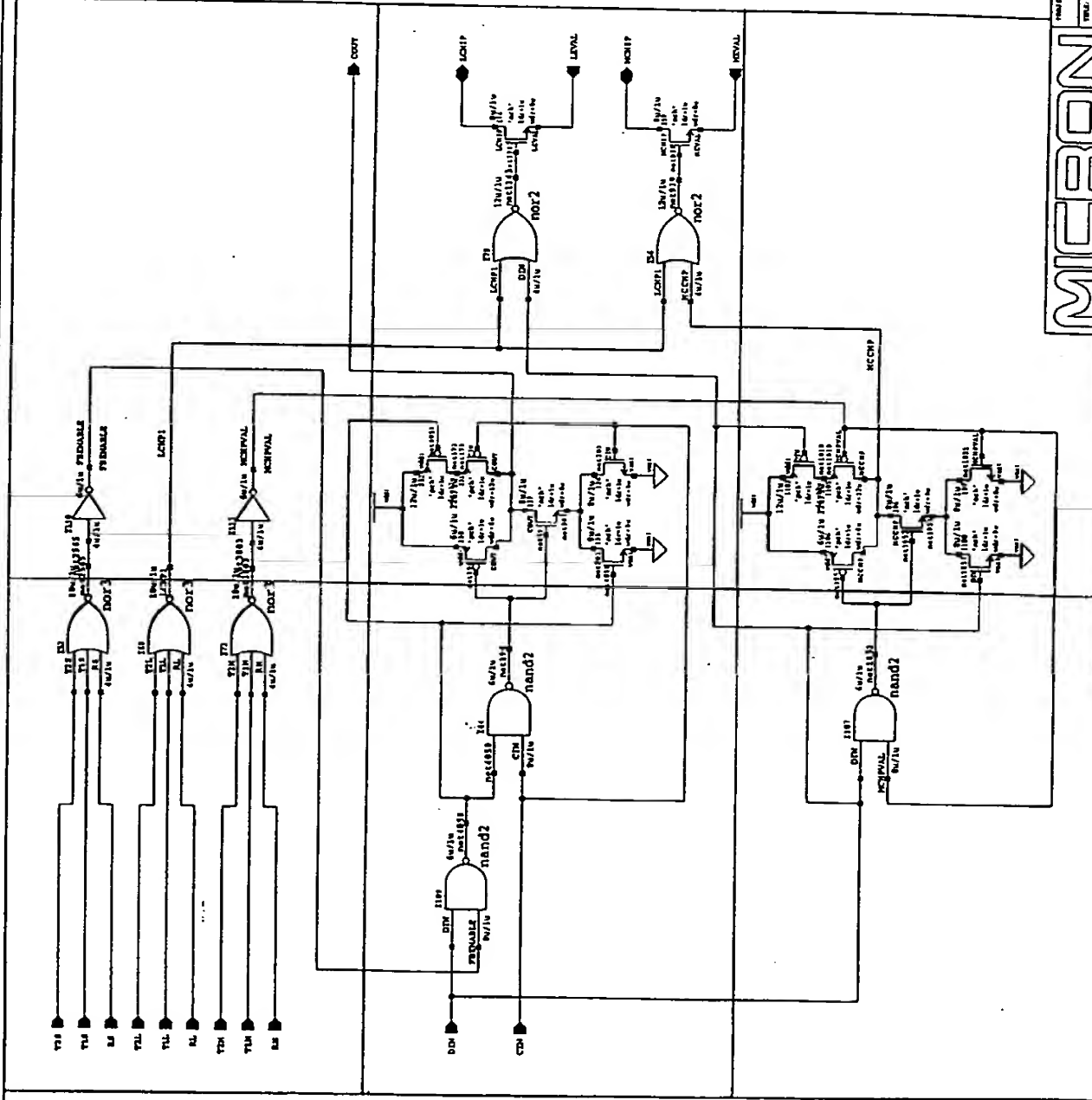
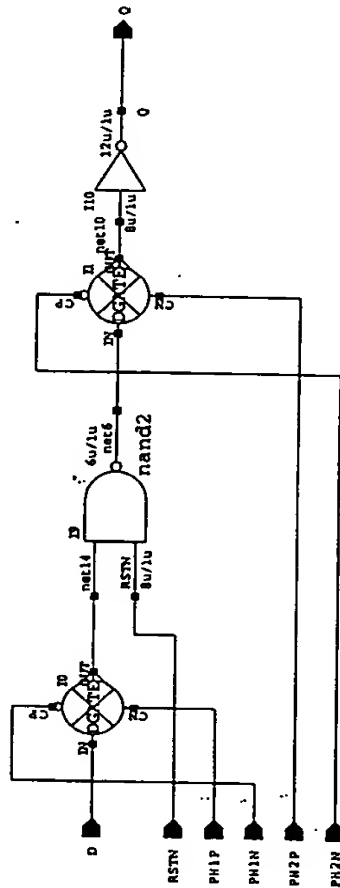


Fig. 10.0402

MICRON		PROJECT: L03	REVISION: Rotzoll
INTEGRATED CIRCUIT DESIGN		TITLE: PN Gen Shift Reg Sumner	
CONFIDENTIAL INFORMATION		DATE: 10/20/93	BY: [signature]
		DATE: 10/20/93	BY: [signature]
		DATE: 10/20/93	BY: [signature]

007420 22520500



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: PN Controller D Flip-Flop			
NAME: 103reva/pndaff		REV: -	SHEET: A
DATE: Nov 26 18:12:59 1993		PART:	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FIG. 10.05

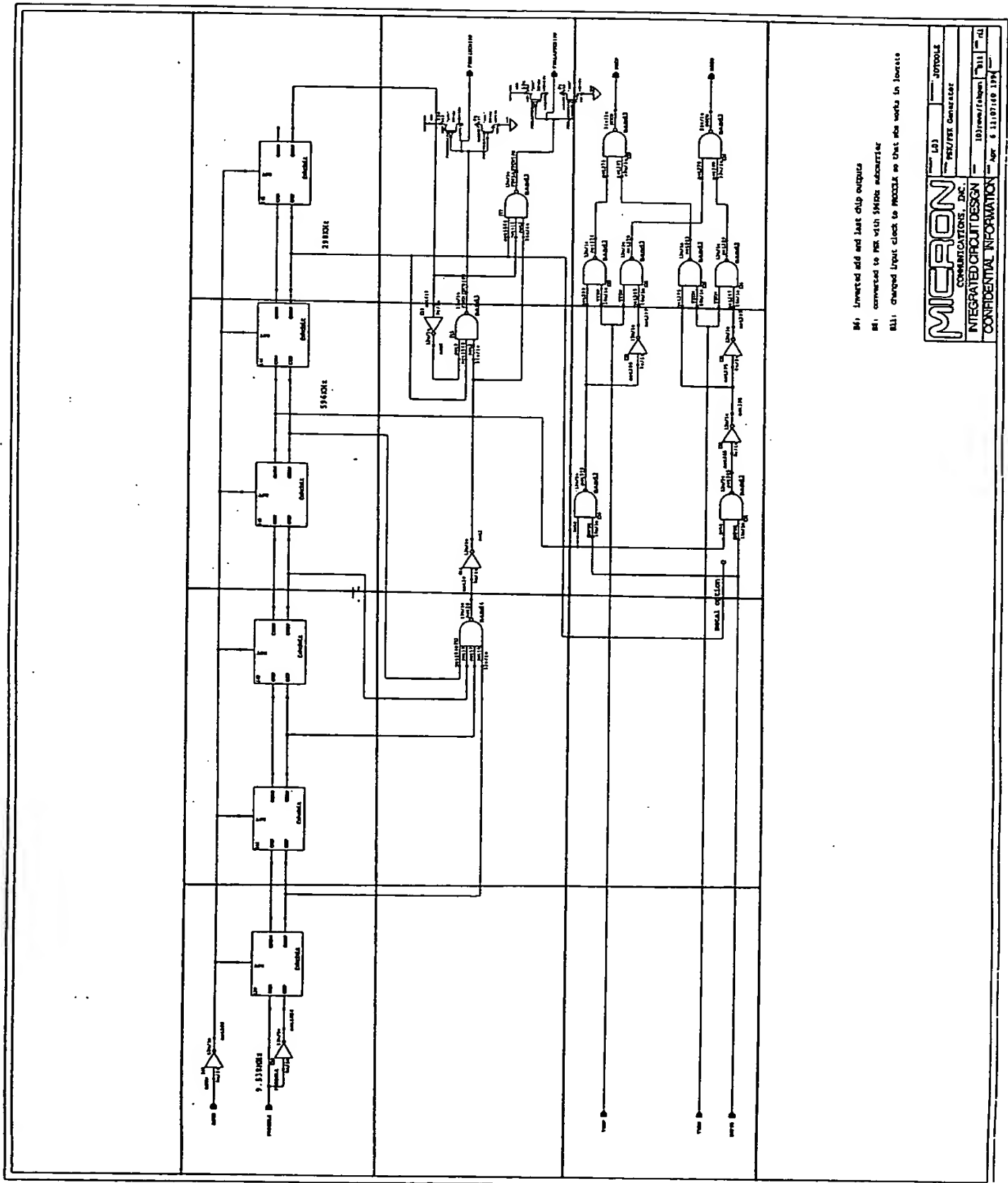
10.06AA	10.06AB	10.06AC	10.06AD							
10.06BA	10.06BB	10.06BC	10.06BD	10.06BE	10.06BF	10.06BG	10.06BH	10.06BI	10.06BJ	10.06BK
10.06CA	10.06CB	10.06CC	10.06CD	10.06CE	10.06CF	10.06CG	10.06CH	10.06CI	10.06CJ	10.06CK
10.06DA	10.06DB	10.06DC	10.06DD	10.06DE	10.06DF	10.06DG	10.06DH			

111. **How many people are there in the world?**

10.07AA	10.07AB	10.07AC	10.07AD
10.07BA	10.07BB	10.07BC	10.07BD
10.07CA	10.07CB	10.07CC	10.07CD

II II III

004720 00920500



M1: Inverted and last chip outputs  
M2: Connected to M1 with 5400E subcarrier  
M3: Charged input clock to M003A in Unit 404 works in locate

Fig. 10.07

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part 103	JUT0010
Part 104	PER/PER Converter
Part 105	1031000/1000
Part 106	1031000/1000
Part 107	1031000/1000
Part 108	1031000/1000
Part 109	1031000/1000
Part 110	1031000/1000
Part 111	1031000/1000
Part 112	1031000/1000
Part 113	1031000/1000
Part 114	1031000/1000
Part 115	1031000/1000
Part 116	1031000/1000
Part 117	1031000/1000
Part 118	1031000/1000
Part 119	1031000/1000
Part 120	1031000/1000
Part 121	1031000/1000
Part 122	1031000/1000
Part 123	1031000/1000
Part 124	1031000/1000
Part 125	1031000/1000
Part 126	1031000/1000
Part 127	1031000/1000
Part 128	1031000/1000
Part 129	1031000/1000
Part 130	1031000/1000
Part 131	1031000/1000
Part 132	1031000/1000
Part 133	1031000/1000
Part 134	1031000/1000
Part 135	1031000/1000
Part 136	1031000/1000
Part 137	1031000/1000
Part 138	1031000/1000
Part 139	1031000/1000
Part 140	1031000/1000
Part 141	1031000/1000
Part 142	1031000/1000
Part 143	1031000/1000
Part 144	1031000/1000
Part 145	1031000/1000
Part 146	1031000/1000
Part 147	1031000/1000
Part 148	1031000/1000
Part 149	1031000/1000
Part 150	1031000/1000
Part 151	1031000/1000
Part 152	1031000/1000
Part 153	1031000/1000
Part 154	1031000/1000
Part 155	1031000/1000
Part 156	1031000/1000
Part 157	1031000/1000
Part 158	1031000/1000
Part 159	1031000/1000
Part 160	1031000/1000
Part 161	1031000/1000
Part 162	1031000/1000
Part 163	1031000/1000
Part 164	1031000/1000
Part 165	1031000/1000
Part 166	1031000/1000
Part 167	1031000/1000
Part 168	1031000/1000
Part 169	1031000/1000
Part 170	1031000/1000
Part 171	1031000/1000
Part 172	1031000/1000
Part 173	1031000/1000
Part 174	1031000/1000
Part 175	1031000/1000
Part 176	1031000/1000
Part 177	1031000/1000
Part 178	1031000/1000
Part 179	1031000/1000
Part 180	1031000/1000
Part 181	1031000/1000
Part 182	1031000/1000
Part 183	1031000/1000
Part 184	1031000/1000
Part 185	1031000/1000
Part 186	1031000/1000
Part 187	1031000/1000
Part 188	1031000/1000
Part 189	1031000/1000
Part 190	1031000/1000
Part 191	1031000/1000
Part 192	1031000/1000
Part 193	1031000/1000
Part 194	1031000/1000
Part 195	1031000/1000
Part 196	1031000/1000
Part 197	1031000/1000
Part 198	1031000/1000
Part 199	1031000/1000
Part 200	1031000/1000



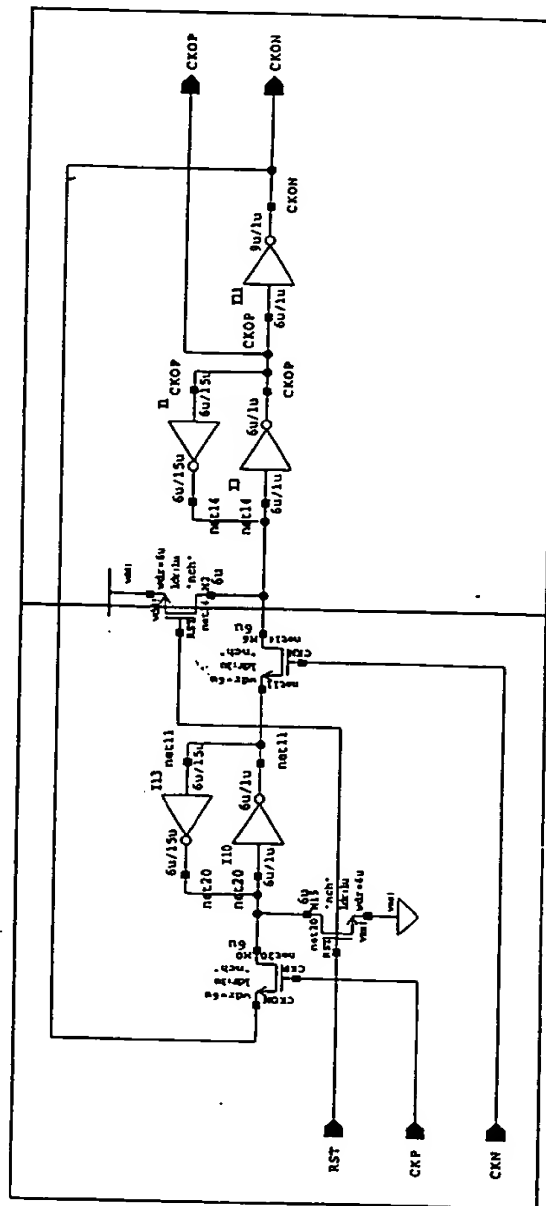
001100" 00000000

MI40-030

10.0701AA	10.0701AB
-----------	-----------

11.11.11.11.11

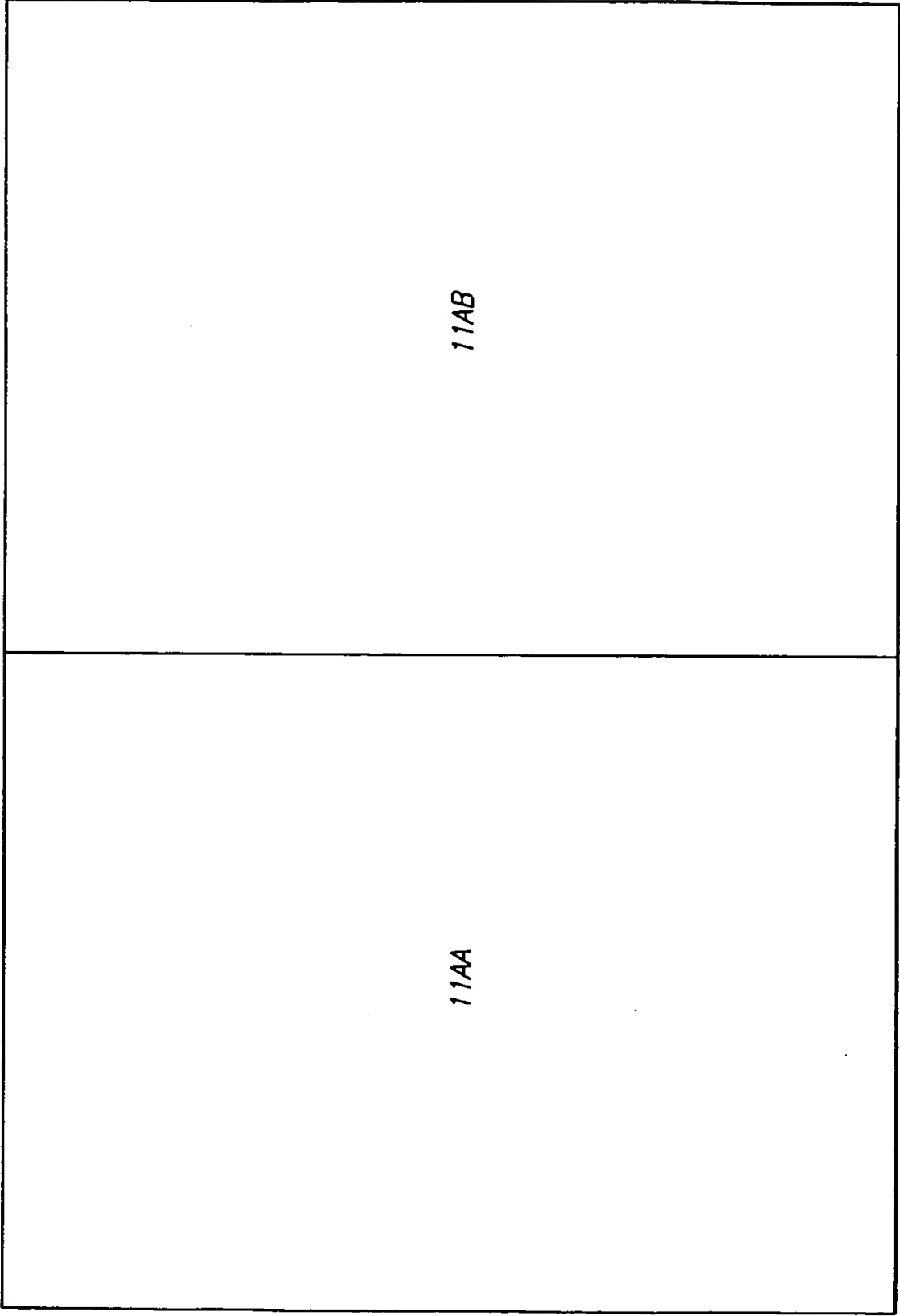
NOTED - E0920500



MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		TITLE: FSK Counter Bit	
INTEGRATED CIRCUIT DESIGN		Same as wuab0rt_cbit	
CONFIDENTIAL INFORMATION		NAME: 103rev0a/fskcbit	REV: B1
		DATE: Apr 17 15:42:44 1995	SIZE: A

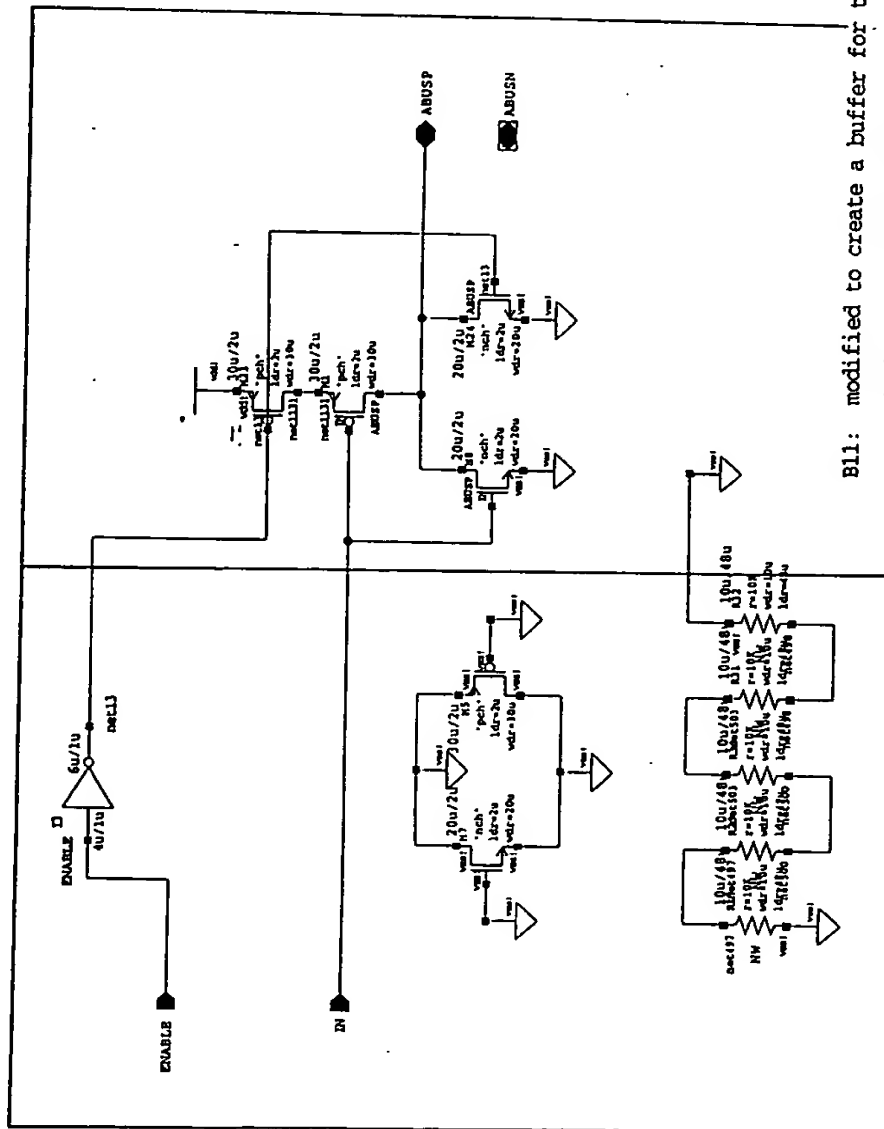
FIG. 10.0701

004420-00300500



11 11

CONFIDENTIAL



B11: modified to create a buffer for the opamp output  
the battery voltage divider is now part of tsm

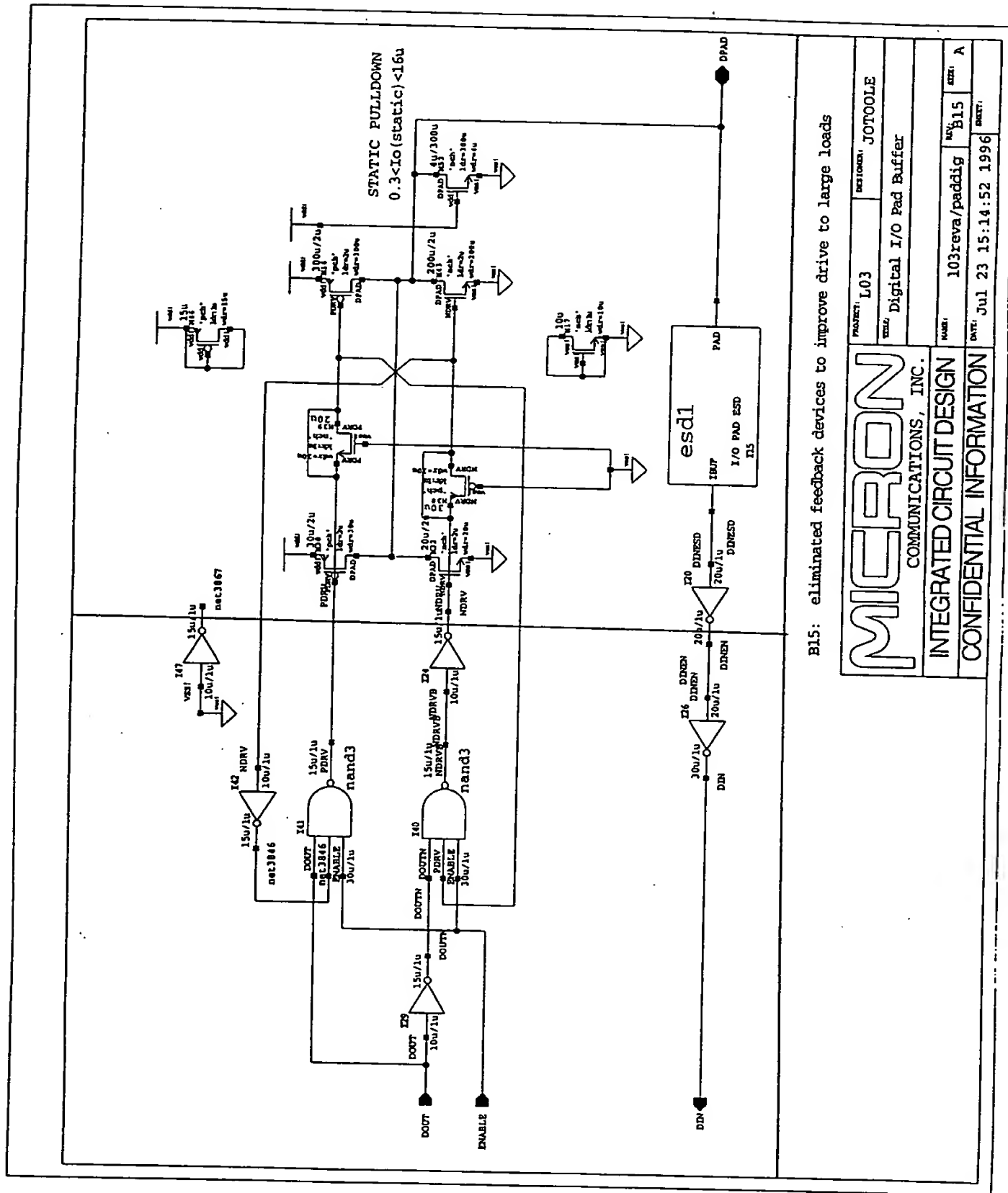
MICRON		PROJECT: L03	REVISION: J0700LE
INTEGRATED CIRCUIT DESIGN		TITLE: Battery Analog I/O Buffer	
COMMUNICATIONS, INC.		NAME: 103reva/batalg	REV: B11
CONFIDENTIAL INFORMATION		DATE: Apr 8 10:19:56 1996	POST: A

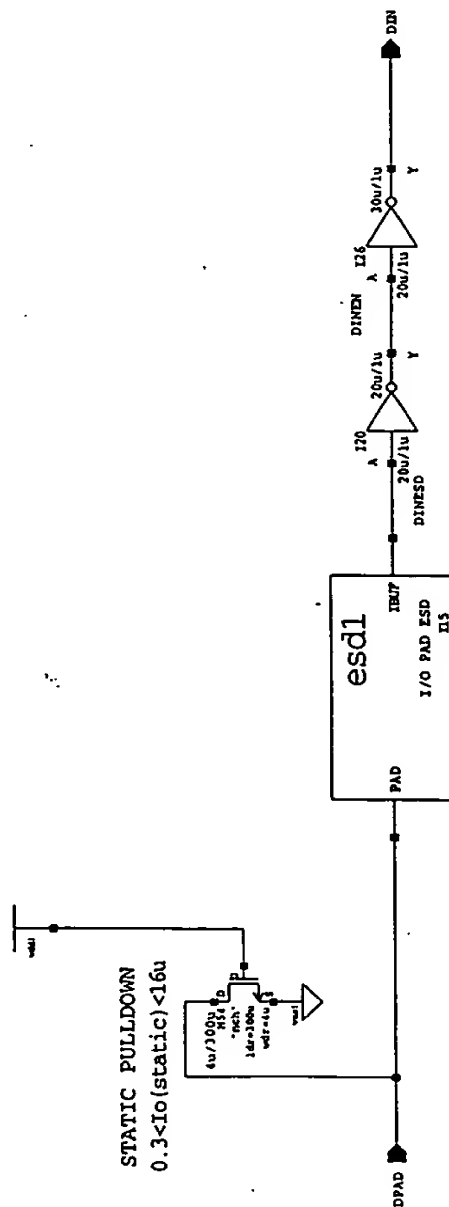
Fig. 11

12AB

112

Fig. 12AA-AB

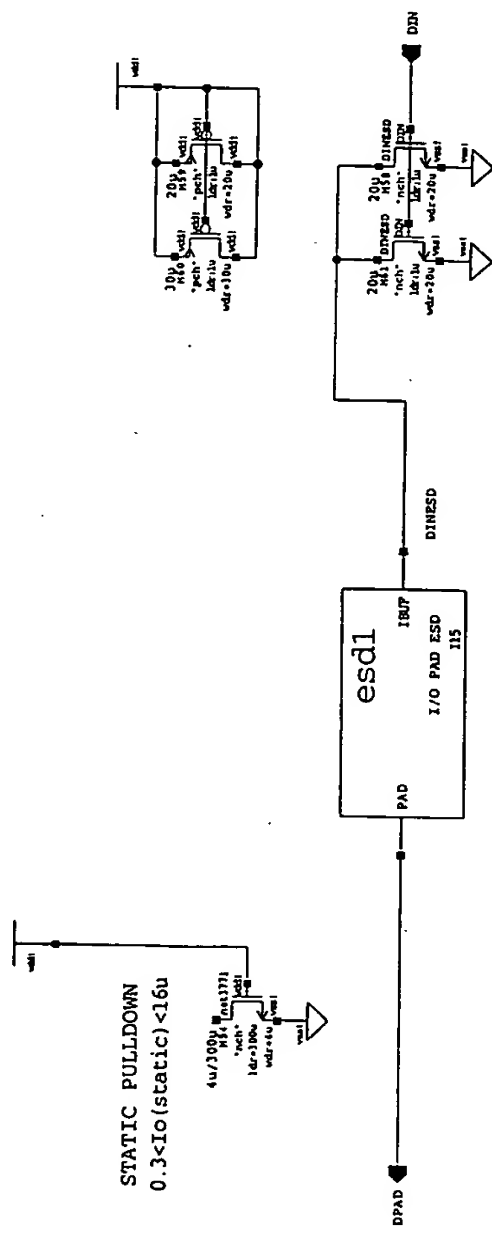


[illegible]

<b>MICRON</b>		PART: L03		REVISION: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: Digital Input Pad Buffer			
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/paddigin		REV: B1	
CONFIDENTIAL INFORMATION		DATE: Apr 11 11:10:35 1995		PAGE: 1	

Fig. 13

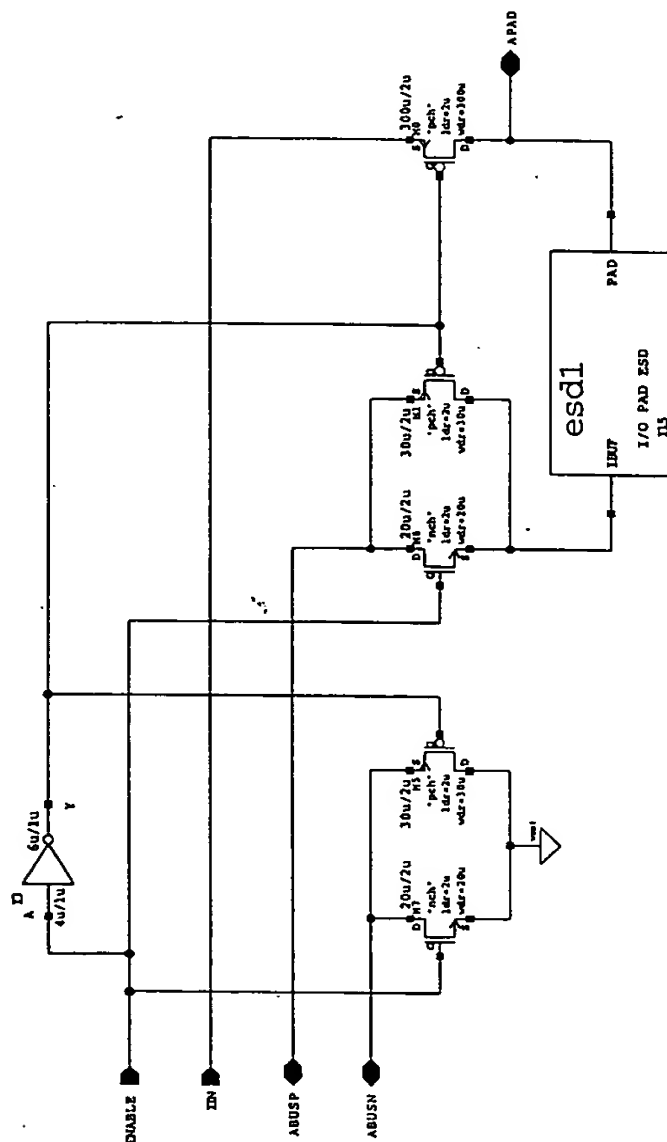
FIG 13.5



MICRON		PART: L03	DESIGN: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Digital Input Pad Buffer	
INTEGRATED CIRCUIT DESIGN		DATE: 103reva/paddigin2	REV: B13
CONFIDENTIAL INFORMATION		DATE: May 24 18:28:29 1996	REV: A

B13: new cell for WAKEUP\* output



[illegible]

<b>MICRON</b>			
COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN			
CONFIDENTIAL INFORMATION			
PROJECT:	L03	DESIGNER:	Rotzoll
TITLE:	Analog I/O Pad Buffer		
NAME:	103eva/padalg	REV:	-
DATE:	DEC 12 21:55:41 1993		SIZE: A

Fig. 19

15AA	15AB	15AC	15AD	15BC
	15BA	15BB		

516.15



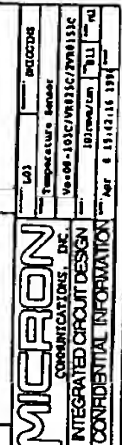
00000000000000000000

MI40-030

16AA	16AB	16AC	16AD	16AE	16AF	16AG	16AH
16BA	16BB	16BC	16BD	16BE	16BF	16BG	16BH
16CA	16CB	16CC	16CD	16CE	16CF	16CG	16CH
16DA	16DB	16DC	16DD	16DE	16DF	16DG	16DH
16EA	16EB	16EC	16ED	16EE	16EF	16EG	16EH

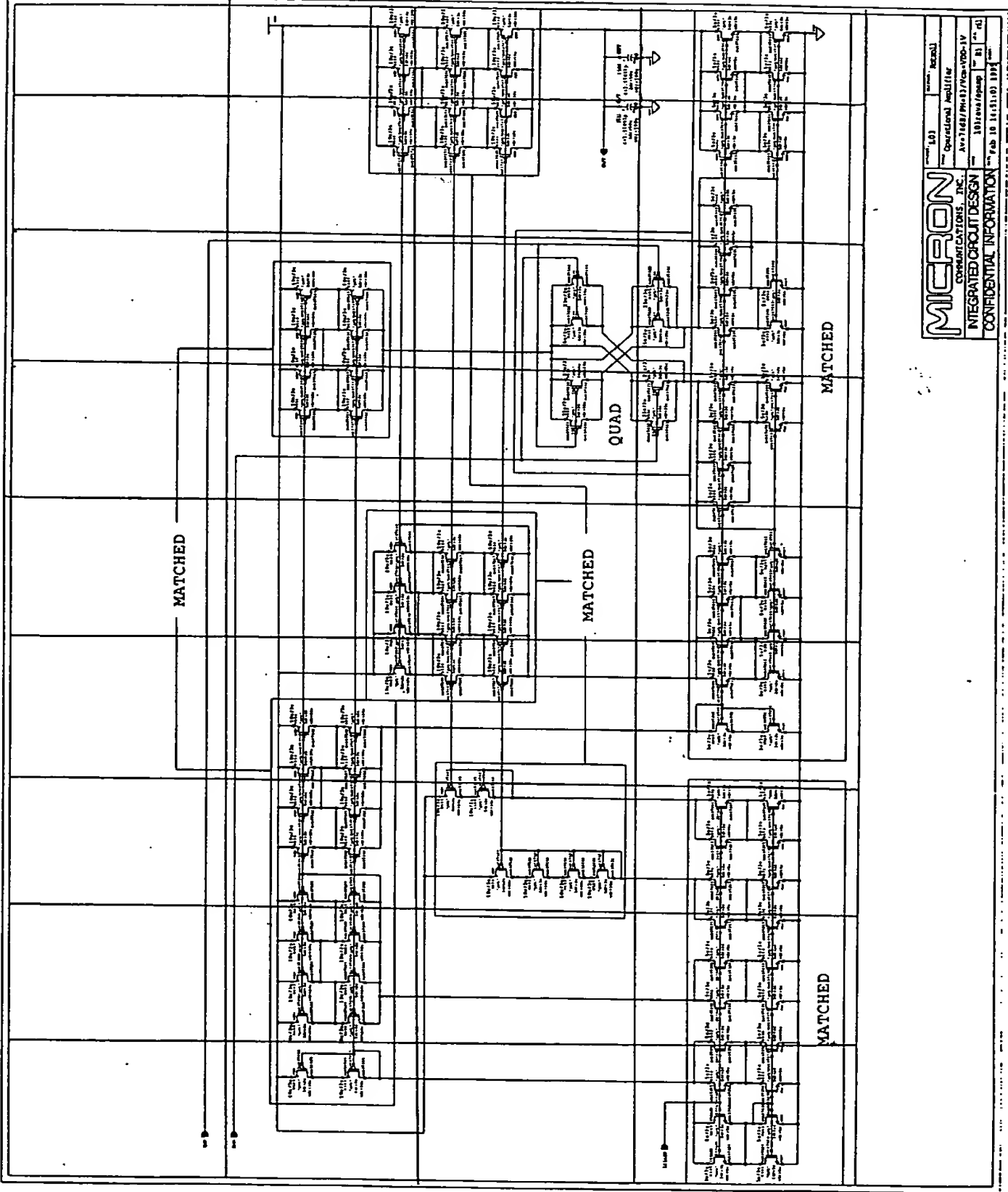
11 11 11

Fig. 16.16



16.01AA	16.01AB	16.01AC	16.01AD	16.01AE	16.01AF	16.01AG		
16.01BA	16.01BB	16.01BC	16.01BD	16.01BE	16.01BF	16.01BG	16.01BH	16.01BI
16.01CA	16.01CB	16.01CC	16.01CD	16.01CE	16.01CF	16.01CG	16.01CH	16.01CI
16.01DA	16.01DB	16.01DC	16.01DD	16.01DE	16.01DF	16.01DG	16.01DH	16.01DI

004720-20920000



<b>MICRON</b>		Q3	Q4	Q5	Q6
COMMUNICATIONS, INC.		Operational Amplifier			
INTEGRATED CIRCUIT DESIGN		Av7142/Pin13/Vcc-VDD-1V			
CONFIDENTIAL INFORMATION		101000/0000			
		Rev 10 11-11-00 1000			

FIG. 16.01

001120 20020500

MI40-030

17AA	17AB
17BA	17BB

II II II

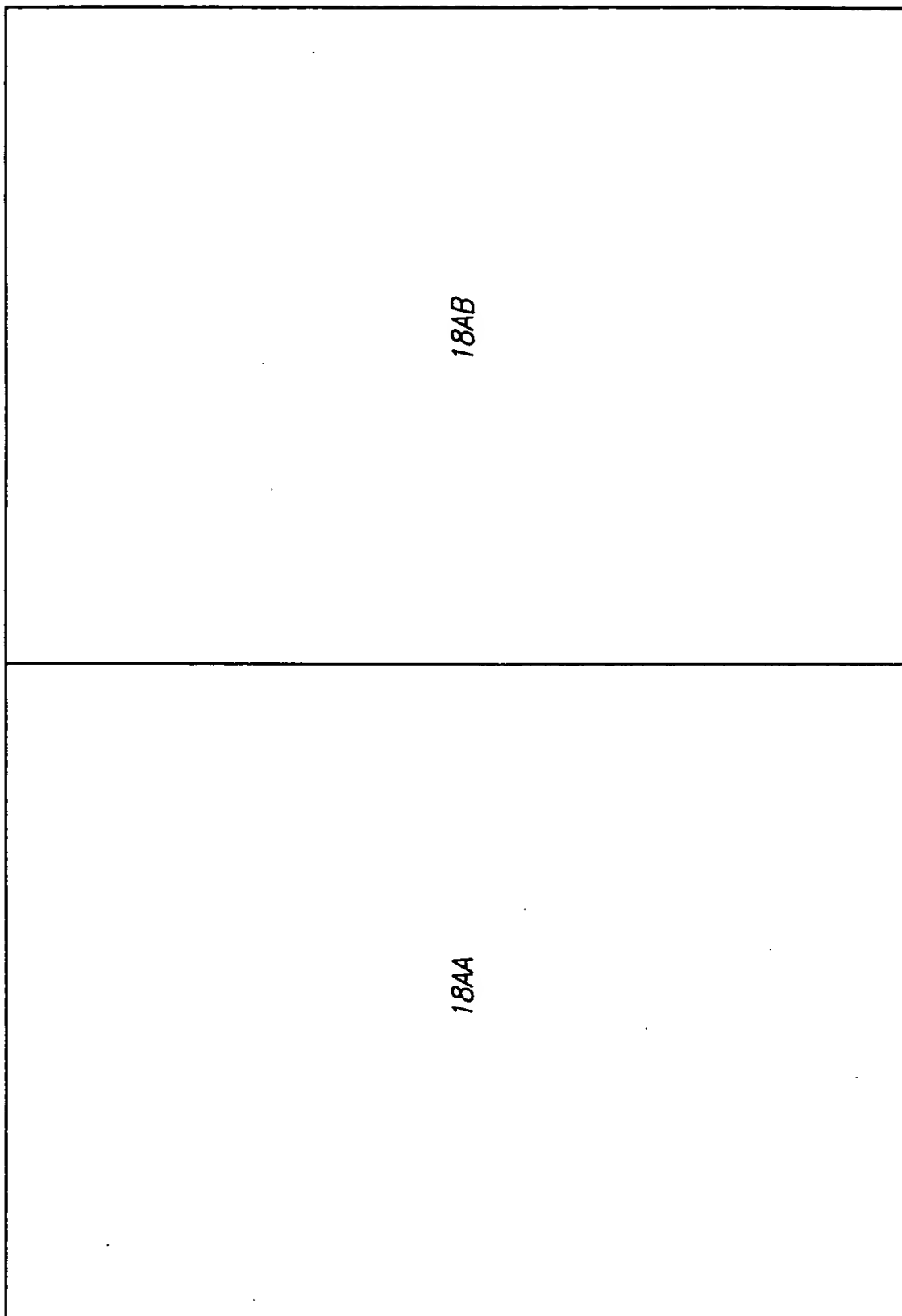


[illegible]

FIG. 17

<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	PROJECT: Rotomall
TYPE: Magnetic Field Sensor	
DATE: 10/29/96	DATE: 8/8/96
DATE: Jan 9 08:58:38 1996	

001120 20000000



11 11 11

004420 00000000

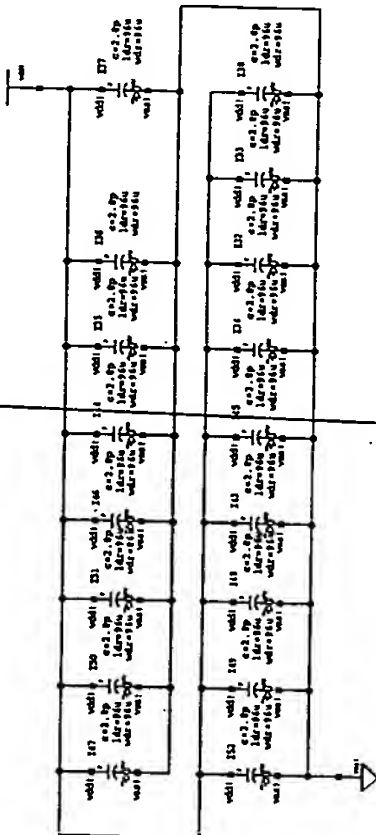


FIG. 18

MICRON		PART NO. 103	REV. 000000
COMMUNICATIONS, INC.		Chip Bypass Capacitor	
INTEGRATED CIRCUIT DESIGN		CT=ppf	
CONFIDENTIAL INFORMATION		DATE: 10/10/94/bypcap3	BY: B2
		DATE: Jul 28 17:43:25 1995	FILE: nl

B2: deleted one cap

00000000000000000000

MI40-030

19AA	19AB	19AC	19AD	19AE	19AF	19AG	19AH	19AI	19AJ	19AK
19BA	19BB	19BC	19BD	19BE	19BF	19BG	19BH	19BI	19BJ	19BK
19CA	19CB	19CC	19CD	19CE	19CF	19CG	19CH	19CI	19CJ	19CK
19DA	19DB	19DC	19DD	19DE	19DF	19DG	19DH	19DI	19DJ	19DK
19EA	19EB	19EC	19ED	19EE	19EF	19EG	19EH	19EI	19EJ	19EK

II II II II II

CONFIDENTIAL

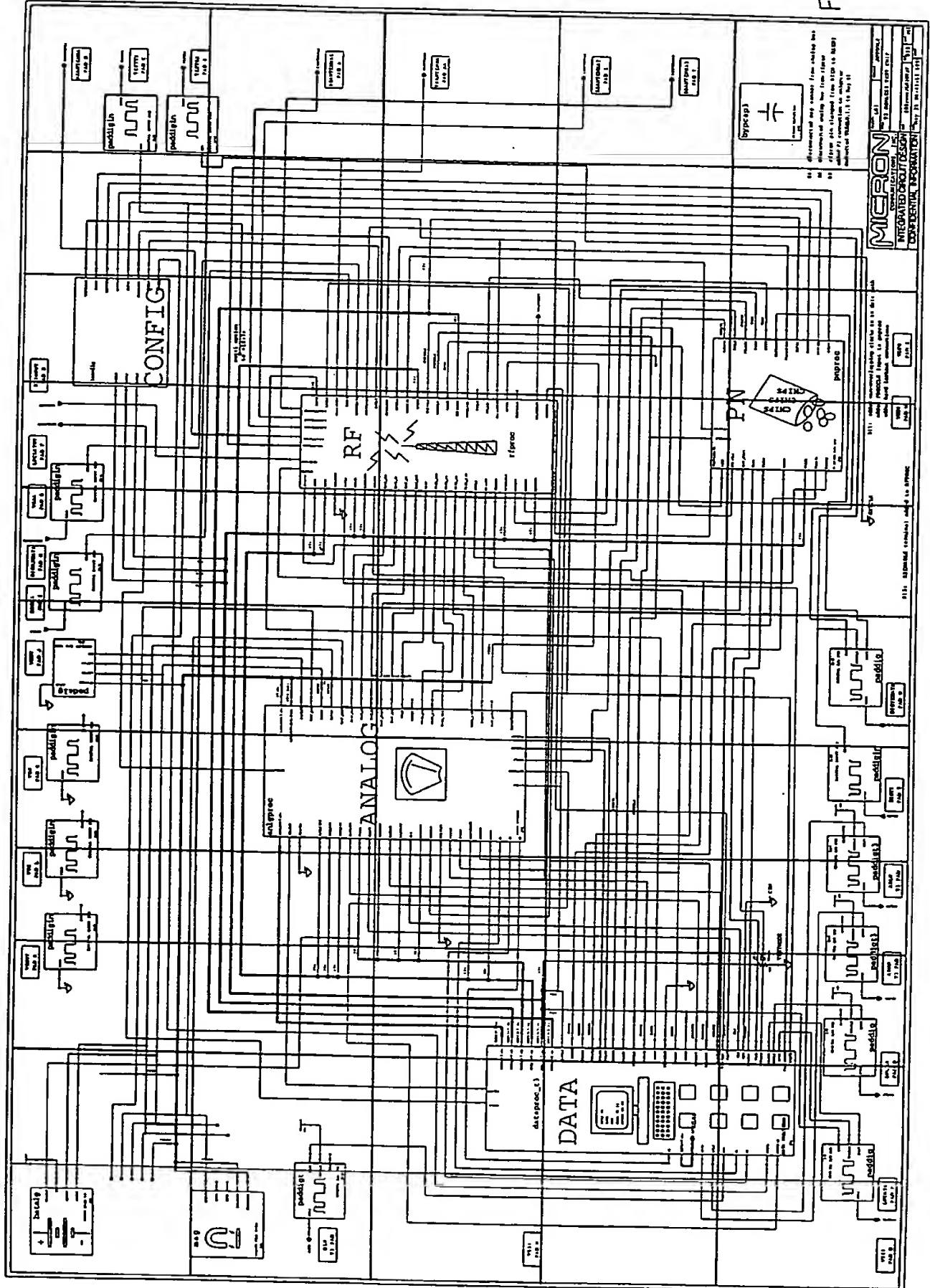


Fig. 19.AA-EK

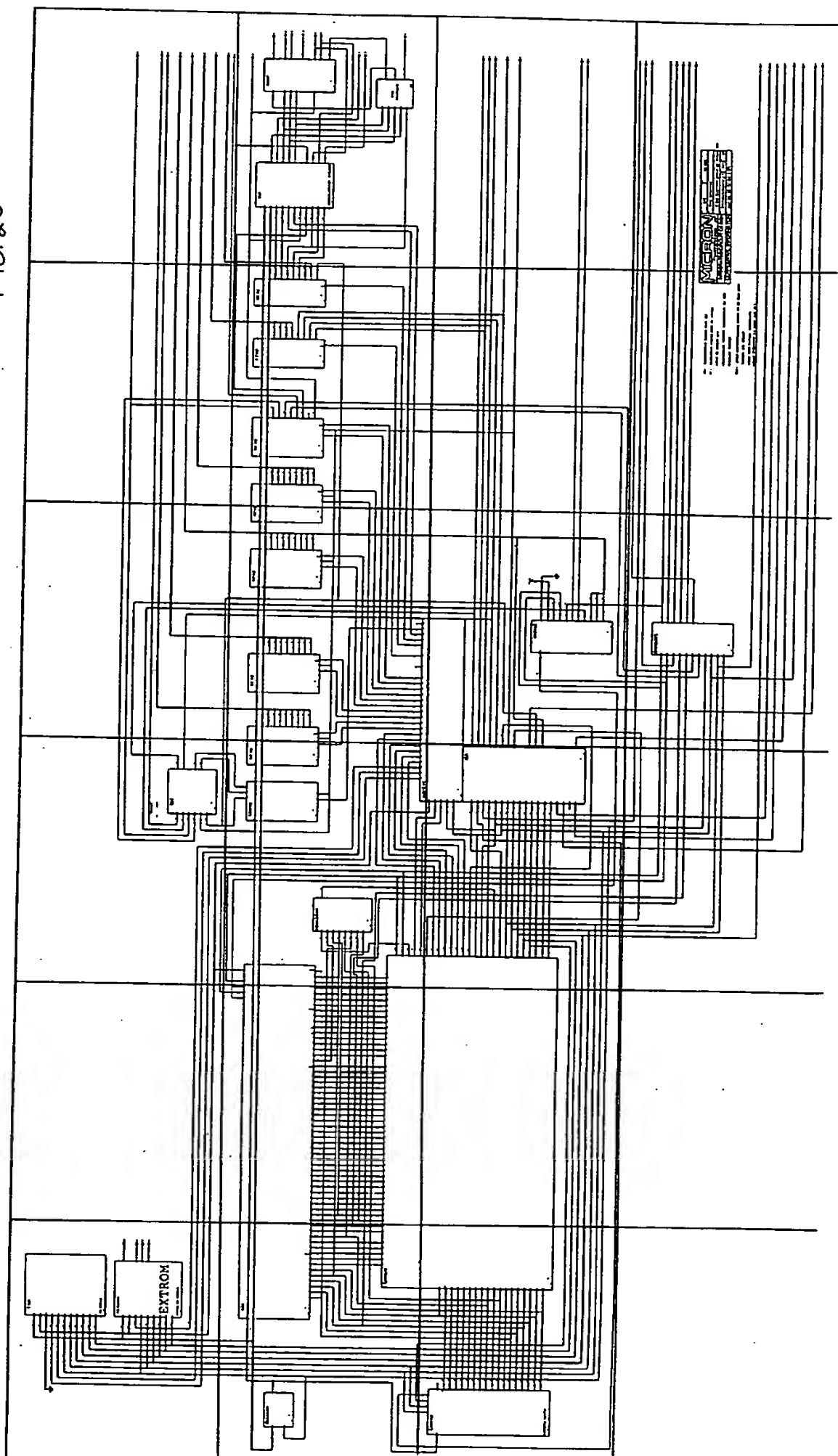
00000000000000000000

20AA	20AB	20AC	20AD	20AE	20AF
20BA	20BB	20BC	20BD	20BE	20BF
20CA	20CB	20CC	20CD	20CE	20CF
		20DC	20DD	20DE	20DF

11 11 11 11

004420 20920500

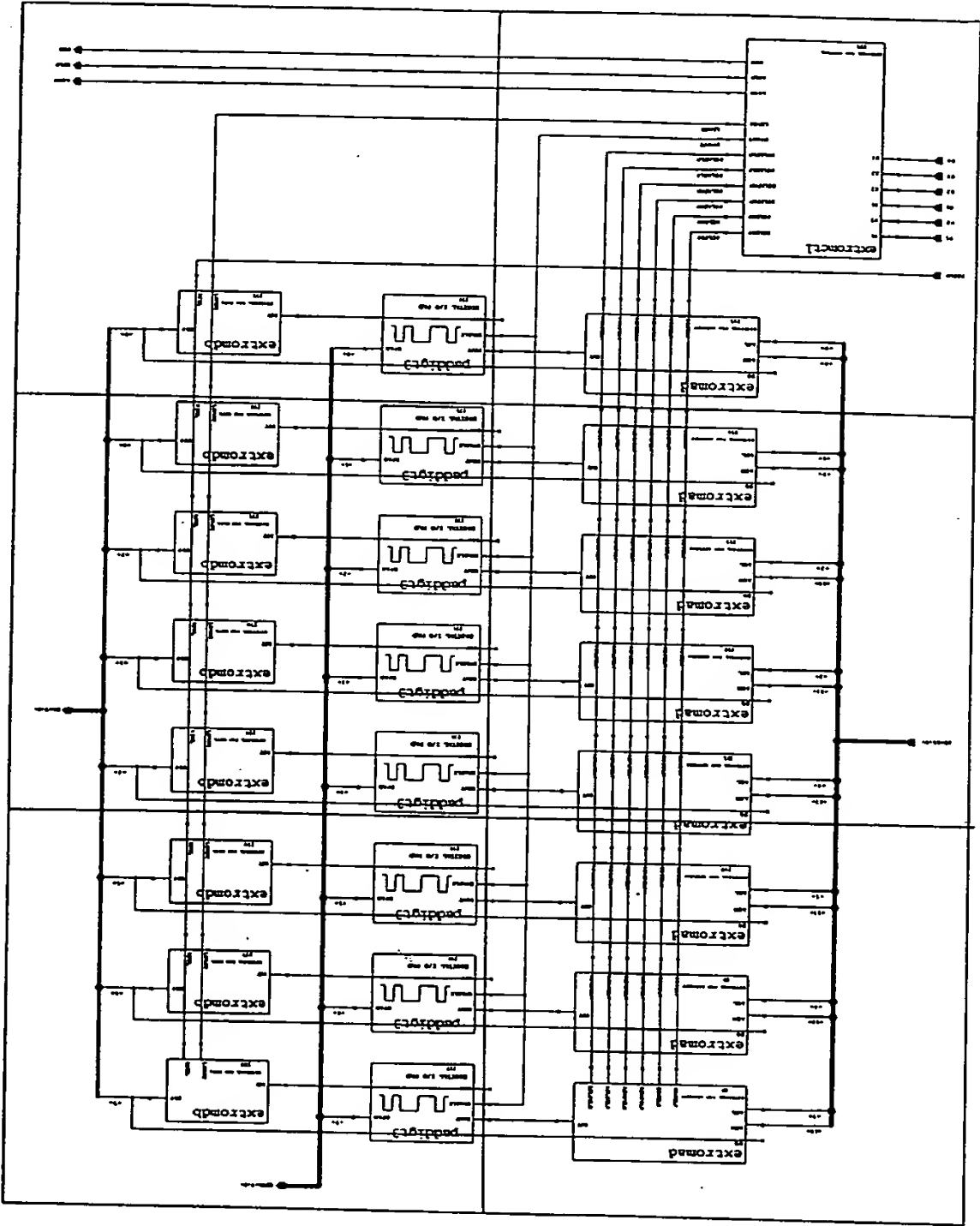
Fig.20



[illegible]



Fig. 20,01



00000000 00000000

00420" 000000

MI40-030

20.0101AA	20.0101AB
20.0101BA	20.0101BB

IL IL IL IL IL IL IL IL

FIG. 20.0101

**NIERON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

PROJECT:	L03	OFFICER:	Potzoll
TITLE: External ROM Control Logic			
MODEL:	103rova/extronctl	REV.:	0114
DATE:	Dec 11 21:56:41 1991		

CONFIDENTIAL

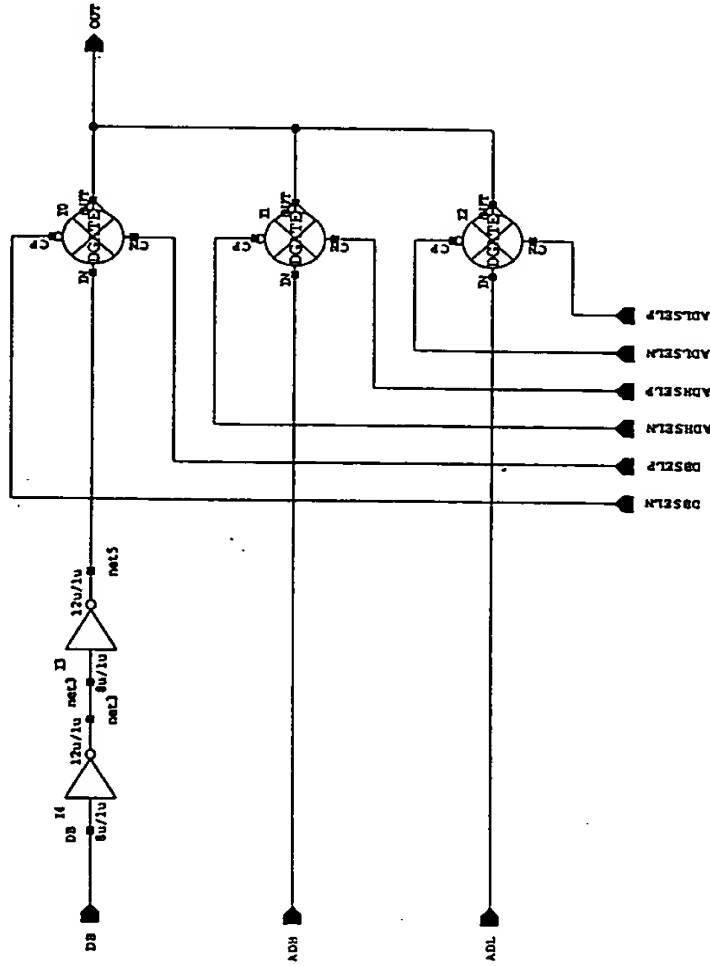


Fig. 20.0102

MICRON COMMUNICATIONS, INC.		PROJECT: L03		DESIGNER: Rotzoll	
		TITLE: External ROM Address Interface			
		NAME: 103reva/extromad		REV: -	
				SIZE: A	
		DATE: Dec 11 01:09:14 1993		PAGE: 1	
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					

001120 20000000

MI40-030

20.0103AA	20.0103AB	20.0103AC
-----------	-----------	-----------

20.0103

F19 20.0103M-AC

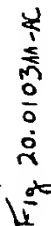
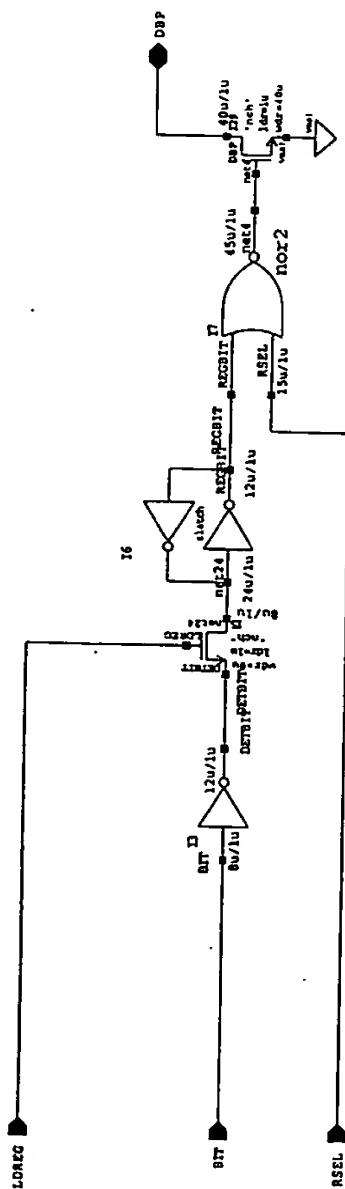


Fig. 20.0109



# zoovision

COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

**PROJECT: L03**

**Rotzoll**

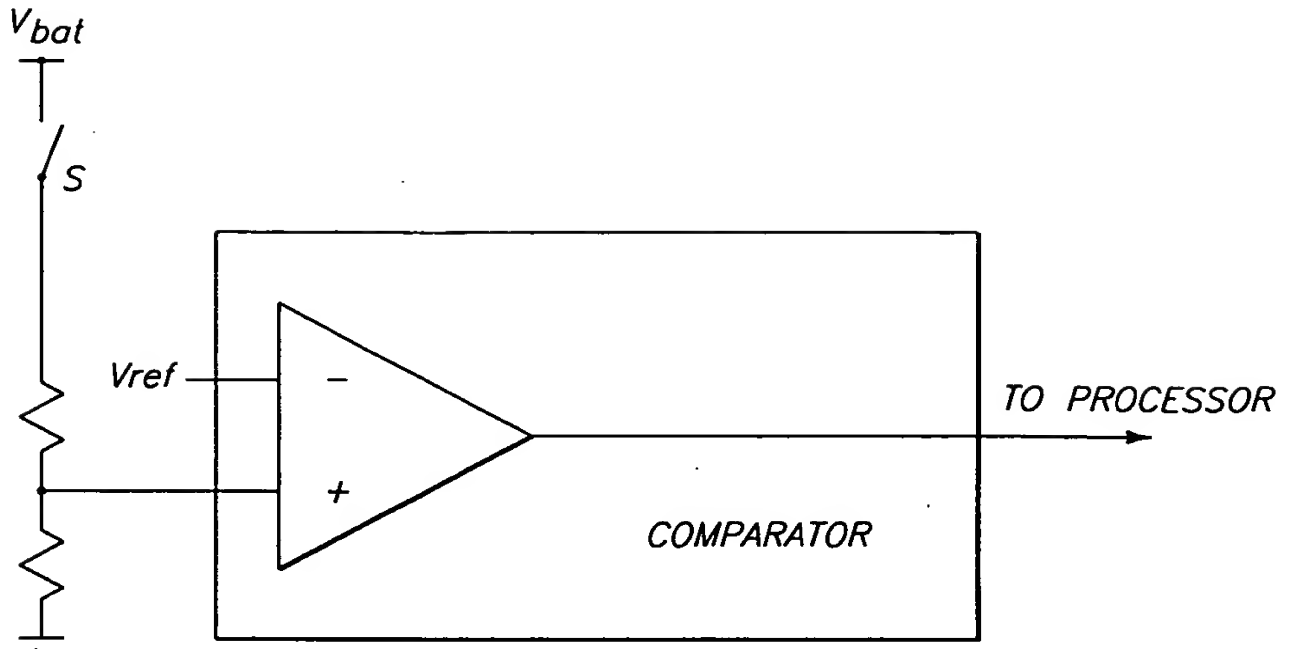
External ROM Databus Interface
--------------------------------

WAGE:	103reva/extromdb	REV:	-	SIZE:	A
-------	------------------	------	---	-------	---

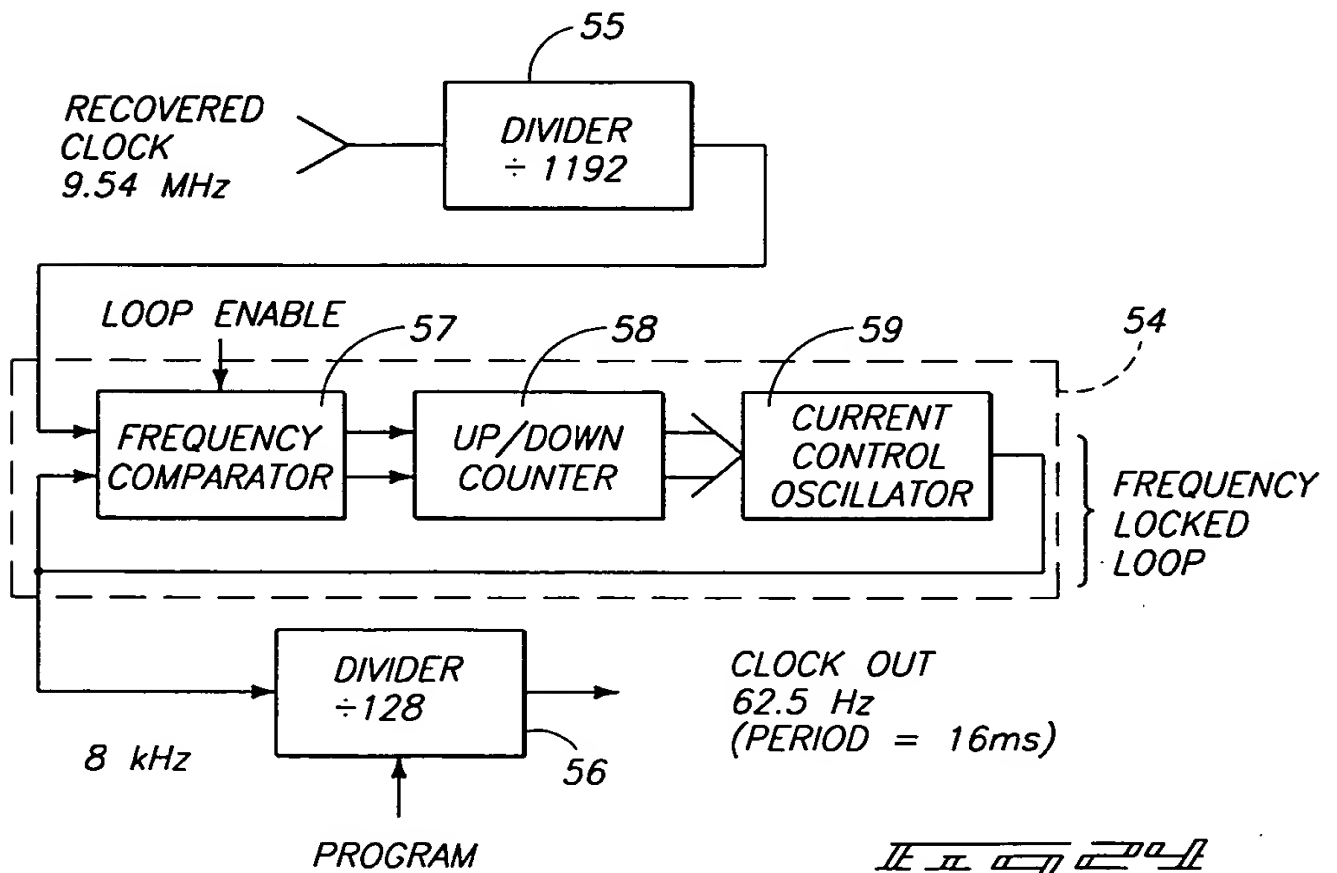
DATE:	Dec 11 01:01:13 1993	SHEET:
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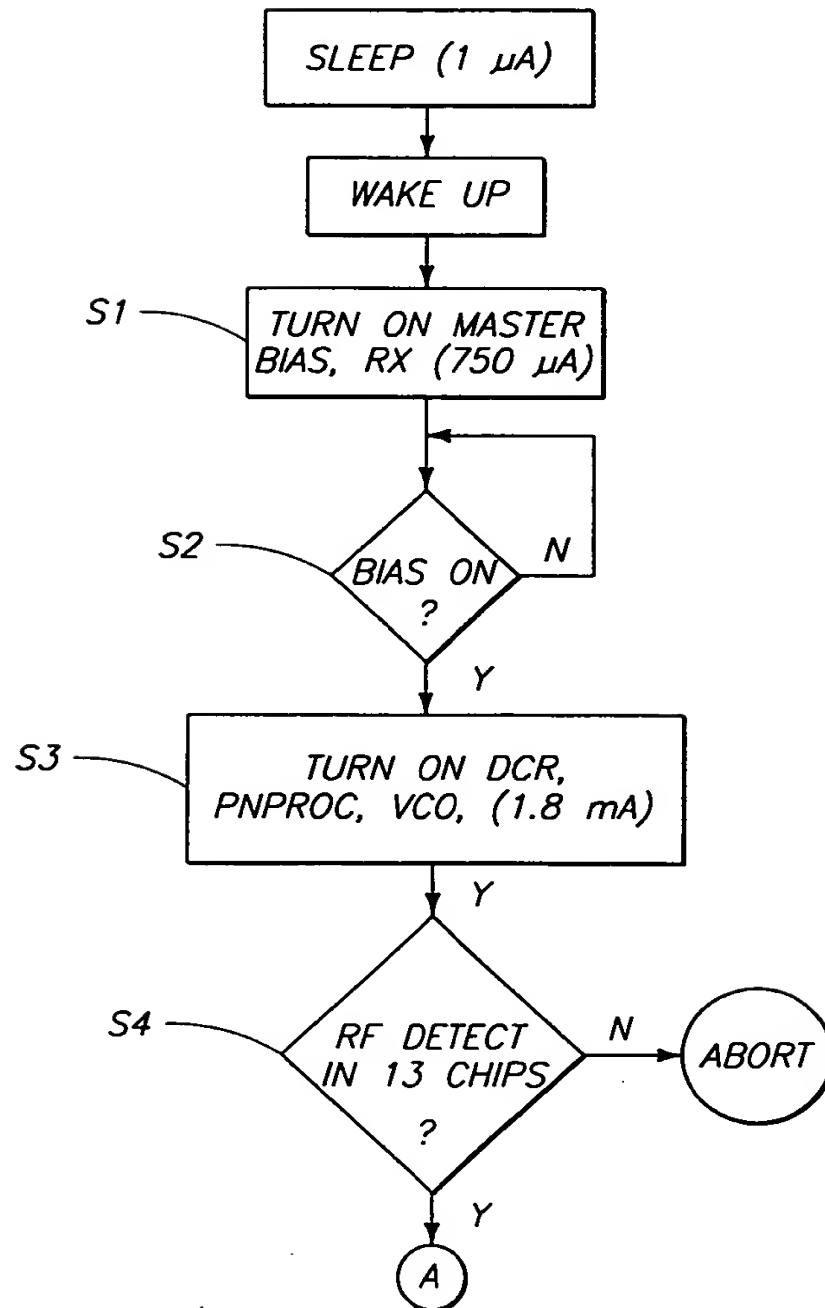




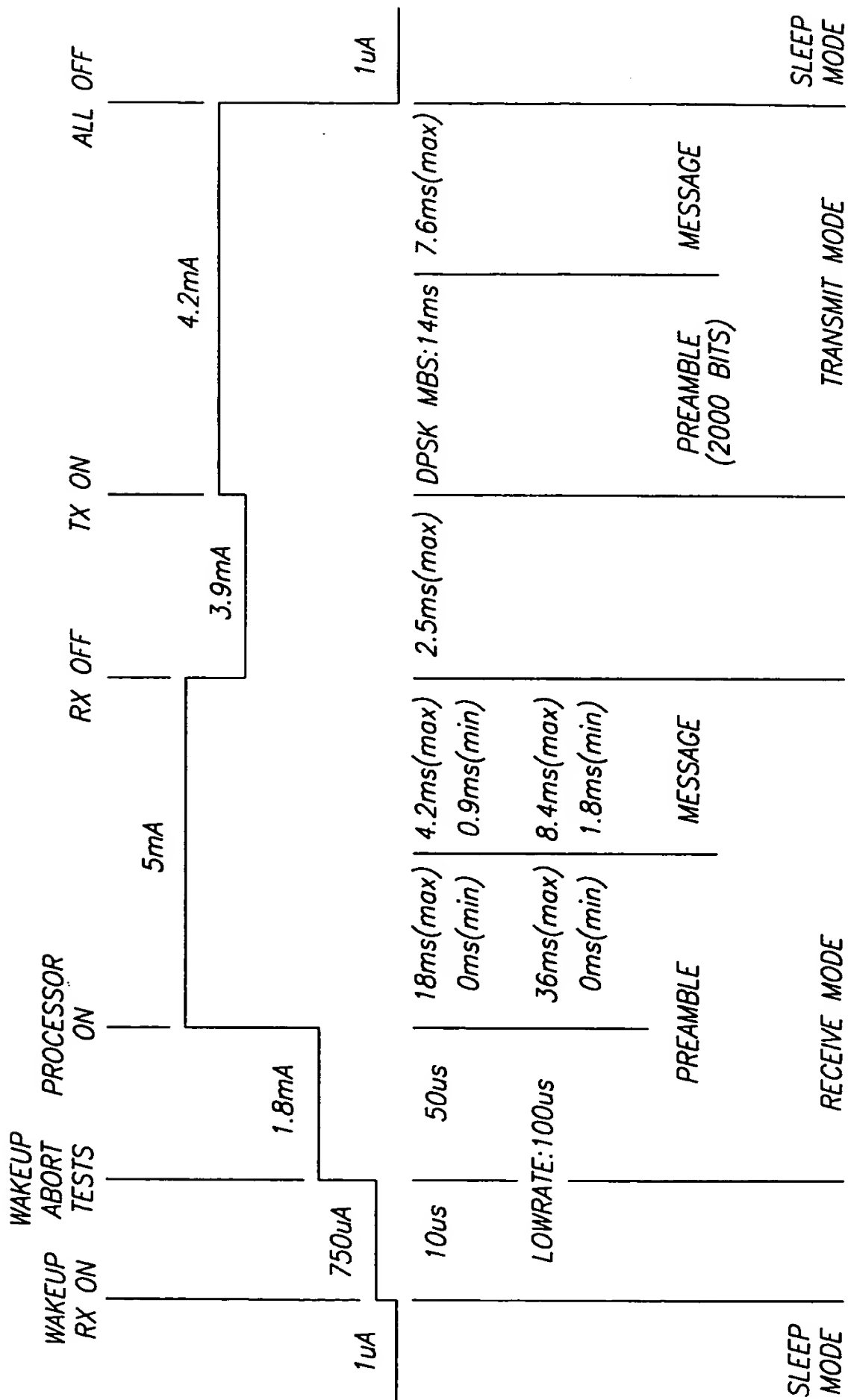


$V_{ref} = \text{bandgap voltage} \approx 1.2 \text{ V for silicon}$



WAKEUP SEQUENCE





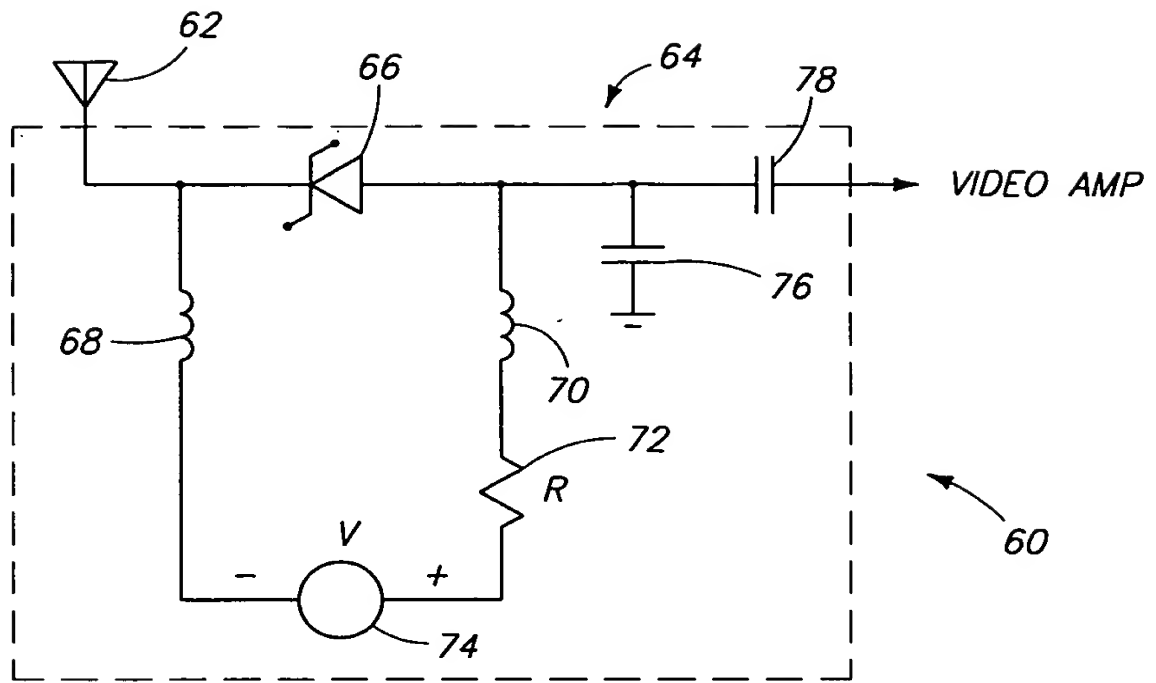


FIG. 2

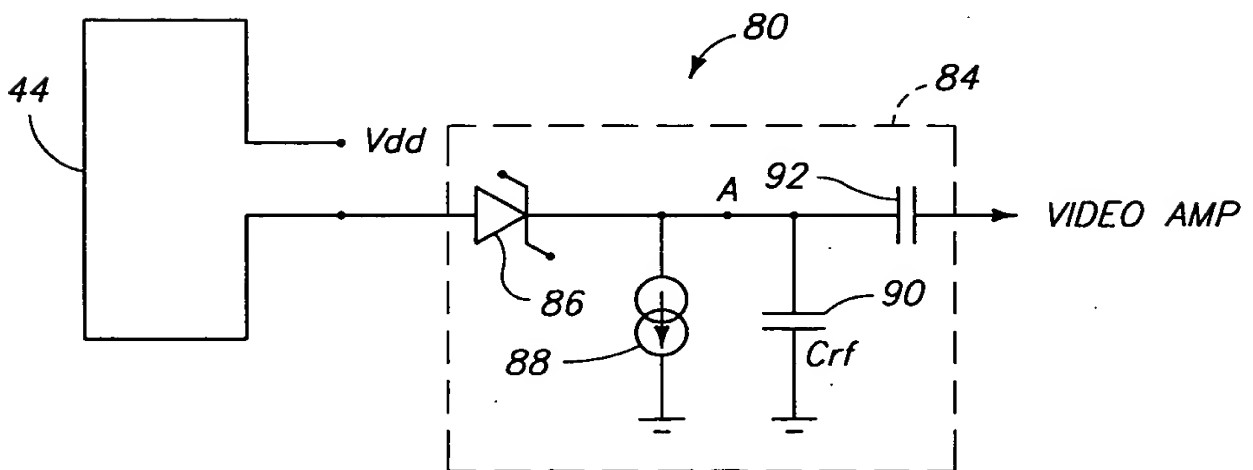
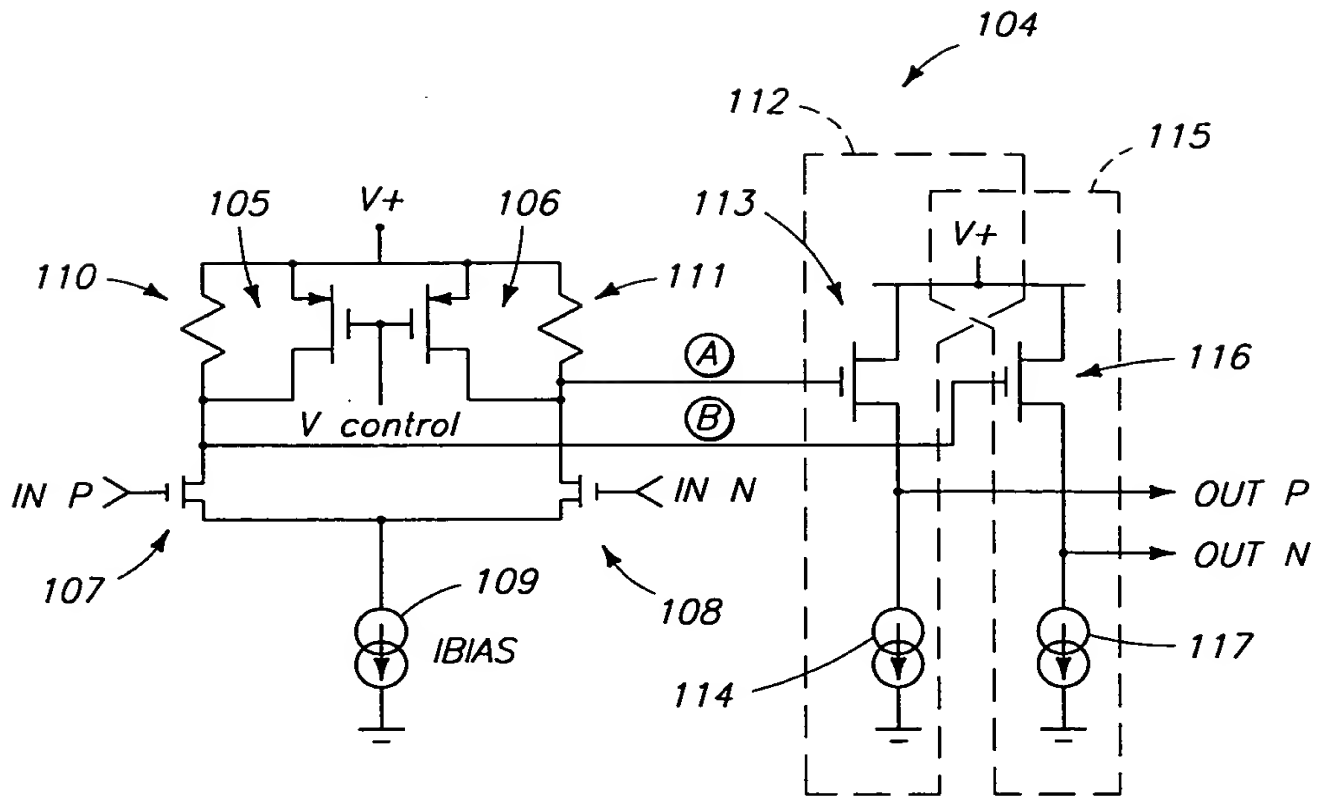
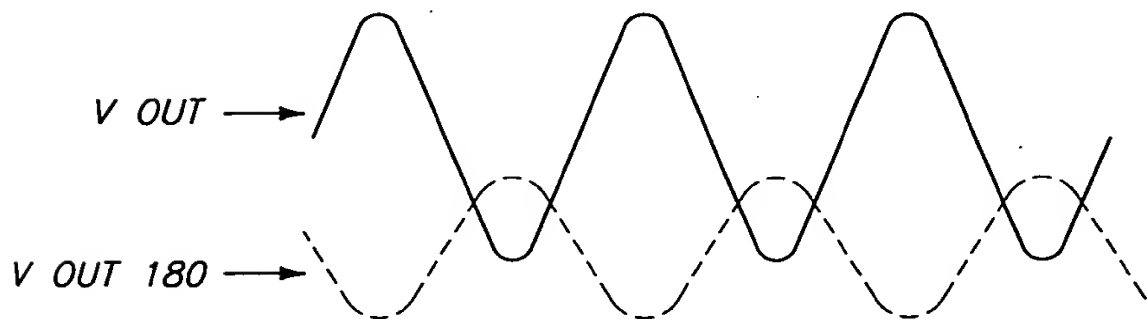


FIG. 3

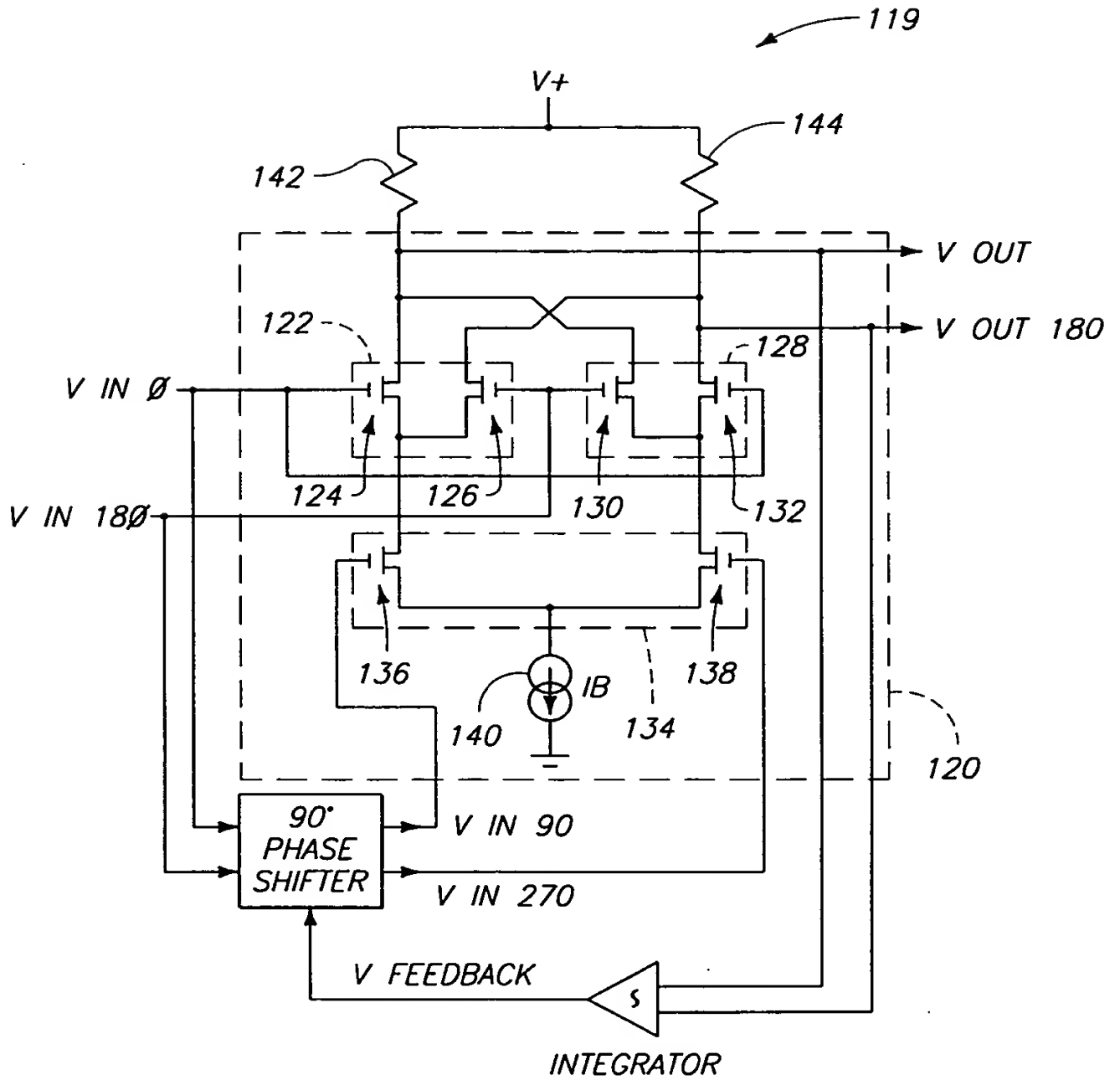




*Fig. 2*

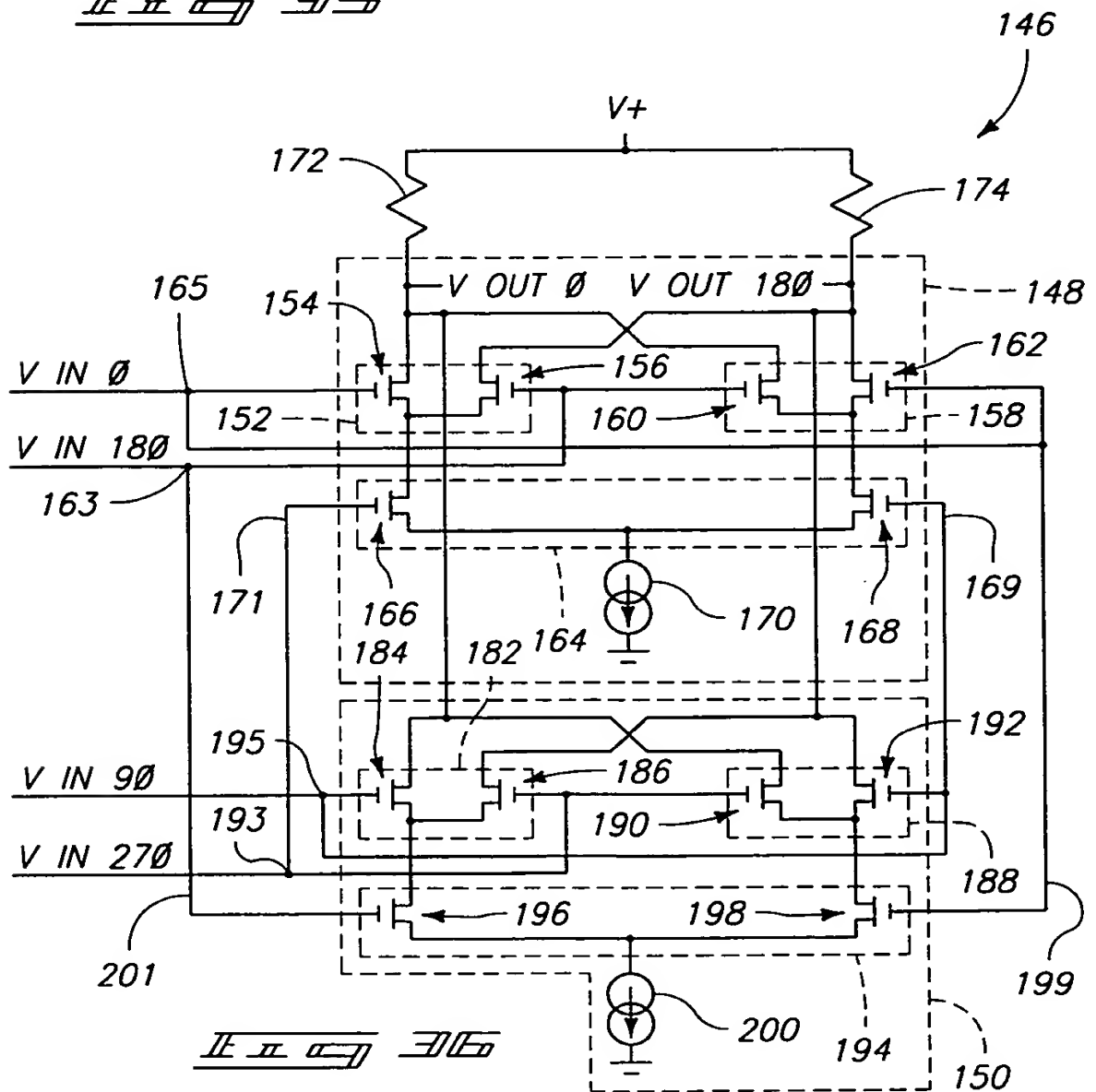
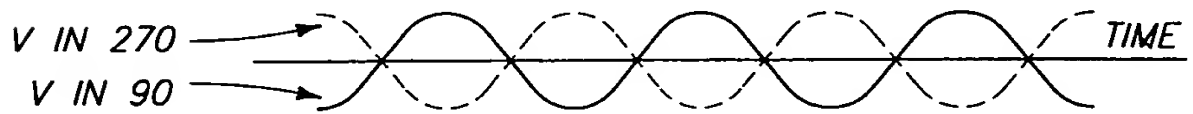


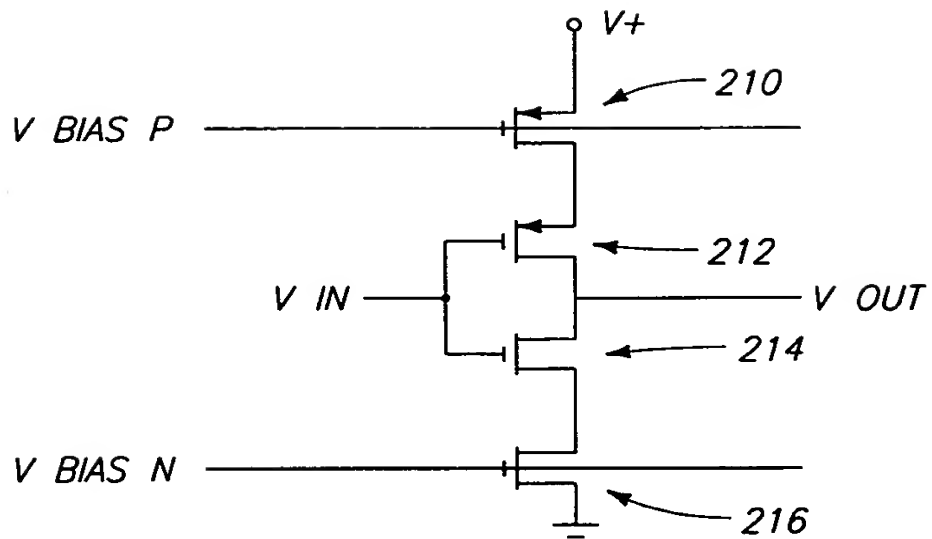
*Fig. 3*



119







IEEE

236

232

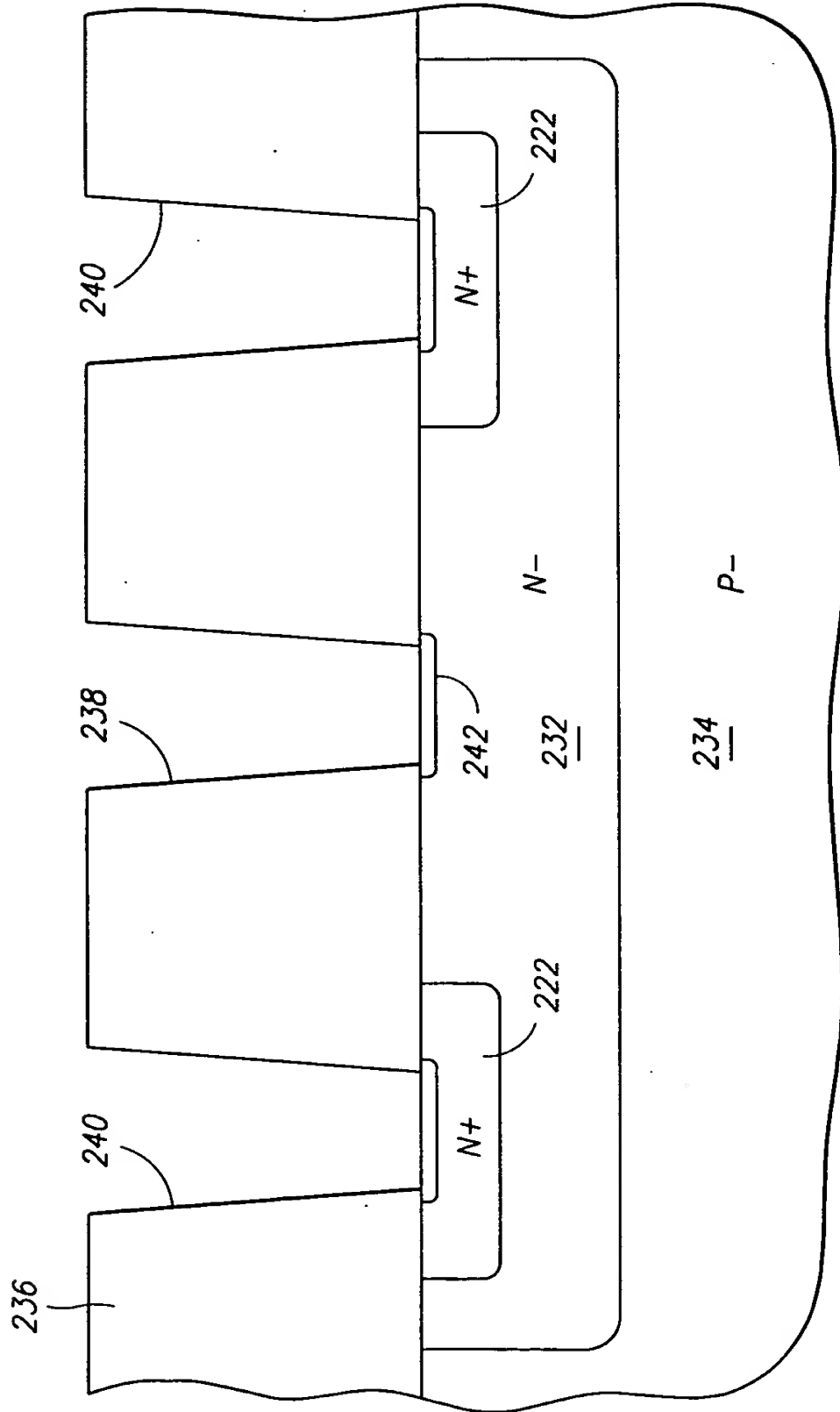
N-

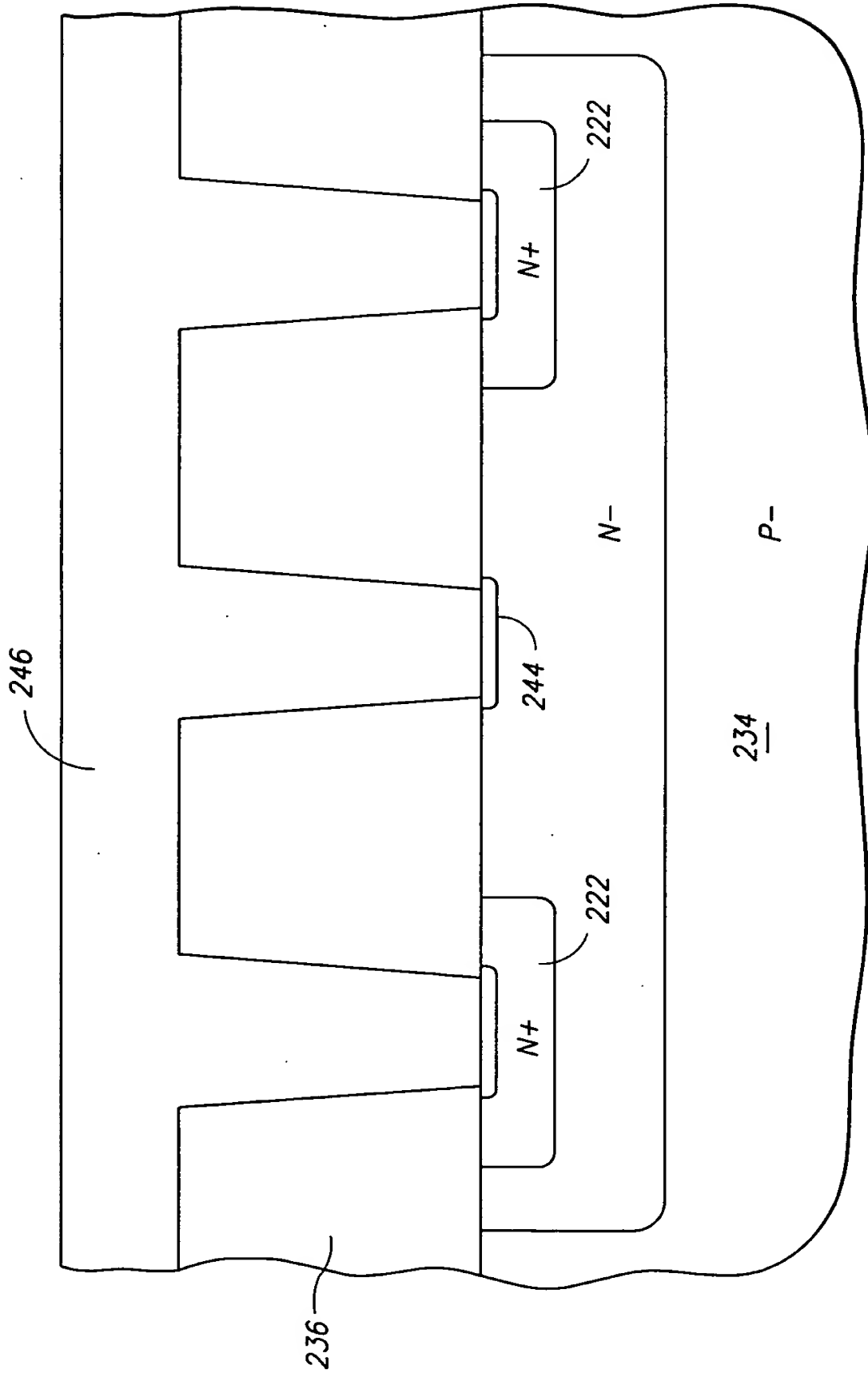
234

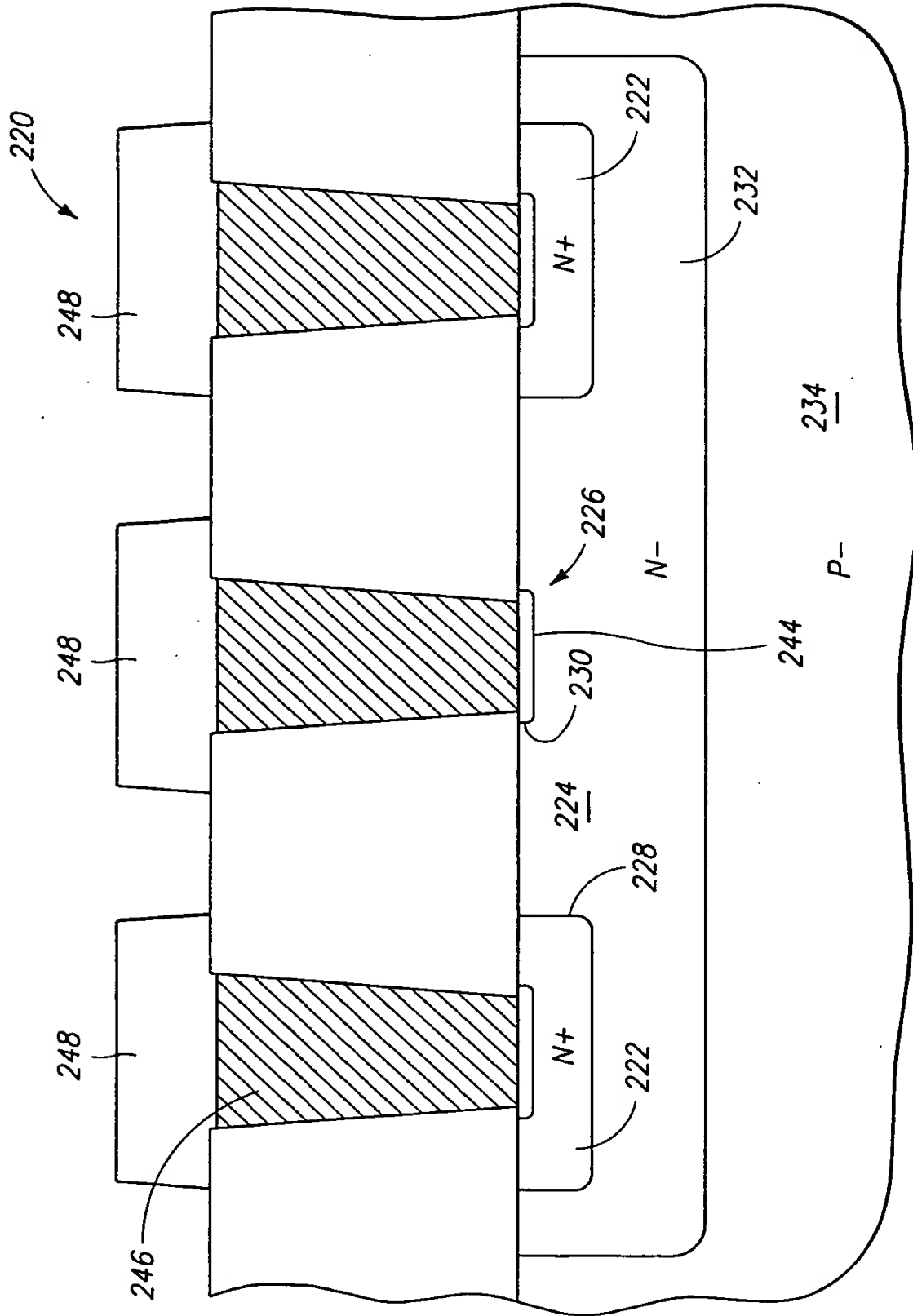
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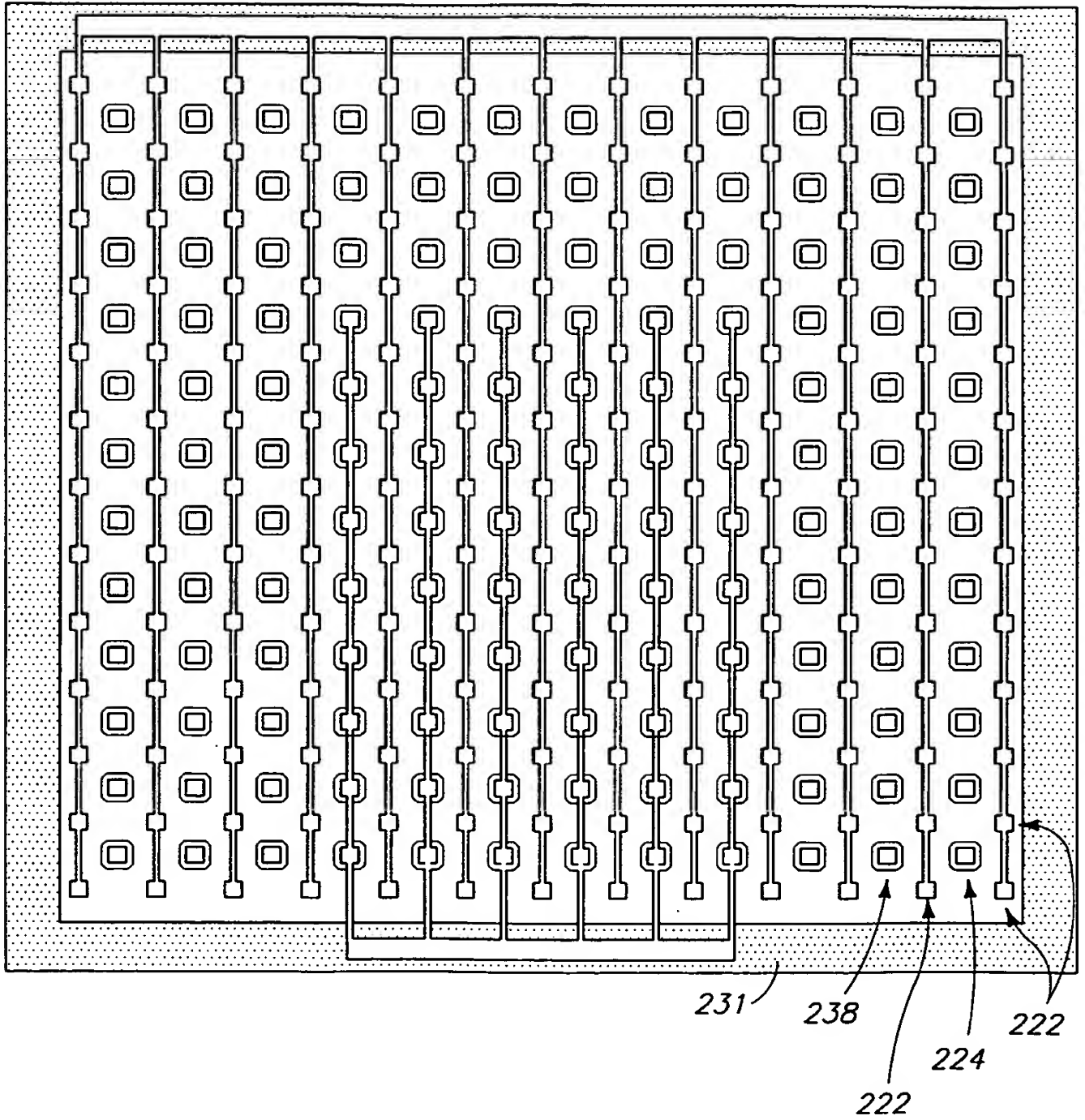






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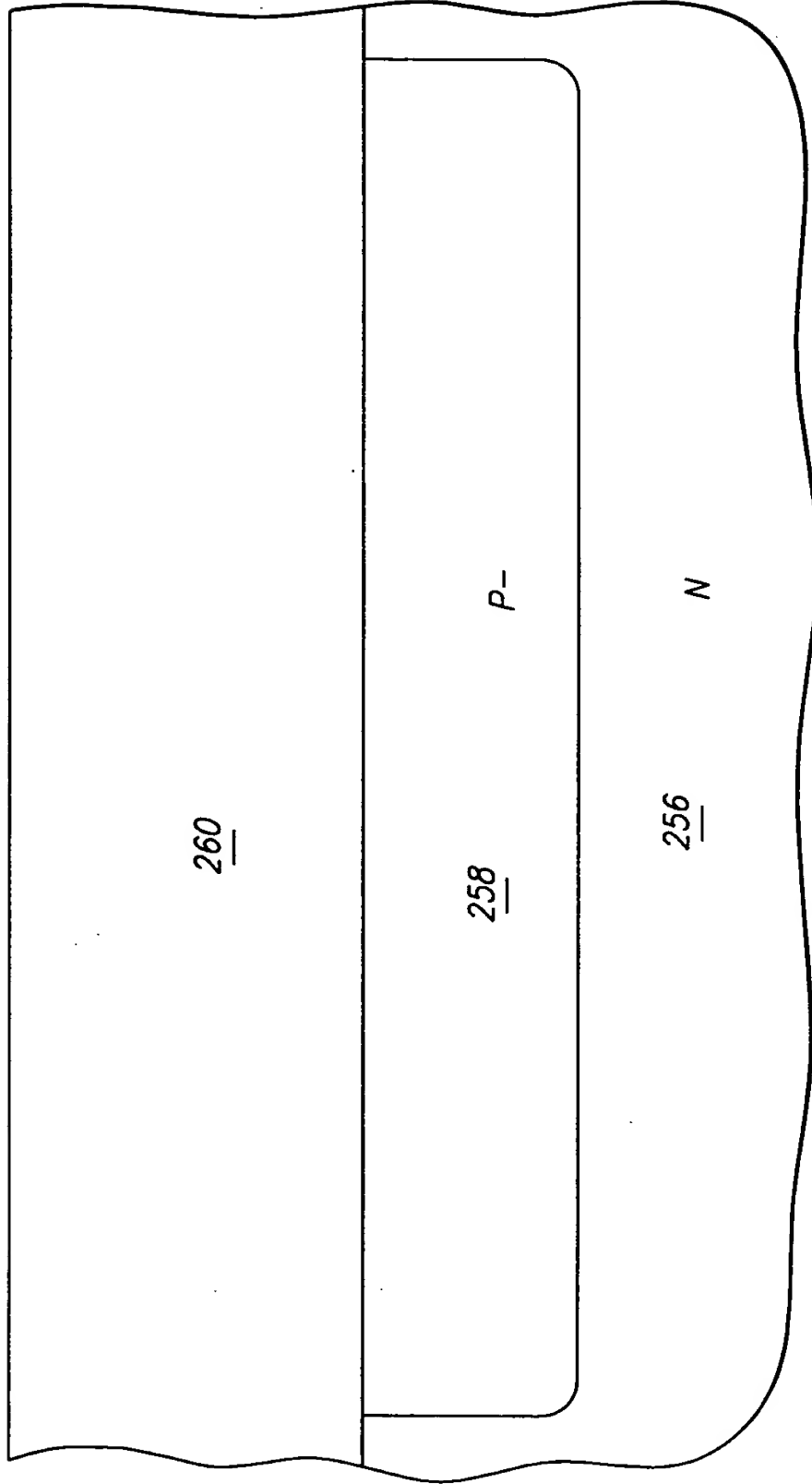




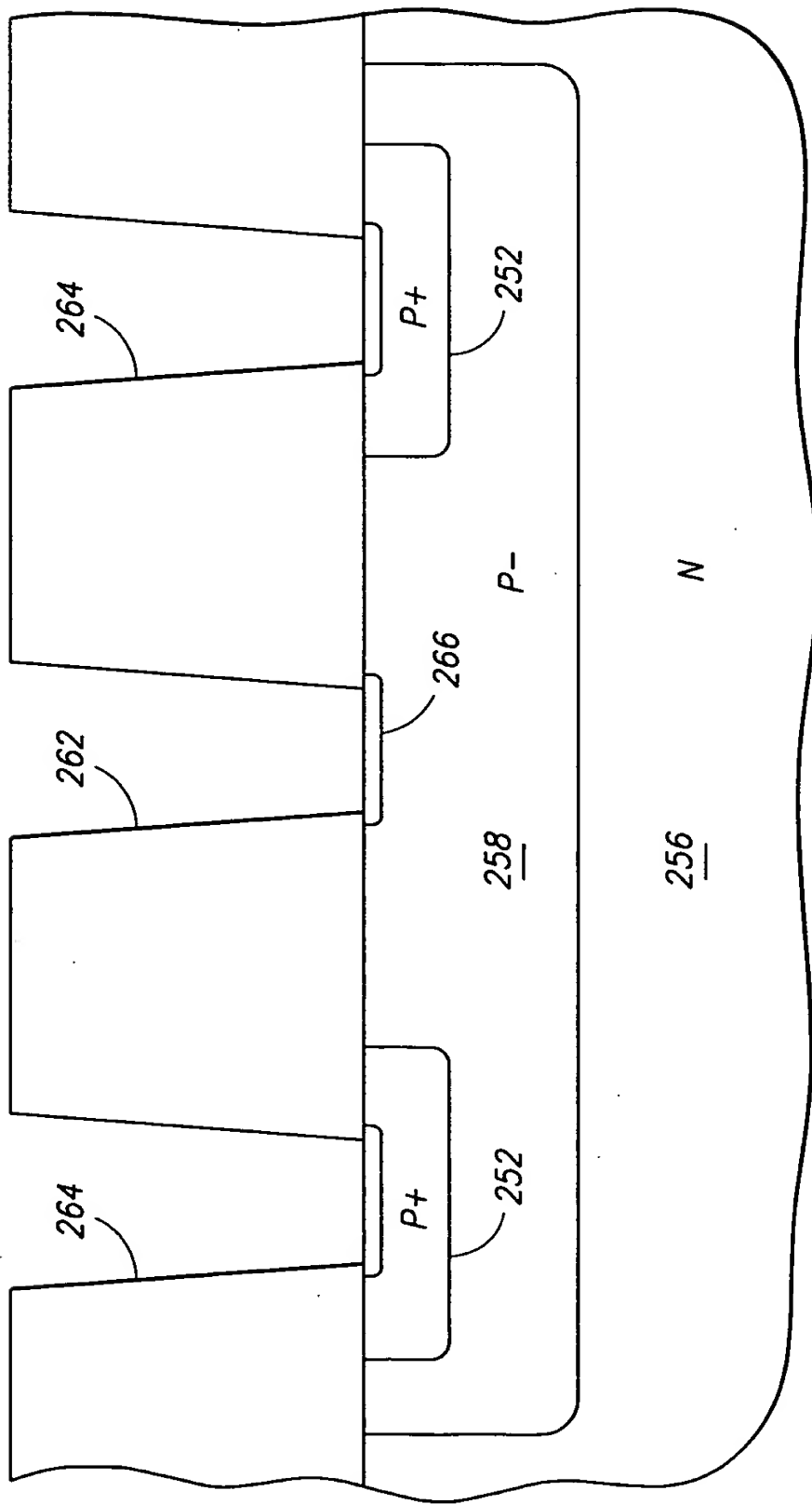


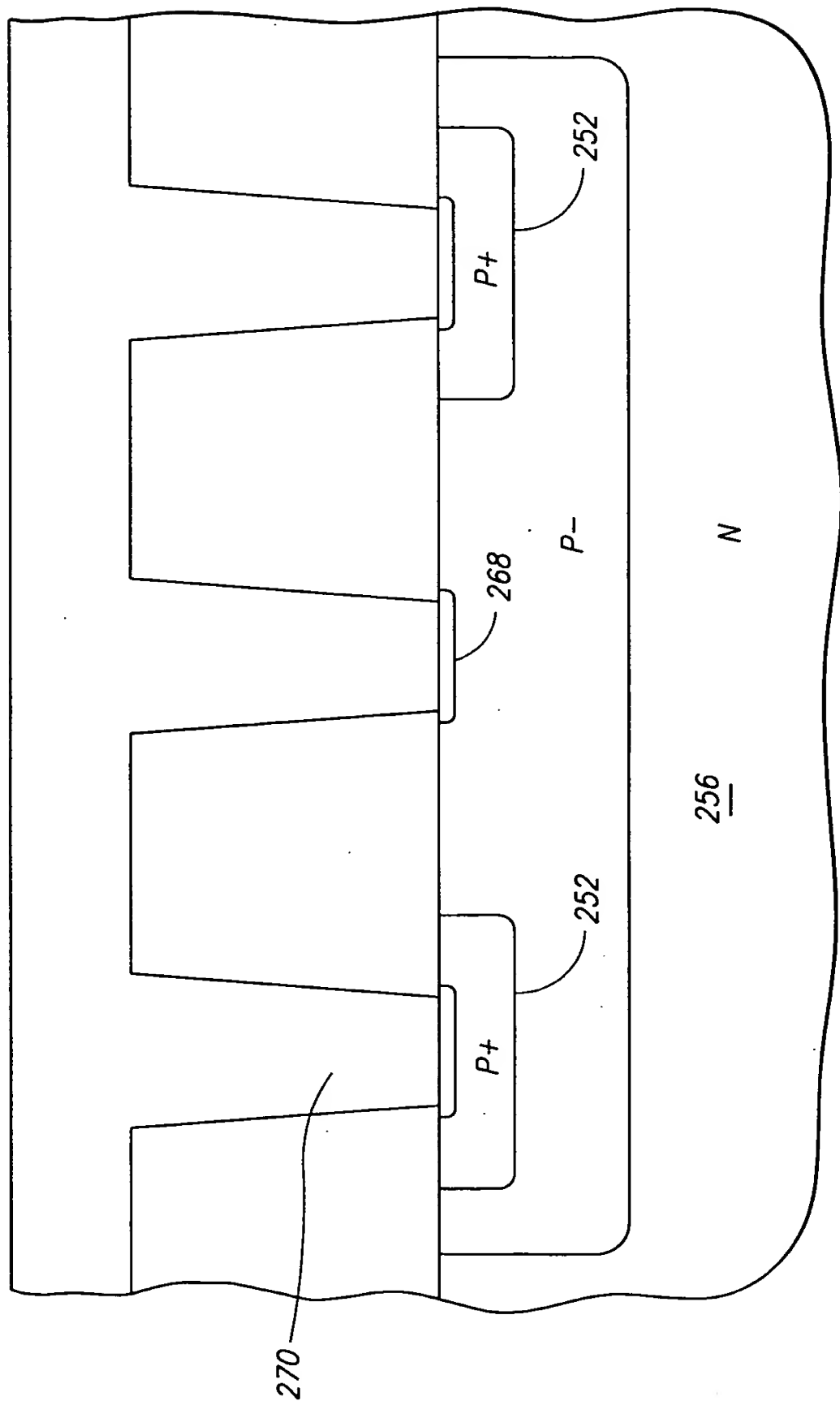
SECRET

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SECRET

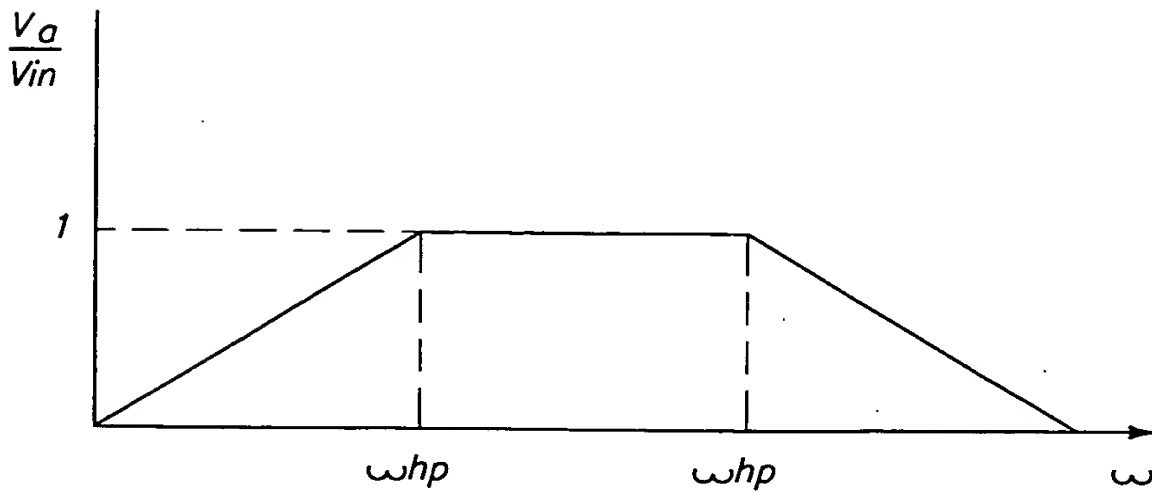
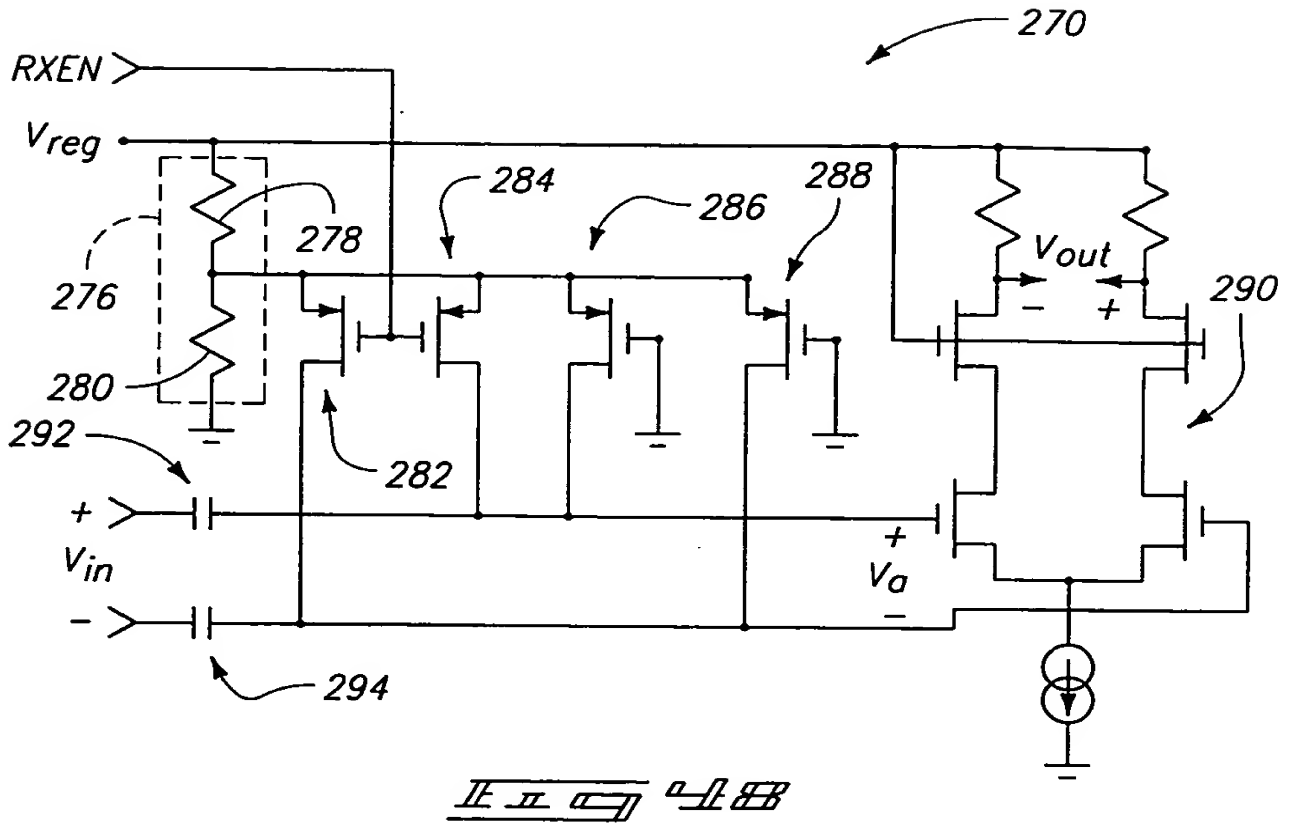




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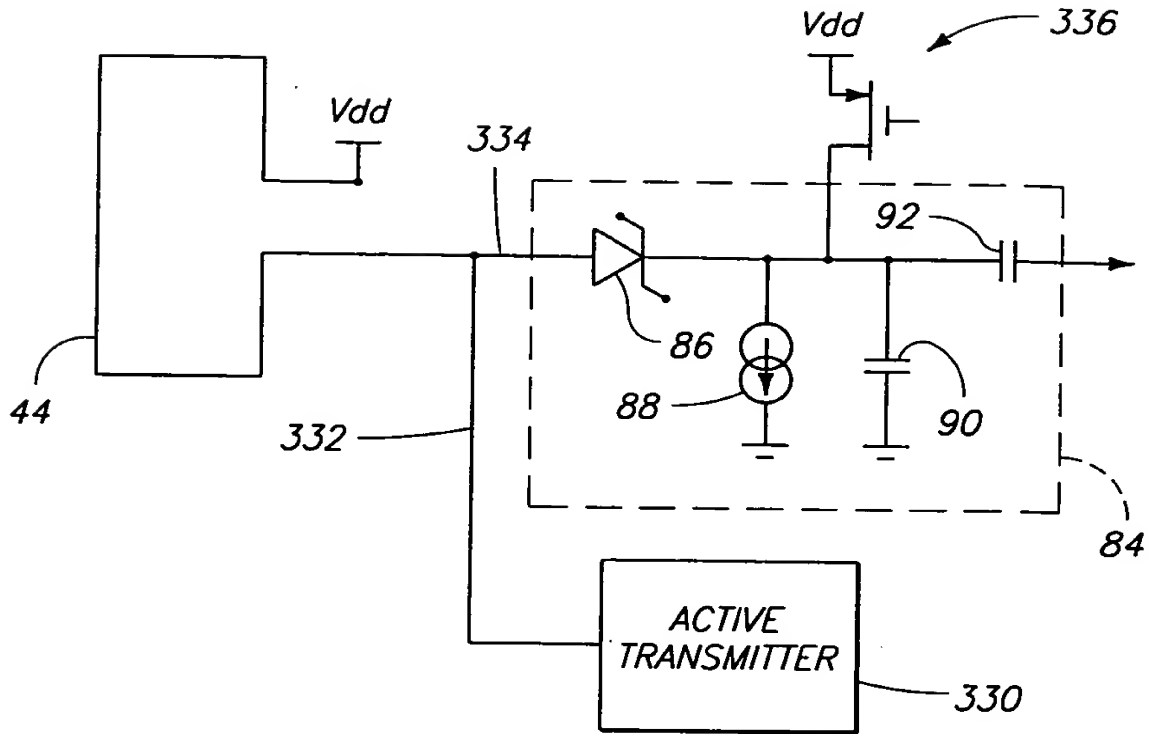


FIG. 50

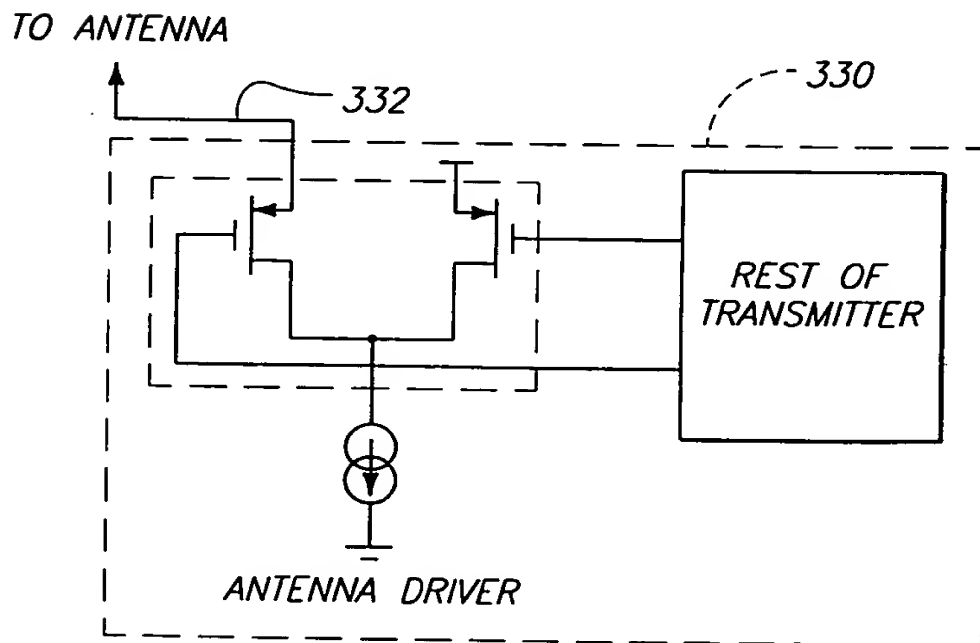
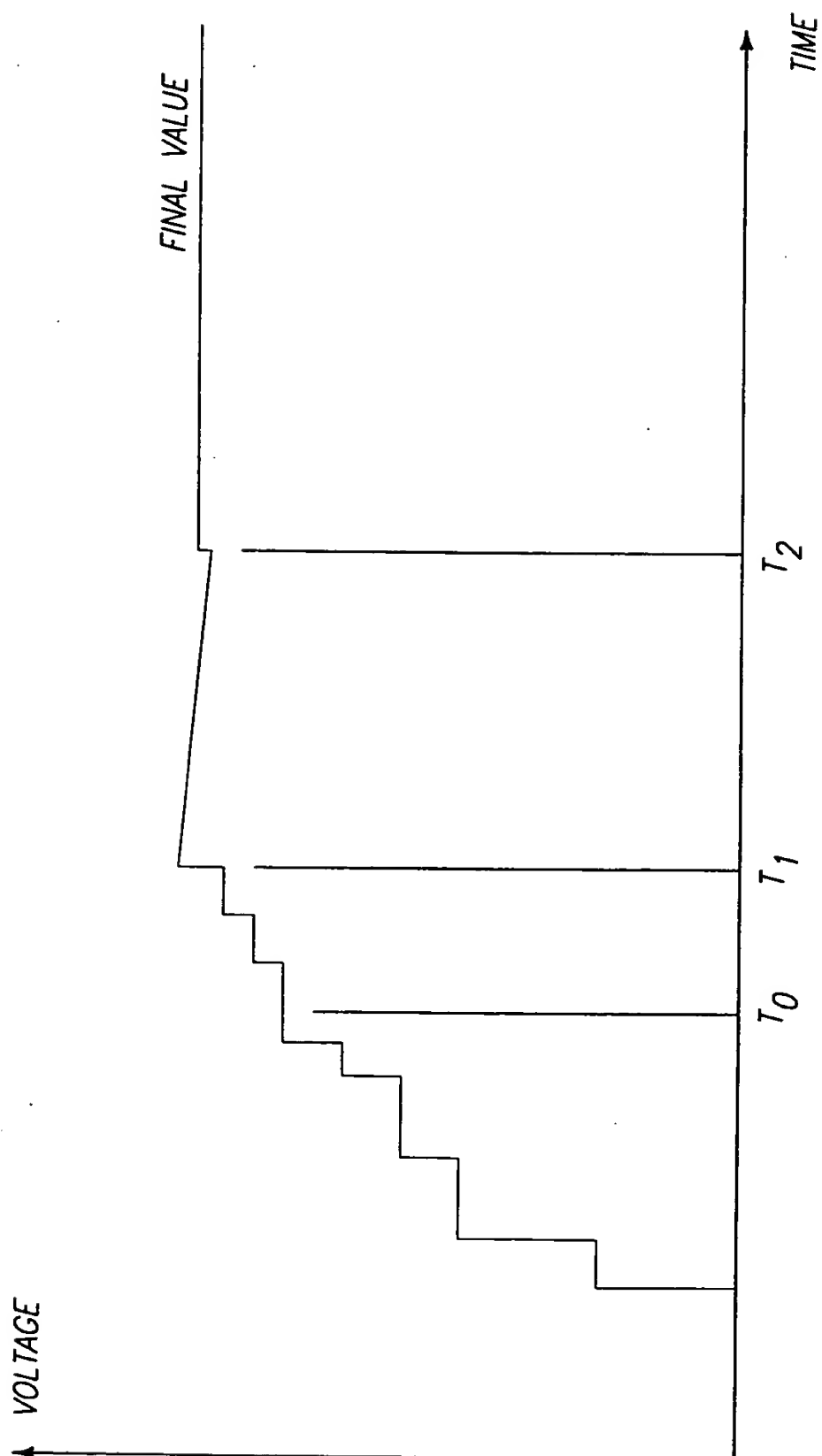


FIG. 51

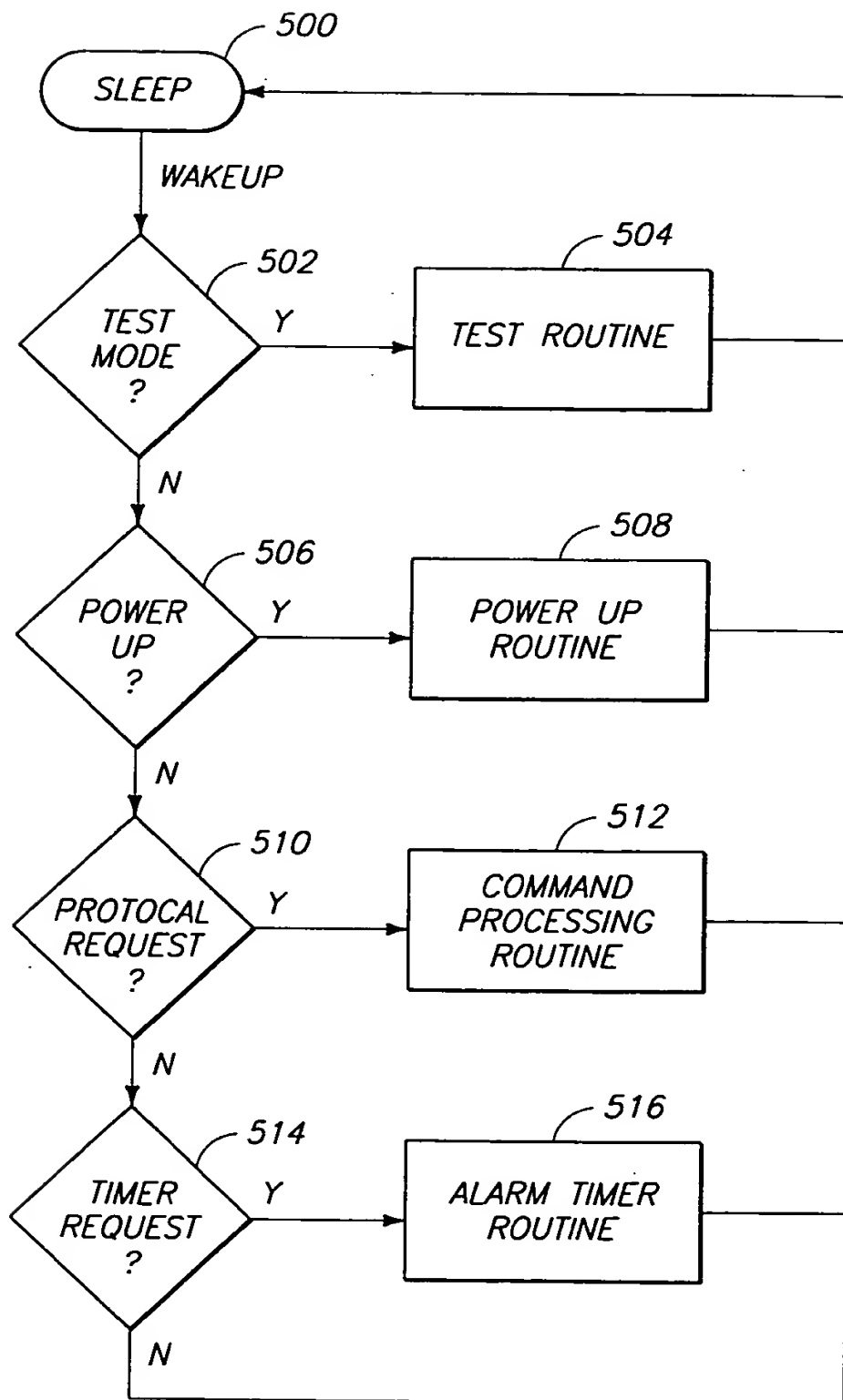
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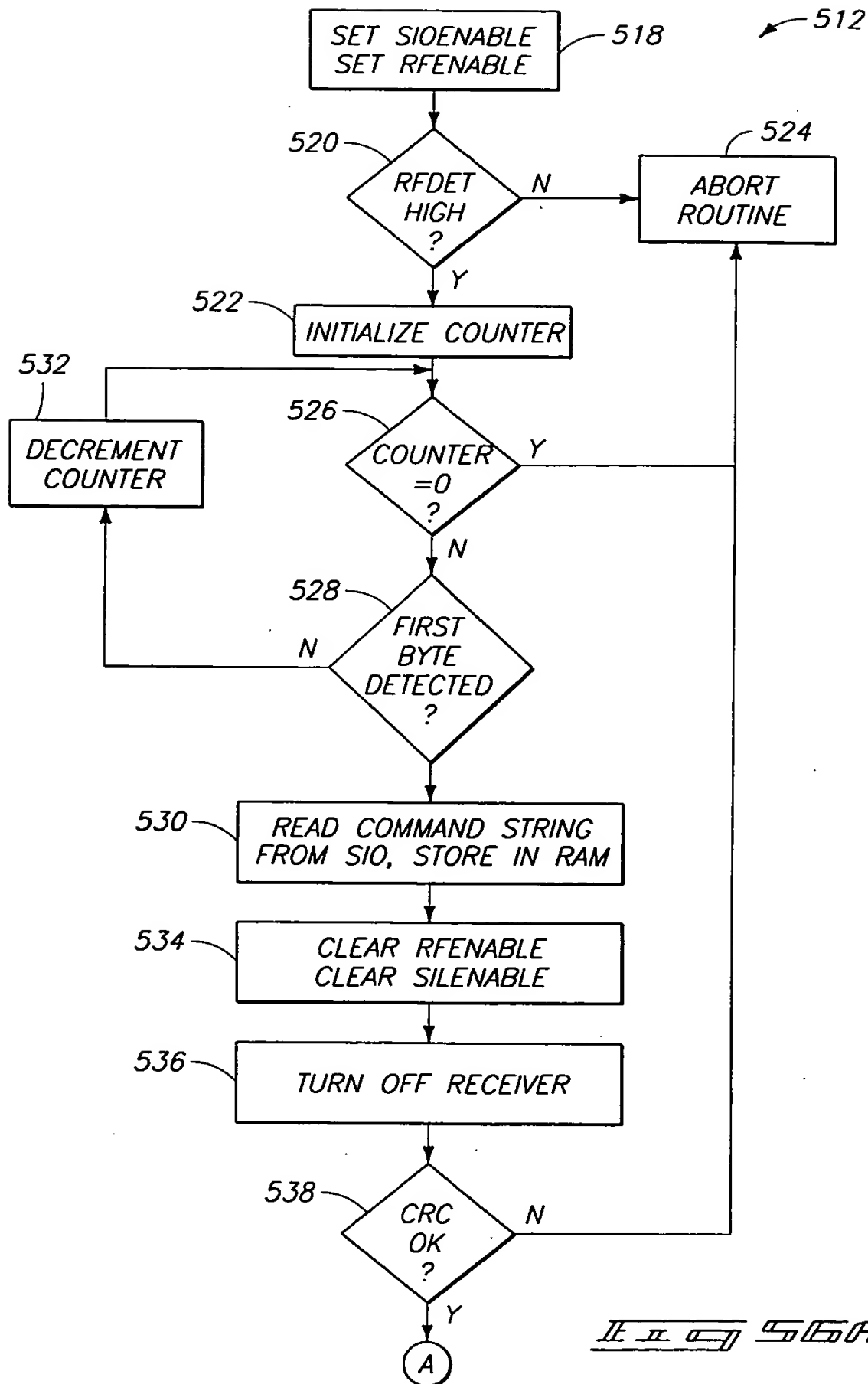


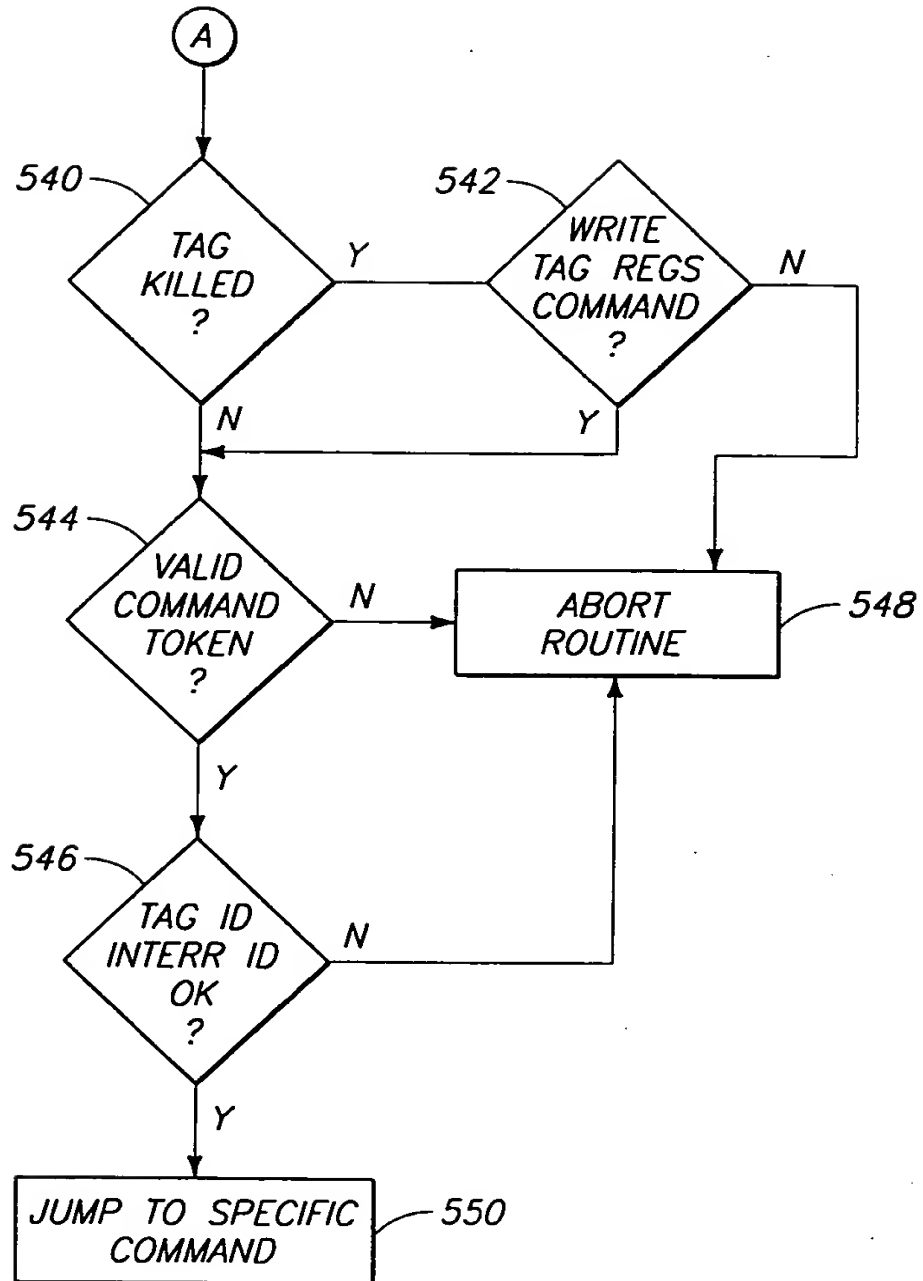


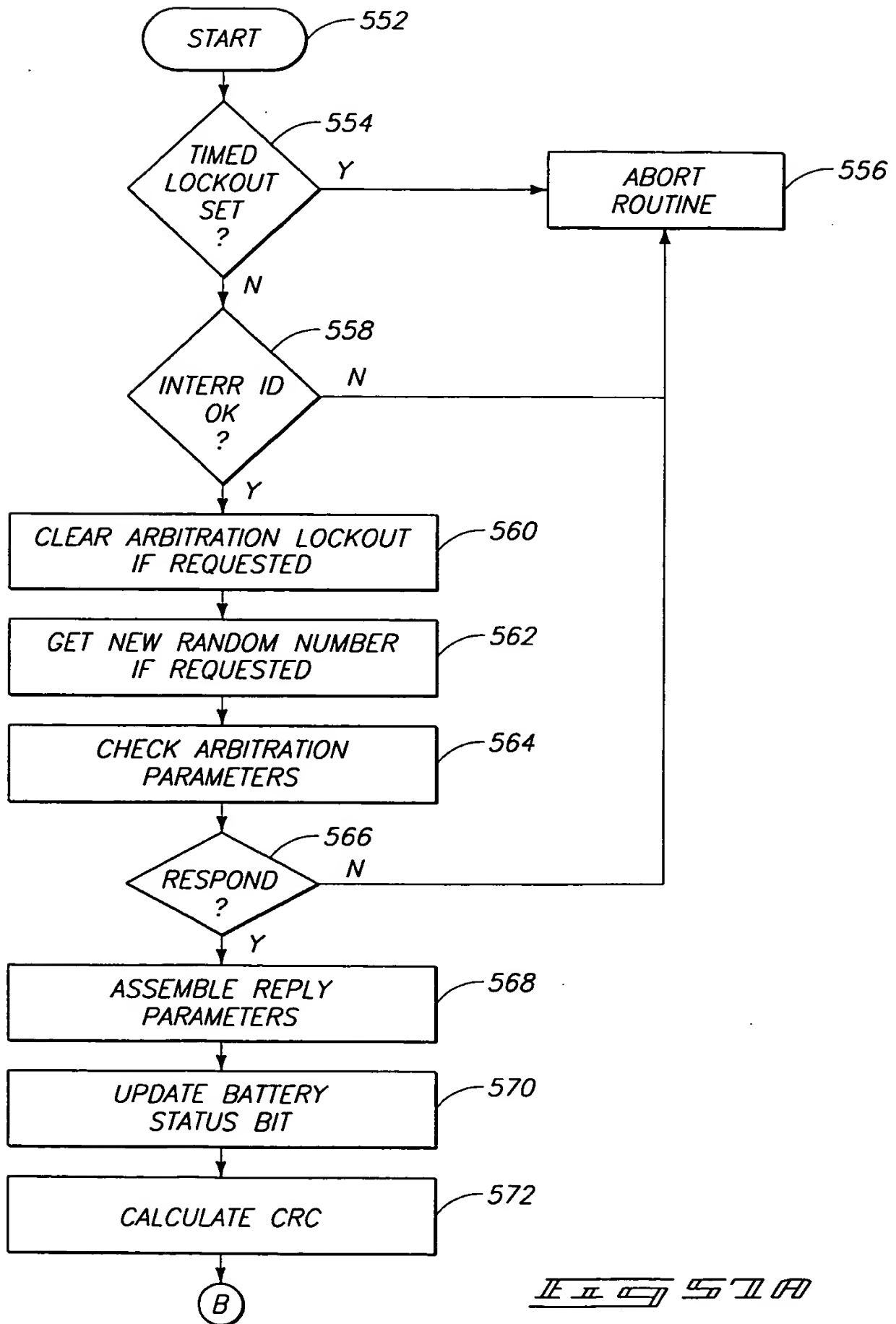
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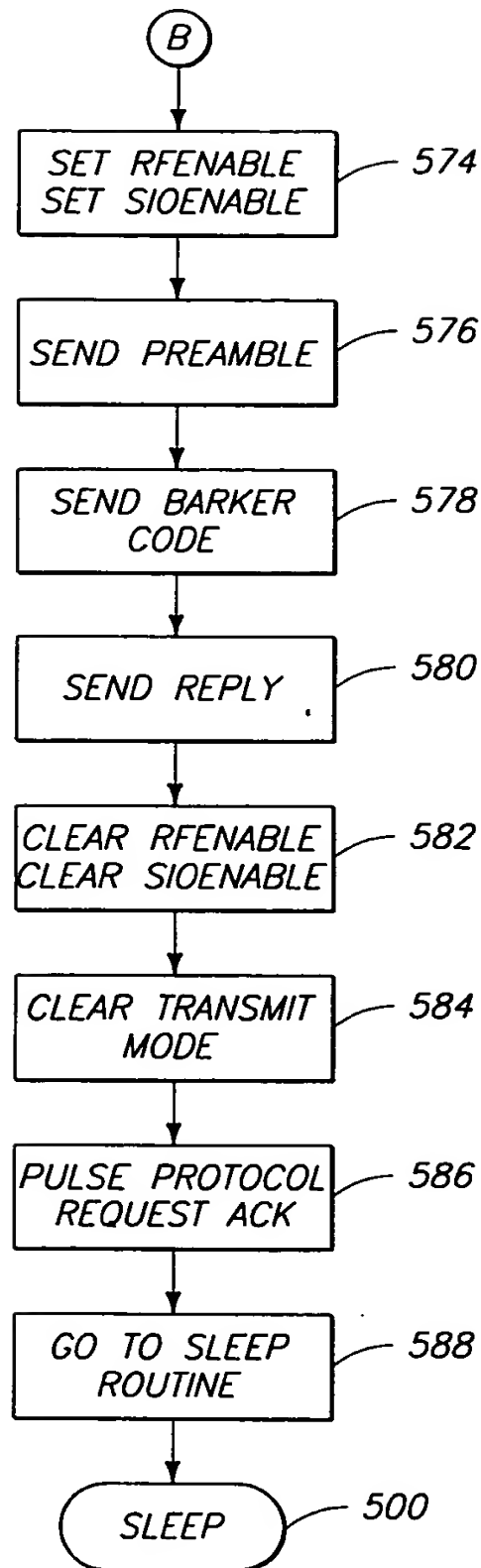


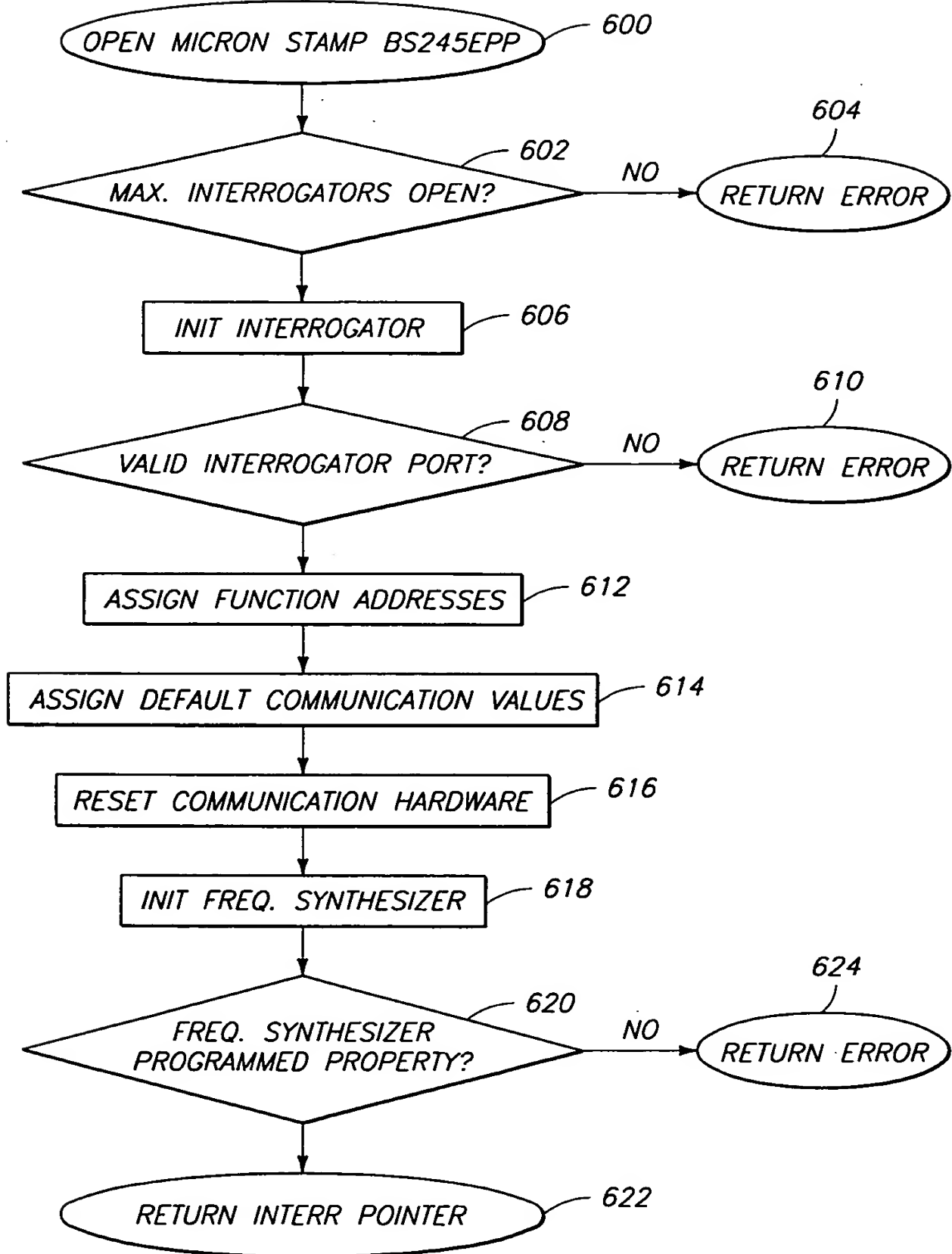


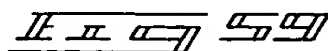


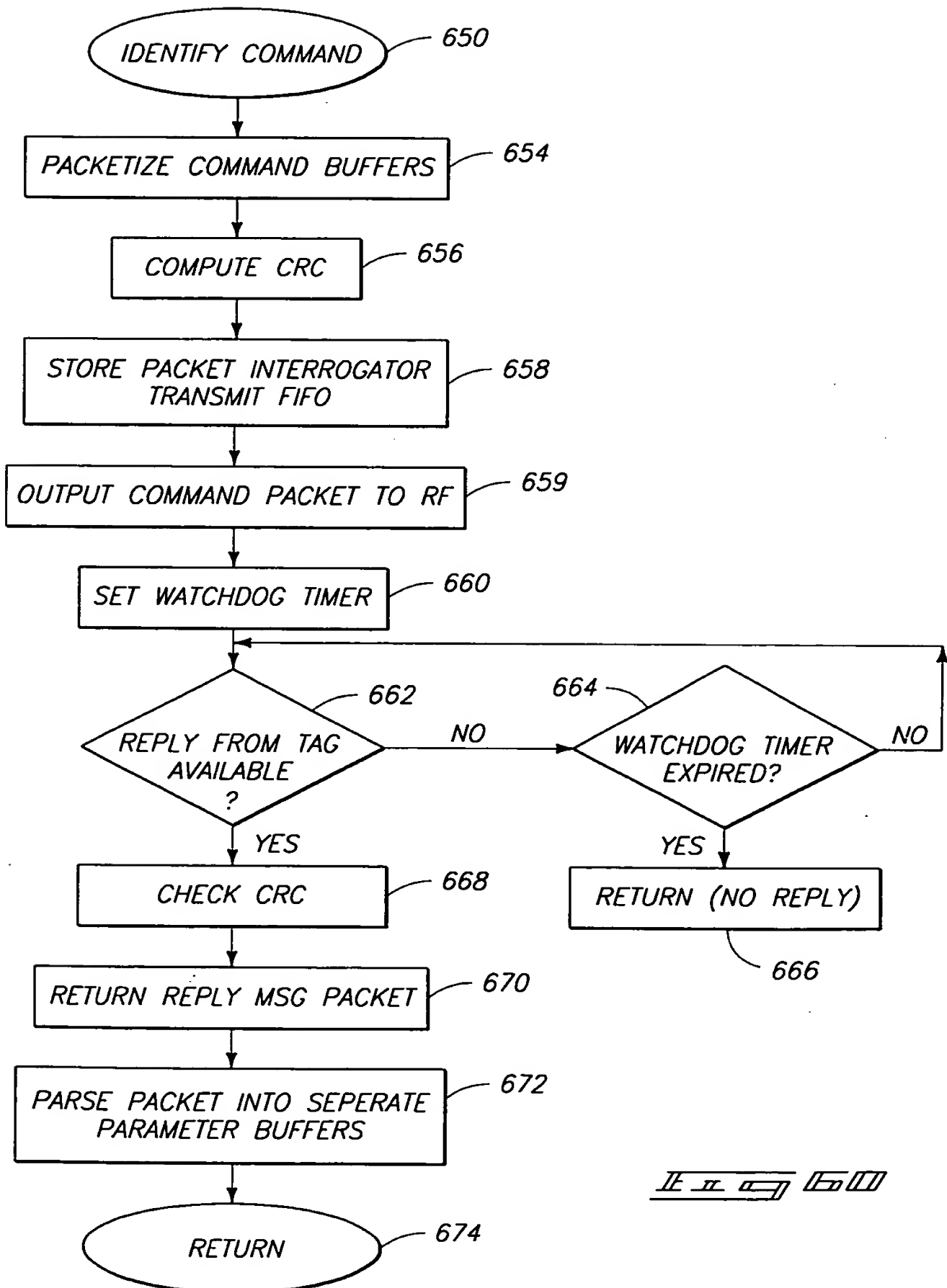


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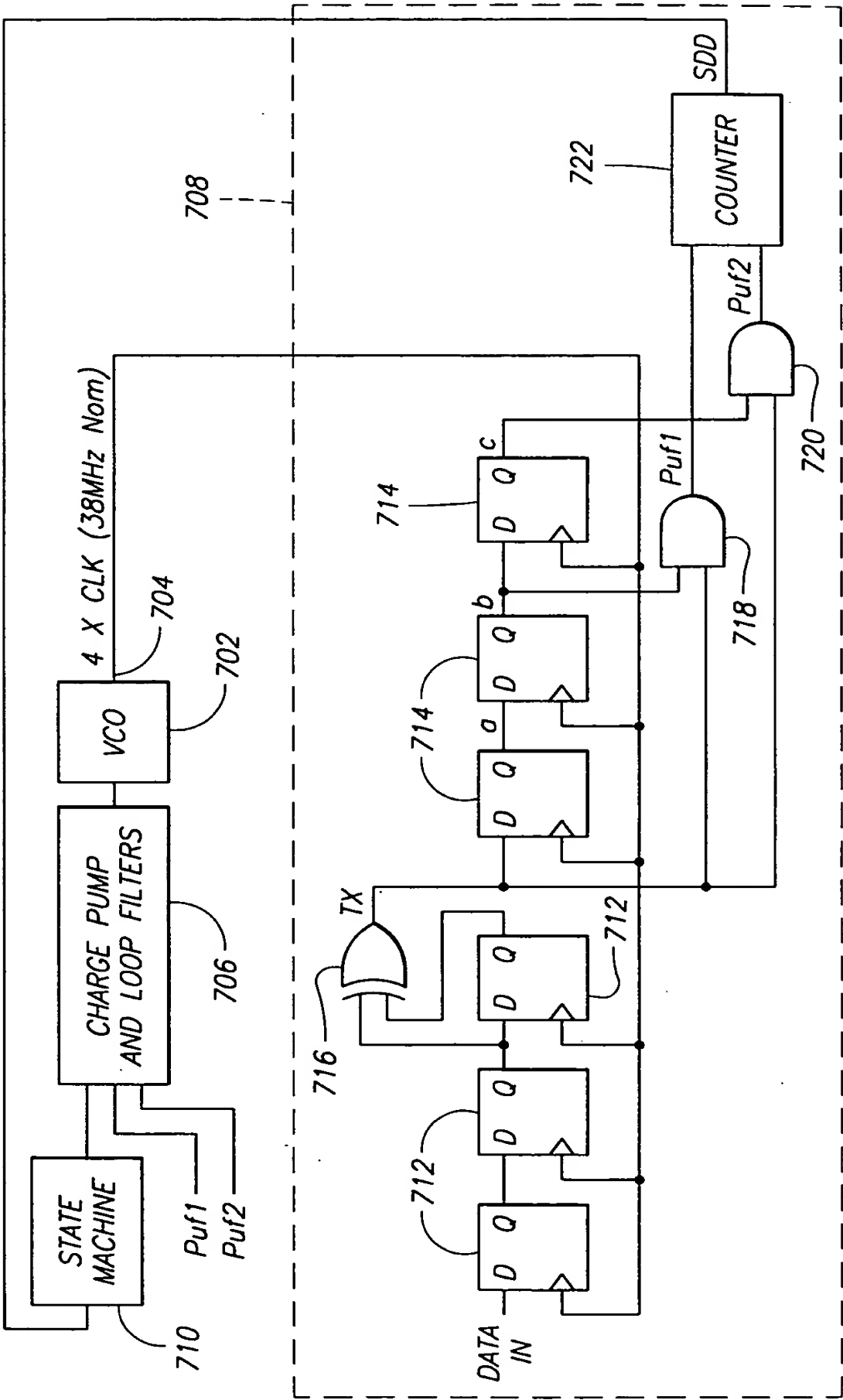




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	PRESENT STATE				NEXT STATE	
	ENABLE	TX	Q1	Q0	D1	D0
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	0	1	0	0	0	0
	1	0	0	0	1	0
	1	1	0	0	0	1
	X	0	0	1	0	0
	X	1	0	1	0	1
	X	X	1	1	0	1
	X	0	1	0	1	1
	X	1	1	0	0	1

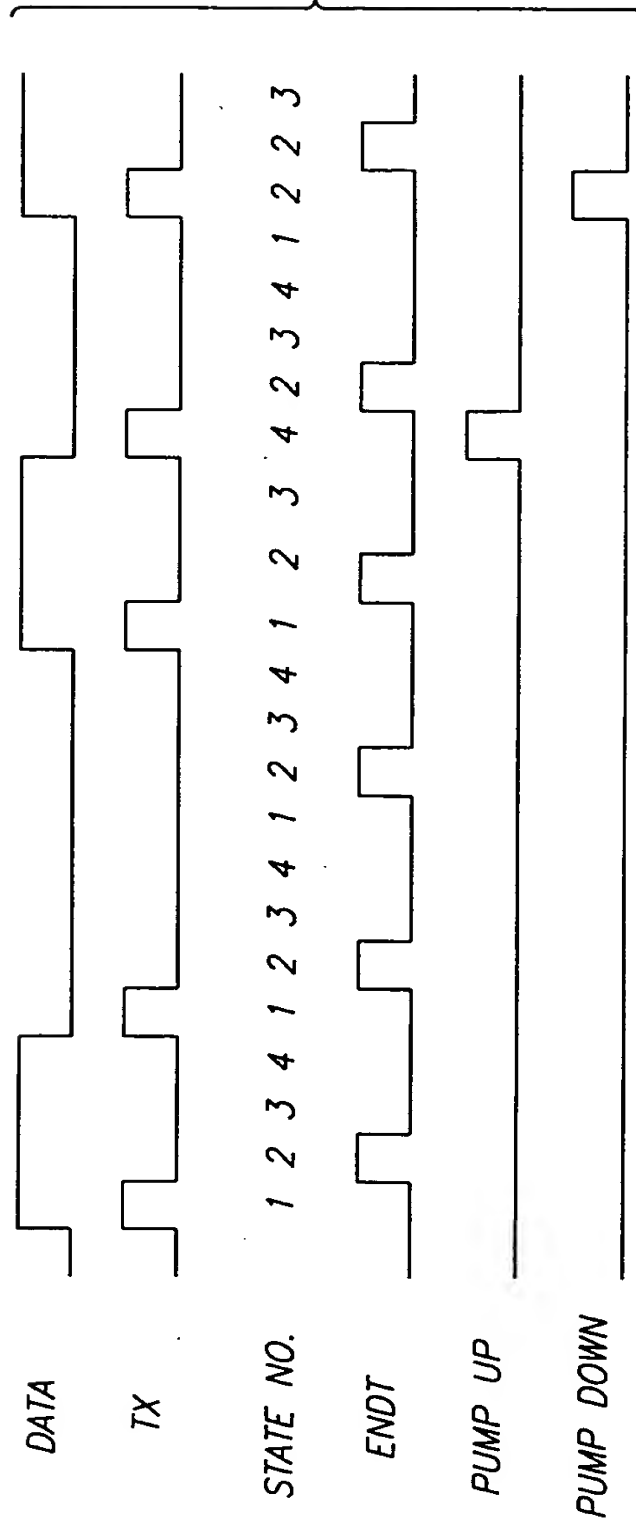
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$$\frac{\pi}{2} \leq \theta \leq \frac{3\pi}{2}$$